DATE November 22, 1960

SUBJECT MODIFIED MULTIPLY SUBROUTINE[1]
FOR PDP-1

TO PDP Disbribution List

FROM Chas. W. Adams Associate:

I.   This is a patch to the multiply subroutine and therefore must be loaded into memory after the multiply subroutine has been loaded. This patch must be used whenever the sine, cosine, arccosine or arcsine subroutine is used. It changes the calling sequence of the subroutine and leaves the right hand portion of the product left-adjusted in the I/O unit without a sign.

II. Calling sequence:

```
LIO   (the address of the multiplier)
JSP       1662
LOC   (the address of the multiplicand)
```

III. Subroutine patch:

```
ORG       001662
DAP       001647
DAP       001665
IDX       001647
LAC       010000
DAC       001660
DIO       Y
JMP       001600
Y
ORG       001600
LIO       Y
ORG       001646
LAC       001660
END
```

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1. Acknowledgment
   The Modified Multiply Subroutine was supplied to DEC by Chas. W. Adams Associates, Inc., Bedford, Mass.