

IDENTIFICATION

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Maintainer: DIAGNOSTIC ENGINEERING
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1.0 ABSTRACT

This diagnostic will functionally test the hardware options on the LA36 terminal,

Up to 48 terminals, including the console device, can be tested at a time. All terminals under test must be interfaced thru a DL11 type interface.

Control of this diagnostic may be thru a switch register, or via interactive console terminal commands,

1.1 Functional Description

The program will first determine if the system has a hardware switch register. If none available a software switch register at loc 176 will be assigned.

The program will then determine what device interfaces are on the system, at the standard DL11-A,B,C,D,E address and the interrupt vector of each interface present. This information will then be stored in a table.

The program will then ask the operator if he/she wants to use console control. If the response is "no" the program will halt and wait for the operator to enter control information thru the switch register.

Note : If no hardware switch register is present control is forced to console control.

If console control is selected a menu of available commands will be printed on the console terminal, and the program will wait for commands via the console.

2.0 REQUIREMENTS

2.1 EQUIPMENT

This diagnostic was written to run on all models of the PDP-11 computer, including the LSI-11. One to forty eight LA36 terminals, connected thru a single line asynchronous interface (DL11-A thru E, DL11-W, DLV11), may be tested. 8K of memory is required.

A hardware switch register is supported, but not required.

2,2 OPTIONS

The following options are tested by this diagnostic:

```

ASCII/APL character set,
Selective Addressing Option,
Auto-Answerback Option,
Top-of-form Option,
Horizontal & Vertical Tab Option,

```

2,3 RELATED PROGRAMS

Although some error checking is done, this diagnostic does not functionally test the basic LA36 terminal, nor the terminals interface.

Therefore the basic LA36 terminal diagnostic, MDEC-11-DZLAC-*, and the DL11-*/DLV-11 interface test should be successfully run prior to this diagnostic. Also any other applicable processor/memory tests.

3,0 LOADING & INITIALIZATION

This diagnostic may be loaded using the standard procedures for paper tape, or via XXDP media. This diagnostic will not run in chained mode because manual intervention is required. The absolute loader area is preserved.

This diagnostic self sizes the system as far as the interfaces, and their interrupt vectors. The only operator modifications to be made are:

1. the location 'TIMER' which is a CPU dependent TIME constant, See listing of DELAYM Routine.

```

TIMER: ,WORD 554 ;SET FOR 11/35 = 11/40
;SET TO 202 IF 11/03
; 251 11/05 = 11/10
; 314 11/15 = 11/20
; 2127 11/45 BIPOLAR = 11/55
; 1237 11/45 = MOS = 11/70
; 755 11/45
DELAYT: ,WORD 0 ;DELAY TIME BUFFER

```

2. any of the preset device address to accommodate a non-standard interface address. (See Sect 7,0 for details)
3. location 'WIDTH' if other than 132 COL PAPER is being used. (See section 4,3). (Common for all terminals)

The initial starting address is 200(8), and all restarts at 1372.

4.0 CONTROL & TEST SELECTION

The diagnostic will ask (via the console) if console control is desired. Answer 'Y' if you want to use interactive commands, otherwise type 'N' for switch register control.

If 'Y' is typed a menu of available commands is printed on the console, and the program will wait for command input. If 'N' is typed the program will print a listing of the line (interface) table, then halt for the operator to set the switches to the desired parameters.

4.1 SWITCH REGISTER CONTROL

When SWITCH REGISTER CONTROL is selected the program will halt. Set the switches to the desired mode, then press continue. The program will check the entry and if a specific test is to be run, or a specific line is to be tested the program will halt again. Enter the desired line/test data in the switches, then press continue.

To change parameters the test must be restarted at loc 1372.

ALL of the switch functions are also available under console control mode, (See sect. 4,2,4,3).

SWITCH REGISTER BIT DEFINITIONS FIRST WORD MODE SELECTION

BIT15	=1 (UP) =0 (DOWN)	HALT ON ERROR CONTINUE AFTER REPORT
BIT14	=1 (UP) =0 (DOWN)	LOOP AFTER ERROR IS DETECTED DON'T LOOP
BIT13	=1 (UP) =0 (DOWN)	INHIBIT ERROR REPORTS PRINT ERROR REPORTS
BIT12	=1 (UP) =0 (DOWN)	PRINT INTERFACE TABLE DON'T PRINT TABLE
BIT11	=1 (UP) =0 (DOWN)	INHIBIT ITERATIONS NORMAL RUN
BIT6	=1 (UP) =0 (DOWN)	RUN ALL TESTS IN SEQUENCE RUN SELECTED TEST ONLY
BIT5	=1 (UP) =0 (DOWN)	RUN ALL AVAILABLE LINES RUN SINGLE LINE ONLY

SECOND WORD LINE AND TEST NO.

BIT15 - BIT8	SELECTED LINE NUMBER (00-57)
BIT7 - BIT0	SELECTED TEST NUMBER (0-5)

4.2 CONSOLE CONTROL

When console control is selected a menu of available commands is typed on the console terminal. The program will wait for commands to be entered thru the keyboard.

Enter one command per line, followed by a carriage return. To terminate command input and start execution type an ALTmode or ESCape.

To regain control once execution has begun type a CTL-C. The program will respond with 'READY'. You can now enter the desired

commands just as in startup.

4.3 COMMANDS

There are two types of commands available, interactive commands, and static commands.

ALL static commands can only be entered while in "Command Mode", that is during startup after "READY" is printed on the console, or after the operator has invoked command mode by typing a CTL-C and the program has printed "READY".

Interactive commands can be entered at any time, and are essentially the same as the switch register bits 15,14,13.

The available Commands are:

- S (STATIC) Single Line Mode. Test a single device. Line 00 is default. Use add command to select the desired line.
- M (STATIC) Multi Line Mode. Test all lines present and not deselected.
- Q (STATIC) Sequence thru all tests, starting with test 0.
- Rn (STATIC) Run test N.
- Dn (STATIC) Deselect or Drop from testing interface line N. (see T command)
- An (STATIC) Add line N. Clear out the error count for line N, and reselect the line for testing. In single line mode sets N as current line to test.
- T (STATIC) Type out a listing of the interface lines present on the system, the vector address at which the device interrupts, and whether or not the line is selected.
- wn (STATIC) Changes location 'width' to N. Used to control output of terminal tests. Enter N as an Octal number 32 to 204. (132 decimal)
- CTL-L (interactive) Loop on error. If an error is detected the program will start looping on the test section which caused the error, and continue to loop until a klear command is issued.
In command mode type an L.

CTL=H (interactive) Halt on error, will cause the program to halt after the error message is printed, (assuming error printouts are enabled).
In command mode type or H.

CTL=K (interactive) Klear = resets both the H and L commands (Don't halt, Don't loop).
In command mode type a C.

CTL=N (interactive) NO Error reports, inhibits the printing of normal error messages,
In command mode type an N.

CTL=P (interactive) Print error reports, ALL report messages will be printed on the console.
In command mode type a P.

CTL=G (interactive) will cause the program PCFLAGS to be displayed on the console for trouble shooting purposes. See listing for bit definitions.

ESC Exit command mode & start execution of the diagnostic test(s).

CTL=C Returns test to command mode,
All operations in progress stop.

4.4 EXAMPLES & HINTS

Test numbers 0 thru 5 may be selected to run individually on all, or any terminal.

ALL tests can be run sequentially on all or any terminal.

Tests can be run sequentially on a terminal, but there is little chance that any terminal will have more than one or two of these options installed. Sequencing all tests will probably result in numerous errors from trying to test non-existent options.

If a line gets more than 16 errors it will be deselected by the program and a message will be printed on the console. If the line is the only one being tested it will automatically be re-selected.

Example 1, the commands to select test #4 to be run on all lines.

```

READY
R04(CR) = Run test 4
M(CH)   = Multi line mode
w120(CR) = Set paper width to 120
$       = ESC = echoed as S

```


Example 2, the commands to run all tests on line no. 06, with Halt on error set.

```

READY
Q(CR)   = Sequence tests
S(CR)   = Single line mode.
A06(CR) = Add line #06
H(CR)   = Halt on error
$       = ESC = echoed as $,

```

EXAMPLE 3. How do I run tests on 10 out of 12 terminals.

First you must be in command mode. If a test is running type CTL=C. The program will respond with "Ready". Now type D nn (cr) to deselect line number nn. Repeat for each additional line to be dropped from the tests. Now select your test other parameters as in Example 1.

EXAMPLE 4. How do I restart a device which has been deselected?

In command mode type Ann (cr) where nn is the line number of the line to be added.

EXAMPLE 5. How do I loop on error.

If the test is running type a CTL=L. When an error is detected the program will start to loop on the test or subtest and continue to loop until a CTL=K or CTL=C command is issued.

If in command mode type L (CR).

EXAMPLE 6. Can I set the 'width' constant different for each terminal?

The constant 'width' is the same for all terminals, although it may be changed any time you are in command mode.

5.0 SCOPE OF TESTS.

This diagnostic will functionally test the following hardware options of the LA36.

1. Secondary character set option APL/ASCII character set selection
2. Selective addressing option
3. Auto-answer back option
4. Top of forms option

5. Horizontal and Vertical tabulation option.

The diagnostic will do cursory testing of the basic interface, and basic LA36 logic. It is assumed that the basic interface, CPU/memory, and LA36 tests have been run successfully.

Due to the nature of the hardware under test most error checking will be by visual inspection of the terminal output by the operator.

Description Of Tests

5.1 Test 0 Secondary Character Set.

This test is an output only test, NO terminal feed back is received.

The test prints on each terminal alternating lines of ASCII character set, and APL character set.

Output of characters per line will consist of char codes 40 thru 176 unless the paper width limit is reached first.

Output format:

```
PRIMARY---(ASCII CHARACTER SET)
SECONDARY--(APL CHARACTER SET)
```

This output is controlled by the "width" of the paper. See W command description.

5.2 Test 1 Selective Addressing Option

This test will alternately send out a select code, followed by it's ASCII Equivalent, for all possible select codes (20 thru 177).

This test will also deslect all terminals and try to output a "this should not print" message, "All terminals should be off"

This test will also try to print "this should not print" messages after transmitting "NUL" select character sequences, and no select codes sequences.

Valid terminal output should be: Select Recognized = /NN(Group Select Code) /NN(Individual Select Code)/

where NN represents the select codes recognized by this terminal, if the group select code and individual select codes are set to the same thing them only one /NN/ should appear,

More than two /nn/ codes printed indicates a logic failure in the decoder section of the option, or possibly interface to terminal line problems.

Any of the "this should not print" message that appear on the terminal indicate a failure of the selection logic,

5.3 Test 2 Auto Answer Back Option

This test is divided into six subtests:

Subtest -1

The first subtest is actually a sizing routine. The terminal should respond to its unique selection code with an answerback when polled. This test has no way of knowing what the answer back is, nor any way of "pre-selecting" its unique selection code. Therefore subtest -1 will try all legal selection codes to see if it can cause an answer back to be transmitted. If one is received the program will store the select code associated with the response in the line table for future testing.

Subtest -2

Will see if any answer back has been received, and check its length, the message should not exceed 20 (10) characters. Subtest 2 will print the ASCII message on the terminal, and an octal representation of the characters (to verify non printables are being transmitted correctly, and as a trouble shooting aid if bad data is being sent out from the switches).

Subtest -3

Will read the answer back ten times to verify reliability of the data, and lines,

Subtest -4

Will try to cause transmission of the answer back in response to a broadcast code,

Single Line Mode = Subtests 5, and 6.

Subtest -5

The test will request the operator to press the "Here-is" key, then check for answerback,

Subtest -6

The test will request the operator to type 'CTL=E', then check for answer back.

The operator must verify that the message echoed back to the terminal is correct, by comparing it to the data switch configuration,

5,4 Test 3 Top Of Forms Option

This test is divided into two subtests, one for multi line mode, the other for single line mode. Operator intervention will be required for the single line test.

1. Multi line mode.

This subtest will assume a standard form of 11 inches being used.

The test will issue a form feed, then print a line of dashes, this FF/dashes is repeated 3 times.

The operator should verify correct operation by checking for a line of dashes at the same place on each page.

2. In single line mode, This test will require the operator to set the forms length switch to the value requested. The test will then do three form feeds at each length setting.

5,5 Test 4 Horizontal Tab Option

This test will adjust it's output to conform to the paper width. Change location "width" to the appropriate value before starting test. (Preset to 132 col.) Note: see w command description.

The test will print a reference line for visual verification. The line will look like this:V.....V.....V.....V.....

Tabs will then be set corresponding to the location of each V. The test will then issue a tab and print an X, tab then X etc until the line is complete. Three lines of X's will be printed. All X's printed should be aligned with the reference line V's.

This will be repeated for various (7) values of tab spacing.

Example of output

```
.....V.....V.....V.....V.....V.....
  X      X      X      X      X
  X      X      X      X      X
  X      X      X      X      X
```

```
.....V.....V.....V.....
  X      X      X
  X      X      X
  X      X      X
```

5.6 Test 5 Vertical Tab Option

This test is divided into two subtests, one for multi line mode, the other for single line mode. The single line mode test requires operator intervention.

Multi line mode subtest

Will set tabs at intervals of 1 line, 2 lines, 3 lines etc. up to 11 lines. The test will then issue a vert. tab then print a line of dashes, then repeat until 1 full page has been done. Three pages of output are run for 1 pass of test.

Single line mode testing involves the operator to set up the forms control to 11 inch forms, and then proceeds with the same subtest as for multi line mode.

6.0 ERROR REPORTING

There will be four basic sources of error messages. First the system sizer, second the command decoder, and third the diagnostic tests, and the I/O drivers.

6.1 Diagnostic Tests

All test error messages will be 2 lines of output. A standard format line, shown below, and a descriptive message telling what went wrong.

Std. Fmt.: #ERROR XXY TEST YY LINE ZZ

where XXX = the error number local to the current test,

YY = the current test number,

ZZ = the line under test.

an example of a descriptive message :

,NO ANSWERBACK MESSAGE RECEIVED

As each error is handled a routine will update an error count for the failing line. If 16 errors are accumulated on a line, that line will be "deselected" and the following message will be displayed.

EXCESSIVE ERRORS ., LINE XX DROPPED.

If the line under test is the only line being tested the program will automatically re-select the line, zero the count, and continue testing after typing the following:

LINE RE-SELECTED FOR TEST.

6.2 I/O Driver

If the IO Driver finds no available line to test a message will be displayed and then control will return to the "ready" state.

NO LINES AVAILABLE FOR TEST,

#377 Multi line driver error.

Error messages tagged as #377 indicate a failure during an I/O driver operation, such as a failure to interrupt on transmit to a terminal with the interrupt enable set.

#376

Same as #377 except a single line driver.

6.3 Command Decoder

Console terminal command errors will be handled by a CMDERR module & will output a line of ??? if the input was invalid.

If a line selection command tries to add (re-select) an invalid or non-existent line a ==LINE INVALID message will be typed.

6.4 System Sizer

If during the sizing operation the sizing routine detects a failure of the interface to interrupt it will be reported.

ex: 'NO INTERRUPT ON TXMIT LINE 27'

7.0 PROCEDURES FOR NON-STANDARD DEVICES.

This diagnostic can be modified for use on devices that have non-standard interface addresses by replacing an unused address in the line table with the address of the interface line to be tested.

The table is preset to the standard DL11-A,B,C,D,E addresses, (775610 = 776170 & 776500 = 776670), and the console address 777560.

No modification need be made because of non-standard interrupt vector addresses. The diagnostic sizes each address for presence on the system, and inserts the interrupt vector data into the table at run time.

NOTE: The table addresses are not in ascending order, rather it has been optimized for relative system size by having the most commonly used addresses at the head of the table, DL11-A,B

and DL11=C,D,E address are merged together.

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```
10                                     00200
11                                     ,TITLE CZLAF0 LA36 TERM TST
(1)                                     ;*COPYRIGHT (C) 1977
(1)                                     ;*DIGITAL EQUIPMENT CORP,
(1)                                     ;*MAYNARD, MASS, 01754
(1)                                     ;*
(1)                                     ;*PROGRAM BY R,SCHAUBER
(1)                                     ;*
(1)                                     ;*THIS PROGRAM WAS ASSEMBLED USING THE PDP-11 MAINDEC SYSMAC
(1)                                     ;*PACKAGE (MAINDEC=11=DZGAC=CJ), JAN 19, 1977.
(1)                                     ;*
(1)                                     $TN=1
(1)                                     $SWR=160000 ;,HALT ON ERROR, LOOP ON TEST1, INHIBIT ERROR IYPOUT
(1)                                     000001
(1)                                     160000
```

```
13                                     00260
14                                     00280 ;***** OPERATING INSTRUCTIONS *****
15                                     00300 ;
16                                     00320 ;-1, THIS TEST ASSUMES THAT THE BASIC INTERFACE
17                                     00340 ; LOGIC TESTS & BASIC LA36 FUNCTIONS TEST
18                                     00360 ; MDEC=11=DZLAC=X HAVE BEEN RUN SUCCESSFULLY.
19                                     00380 ;
20                                     00400 ; TIMING FOR ALL TESTS IS DEPENDENT ON CPU TYPE.
21                                     00420 ; THE TIMER IS SET FOR AN 11/40,,IF THIS IS
22                                     00440 ; NOT THE CASE CHANGE LOCATION "TIMER"
23                                     00460 ; ACCORDING TO THE TABLE SUPPLIED IN THIS LISTING.
24                                     00480 ;
25                                     00500 ;-2. THE DIAGNOSTIC WILL START BY ASKING IF THE OPERATION
26                                     00520 ; WANTS TO USE CONSOLE TERMINAL CONTROL, ANSWER Y OR N.
27                                     00540 ;
28                                     00560 ; IF Y IS ENTERED, A "MENU" OF AVAILABLE COMMANDS IS
29                                     00580 ; PRINTED ON THE TERMINAL, AND THEN THE PROGRAM WAITS
30                                     00600 ; FOR INSTRUCTIONS THRU THE KEYBOARD.
31                                     00620 ;
32                                     00640 ; IF N IS ENTERED, THE PROGRAM WILL PRINT A LISTING OF
33                                     00660 ; INTERFACES BY LINE NUMBER, THEN HALTS. SET THE SWITCHES
34                                     00680 ; TO THE DESIRED MODE AND PRESS CONTINUE. THE PROGRAM WILL
35                                     00700 ; DECODE THE SWITCH REGISTER, AND IF RUNNING A SELECTED
36                                     00720 ; TEST, OR A SELECTED LINE, WILL HALT AGAIN.
37                                     00740 ; ENTER THE DESIRED TEST NO. IN THE LOW ORDER BYTE, AND/OR
38                                     00760 ; THE DESIRED LINE NO. IN THE HIGH ORDER BYTE. PRESS CONTINUE.
39                                     00780 ;
40                                     00800 ; IF NO HARDWARE SWITCH REGISTER IS PRESENT ON THE SYSTEM
41                                     00820 ; THE PROGRAM WILL USE LOCATION 176 AS A SOFTWARE SWITCH
42                                     00840 ; REGISTER. CONTROL WILL DEFAULT TO THE CONSOLE I
43                                     00860 ;
44                                     00880 ;
45                                     00900 ;-3. TO CHANGE MODES TYPE CIL=C.
46                                     00920 ; THE CONSOLE WILL RESPOND WITH READY.,
47                                     00940 ; ENTER YOUR COMMANDS FOLLOWED BY AN ESC.
48                                     00950 ; INTERACTIVE COMMANDS SUCH AS CIL=G MAY BE ENTERED
49                                     00955 ; DURING COMMAND, OR RUN MODES.
50                                     00960 ;
51                                     00980 ;-4. IF MULTI-LINE MODE IS SELECTED, A TABLE
52                                     01000 ; OF INTERFACE LINES WILL BE PRINTED.
53                                     01020 ; ALL LINES PRESENT WILL BE INITIALLY SELECTED.
54                                     01040 ; IF YOU ADD OR DROP LINES USE THE LINE NO.
55                                     01060 ; SPECIFIED IN THE LINE TABLE LIST.
56                                     01080 ;
```

```

58      01100 ;***** SWITCH REGISTER BIT DEFINITIONS *****
59      01180 ;
60      01200 ; BIT15 =1 (UP) HALT ON ERROR
61      01220 ; =0 (DOWN) CONTINUE AFTER REPORT
62      01240 ;
63      01260 ; BIT14 =1 (UP) LOOP AFTER ERROR IS DETECTED
64      01280 ; =0 (DOWN) DON'T LOOP
65      01300 ;
66      01320 ; BIT13 =1 (UP) INHIBIT ERROR REPORTS
67      01340 ; =0 (DOWN) PRINT ERROR REPORTS
68      01360 ;
69      01380 ; BIT12 =1 (UP) PRINT INTERFACE TABLE
70      01400 ; =0 (DOWN) DON'T PRINT TABLE
71      01420 ;
72      01440 ; BIT11 =1 (UP) INHIBIT ITERATIONS
73      01460 ; =0 (DOWN) NORMAL RUN
74      01480 ;
75      01500 ; BIT6 =1 (UP) RUN ALL TESTS IN SEQUENCE
76      01520 ; =0 (DOWN) RUN SELECTED TEST ONLY
77      01540 ;
78      01560 ; BIT5 =1 (UP) RUN ALL AVAILABLE LINES
79      01580 ; =0 (DOWN) RUN SINGLE LINE ONLY
80      01600 ;
81      01620 ;
82      01640 ; SECOND WORD ENTRY VIA SWITCHES
83      01660 ;
84      01680 ; BIT15 = BIT8 SELECTED LINE NUMBER
85      01700 ;
86      01720 ; BIT7 = BIT0 SELECTED TEST NUMBER
87      01740 ;
88      01760 ;
89      01780 ;***** TEST ASSIGNMENTS *****
90      01800 ;
91      01820 ; TEST 0 SECONDARY CHARACTER SET
92      01840 ; TEST 1 SELECTIVE ADDRESSING OPTION
93      01860 ; TEST 2 AUTO ANSWERBACK OPTION
94      01880 ; TEST 3 TOP OF FORMS OPTION
95      01900 ; TEST 4 HORIZONTAL TAB OPTION
96      01920 ; TEST 5 VERTICAL TAB OPTION
    
```

```

98      01960 ;
99      02000 ;
100     02100 ;*****
101     02120 ; LOCAL PROGRAM EQUATES
102     02140 ;
103     000020 ; ABO = BIT4 ;LINE ABORT FLAG
104     000006 ; ACK = 6
105     000004 ; ADDC = BIT2
106     000200 ; ATTN = BIT7
107     000015 ; CR = 15
108     000003 ; CTLC = 3
109     000000 ; CTLCNT = 0
110     000007 ; CTLG = 7
111     000010 ; CTLH = 10
112     000013 ; CTLK = 13
113     000014 ; CTLL = 14
114     000016 ; CTLN = 16
115     000020 ; CTLP = 20
116     004000 ; DATAB = BIT11
117     100000 ; DLP = BIT15 ;LINE PRESENT FLAG
118     000010 ; DROPC = BIT3
119     000005 ; ENQ = 5
120     004000 ; EOL = BIT11
121     020000 ; EOP = BIT13
122     040000 ; EOT = BIT14
123     000033 ; ESC = 33
124     000003 ; ETX = 3
125     000001 ; FLAG1 = BIT0
126     000002 ; FLAG2 = BIT1
127     100000 ; HALTOE = BIT15
128     000200 ; HALTC = BIT7
129     177776 ; ICNT = -2
130     000040 ; INHR = HIT5
131     020000 ; INHRPT = BIT13
132     005726 ; ISP = 5726 ;INC SP 2
133     022626 ; ISP2 = 22626 ;INC SP 4
134     000400 ; LDONE = BIT8
135     000100 ; LOOPC = BIT6
136     040000 ; LOUPOE = BIT14
137     000200 ; PRI4 = 200 ;PRIORITY 4
138     000340 ; PRI7 = 340 ; " 7
139     000000 ; PRI0 = 0 ; " 0
140     000000 ; SEL = BIT7 ;LINE SELECT FLAG
141     000240 ; NOP = 240
142     000240 ; NOOP = 240
143     003000 ; MAJOR = 3000
144     000006 ; POINT = 6
145     000002 ; PASCNT = 2
146     000010 ; RPC = 10
147     000340 ; NREQ = 340
148     000017 ; SI = 17
149     000016 ; SO = 16
150     000001 ; SOH = 1
151     000002 ; STX = 2
152     010000 ; PRINT = BIT12
153     020000 ; TDONE = BIT13
    
```

159	100000	03000	HERR = BIT15
160	002000	03120	NEWTST = BIT10
161	001000	03140	NEWMOD = BIT9
162	000200	03160	READY = BIT7
163	000100	03180	SEQ = BIT6
164	000040	03200	MULTI = BIT5
165	000020	03220	SWCTL = BIT4
166	000020	03240	PRINT = BIT4
167	100377	03260	HERRN = 100377
168	000004	03280	MFLAGS = 4
169	177564	03281	TPS = 177564
170	177566	03282	TPB = 177566
171	177560	03283	TKS = 177560
172	177562	03284	TKB = 177562
173	000000	03285	TKV = 00
174	104000	03300	;***** EMT CALL EQUATES
175	104002	03320	TYPE = EMT
176	104002	03340	PRITBL = EMT+2
177	104004	03360	ITRAP = EMT+4
178	104006	03380	DELAYR = EMT+6
179		03460	

181	000000	03500	,=0	;TRAP CATCHER
182	000000	03520	,+2	
183	000002	03540	HALT	
184	000004	03560	MACHER; ,+2	
185	000006	03580	HALT	
186	000010	03600	,+2	
187	000012	03620	HALT	
188	000014	03640	INTAP	;BREAKPOINT TRAP
189	000016	03660	PR14	;USED DURING SYSTEM SIZER
190	000020	03680	IXTRAP	;IOT TRAP
191	000022	03700	PR14	;USED BY TXMIT I/O DRIVER
192	000024	03720	RESTR	;POWER FAIL TRAPS TO RESTART
193	000026	03740	PR10	
194	000030	03760	EMTBOS	
195	000032	03780	PR10	
206	000172	04000	,=172	
207	000174	04020	SWTEST; ,WORD 0	
208	000176	04040	SWLINE; ,WORD 0	
209	000178	04060	SSWR; ,WORD 0	
210	000200	04080	JMP	START
211		04100		
212			,SBTTL	ACT11 HOOKS
(1)				
(2)				
(1)			;*****	
(1)	000204		;HOOKS REQUIRED BY ACT11	
(1)	00046		SSVPC=,	;SAVE PC
(1)	00052		,=46	
(1)	00052		START	;1)SET LOC,46 TO ADDRESS OF START
(1)	00052		,=52	
(1)	00052	02000	,WORD 20000	;2)SET LOC,52 TO 20000
(1)	000204		,=SSVPC	; RESTORE PC
213	001100	04140	,=1100	
214	001100	04160	NOP	

```

216          ,SBTTL TEST CONTROL & INITIALIZATION
221          04300 ;*****
222          04320 ;
223 001102    04340 START; ;***** TEST MONITOR *****
225 001102    000005 04380 RESET ;*****
226          04400 ;
227          ; PROGRAM INITIALIZATION SECTION
228          04440 ;
229 001104    012706 001100      MOV    #STACK,SP
230 001110    005037 010152      CLR    NEXT
231 001114    005037 010154      CLR    INTST
232 001120    005037 010162      CLR    NXTLIN
233 001124    005037 010160      CLR    ONLIN
234 001130    012737 010114      MOV    #INBUF,PTR
235 001136    012705 010352      MOV    #T00BLK,RS
236          04500 ;
237          04600 ; SEE IF SYSTEM HAS A SWITCH REGISTER
238          04620 ;
239 001142    004737 003006      JSR    PC,SWRTST
240          04660 ;
241          04680 ; PRINT TEST IDENTIFICATION MESSAGE
242          04700 ;
243 001146    012700 016772      MOV    #PROGID,R0
244 001152    104000              TYPE
245          04740 ;
246          04760 ; DETERMIN SYSTEM CONFIGURATION
247          04780 ; BUILD A TABLE OF INTERFACE LINES.
248          04800 ;
249          04820 ;
250 001154    004737 005640      JSR    PC,BUILD
251          04860 ;
252          04880 ; RESTORE TRAP CATCHER FROM 100 TO 1000
253          04900 ;
254 001160    004737 000126      JSR    PC,CATCH
255 001164    104002              PRTTBL
256          04940 ;
257          04960 ; FIND OUT IF OPERATOR WANTS TO USE
258          04980 ; CONSOLE OR SWITCHS FOR CONTROL
259          05000 ;
260 001166    004737 003350      JSR    PC,GETSRC
261 001172    004737 003332      START2: JSR   PC,CONSON
262 001176    032737 000020      BIT    #5*CTL,PCFLAG
263 (9) 001204    001011          BNE    500015
264          05060 ;
265          05080 ; PRINT A MENUE OF AVAILABLE COMMANDS
266          05100 ;
267 001206    012700 017051      MOV    #L3,R0
268 001212    104000              TYPE
269 001214    012700 017056      MOV    #HEADR1,R0
270 001220    104000              TYPE
271 001222    012700 017103      MOV    #CONSUM,R0
272 001226    104000              TYPE
273 001230    004737 003332      500015: JSR   PC,CONSON
274 001234          500020:
275 001234    032737 000020      BIT    #5*CTL,PCFLAG
    
```

```

(9) 001242    001403          BEQ    500045
276          05340 ; IF IN SWITCH CONTROL GET CONTENTS OF SW REG.
277 001244    004737 003130      JSR    PC,GETSWS
278 001250    000421          BR     500055
279 001252          500045:
280          05420 ;
281          05440 ; IN CONSOLE CONTROL SIGNIFY READY
282          05460 ; AND READ COMMANDS FROM THE CONSOLE.
283          05480 ;
284 001252    012700 020123      MOV    #RDY,R0
285 001256    104000              TYPE
286 001260          500065:
287 001262    000001          WAIT
288 (5) 001270    001001          BIT    #ATIN,PCFLAG
289 001272    000772          BNE    500075
290 001274          BR     500065
291          05620 ;
292          05640 ; PRINT THE LINE TABLE IF REQUESTED.
293          05660 ;
294 001274    032737 010000 001364      BIT    #PRINTI,PCFLAG
295 001302    001404          BEQ    500105
296 001304    104002          PRTTBL
297 001306    042737 010000 001364      BIC    #PRINTI,PCFLAG
298 001314          500105:
299 001314          500055:
300          05780 ;
301          05800 ; SET UP THE I/O DRIVER AREAS
302          05820 ; SET UP & EXECUTE REQUESTED TESTS.
303          05840 ;
304 001314    004737 002110      JSR    PC,LINMON
305 001320    012700 020411      MOV    #ALLON,R0 ;ISSUE A SELECT ALL COMMAND
306 001324    004737 007010      JSR    PC,MTYPE ;IN CASE THERE ARE SELECTIVE
307          05872 ; ;TERMINALS ON LINE.
308          05874 ;
309 001330    004737 001446      JSR    PC,TSTCTL
310 001334    032737 004000 002032      BIT    #EOL,CFLAGS
311 (5) 001342    001406          BEQ    500035
312 001344    042737 004000 002032      BIC    #EOL,CFLAGS
313 001352    004737 001372      JSR    PC,RESTART
314 001356    000726          BR     500025
315 (3) 001360          500035:
316 001360    000137 001172          JMP START2
317 001364          500005:
318          06020 ;
319          06040 ; *****
320          06060 ;
321          06080 ; * NOTE,..TYPING CTL=G WHILE IN CONSOLE *
322          06100 ; * CONTROL MODE WILL CAUSE THE *
323          06120 ; * PCFLAG WORD TO BE PRINTED, *
324          06140 ; *
325          06160 ; *****
326 001364    06180 CTLBLK ;PROGRAM CONTROL BLOCK
327 001366    06200 PCFLAG;WORD 1 ;PROGRAM CONTROL FLAGS
328 001368    06220 TESTNO;WORD 0 ;TESTNO
329 001370    06240 LINENO;WORD 0 ;LINE#
330          06260
    
```

```

325 ;*****
326 ; PCFLAG BIT DEFINITIONS *
327 ;*****
328 ;
329 ; BIT 15 HALTOE HALT ON ERROR (SW=15)
330 ; BIT 14 LOOPOE LOOP ON ERROR (SW=14)
331 ; BIT 13 INHRPT INHIBIT REPORTS (SW=13)
332 ; BIT 12 PRINTT PRINT TABLE (SW=12)
333 ; BIT 11 DATAIN DATA IN FROM KBD,
334 ; BIT 10 NEWTST CHANGE IN TEST NO,
335 ; BIT 9 NEWMOD CHANGE IN MODE,
336 ; BIT 8 LDONE END OF LINE TABLE REACHED
337 ; BIT 7 ATTN ATTENTION !!!!!!!!
338 ; BIT 6 SEQ SEQUENCE TESTS MODE
339 ; BIT 5 MULTI MULTI LINE MODE,
340 ; BIT 4 SWCTL CONTROL VIA SWITCHES,
341 ; BIT 3 DROPC DROP LINE COMMAND
342 ; BIT 2 ADDE ADD LINE COMMAND
343 ; BIT 1 FLAG2 MODE 0 = NO CURRENT I/O TO CONSOLE
344 ; BIT 0 FLAG1 1 = IN COMMAND INPUT MODE
345 ; 2 = I/O TESTING OF CONSOLE
346 ; 3 = ?
347 ;
348 ;
349 ;
350 ;*****
351 ; RESTART
352 ;*****
353 ;
354 001372 012706 001100 06380 RESTRT: MOV #STACK,SP ;REINITIALIZE EVERYTING
355 001376 005037 010152 06400 CLR NEXT
356 001402 005037 010154 06420 CLR INTST
357 001406 012737 000001 001364 06440 MOV #1,PCFLAG
358 001414 005037 010162 06460 CLR NXTLIN
359 001420 005037 010160 06480 CLR ONLIN
360 001424 012737 010114 010164 06500 MOV #INBUF,PTR
361 001432 004737 000120 06520 JSR PC,CATCH
362 001436 000240 06540 NOP
363 001440 000005 06560 RESET
364 001442 000137 001230 06580 JMP START;
  
```

```

369 ;*****
370 ; TSTCTL THIS SECTION CONTROLS TEST SELECTION, TEST
371 ; SEQUENCING, AND INTERFACES TO ERROR AND REPORT
372 ; MODULES AS REQUIRED BY THE TEST MODULES.
373 ;*****
374 ;
375 001446 013737 001366 010152 06800 TSTCTL: MOV TESTNO,NEXT ;GET TEST NO.
376 001454 013737 010152 010154 06820 LOOP1: MOV NEXT,INTST ;GET CURRENT TEST NO.
377 001462 004737 002056 06840 JSR PC,SUTEST
378 001466 004777 014464 06920 LOOP2: JSR PC,@TESTAD ;START TEST
379 ;
380 ;
381 ; CHECK FOR ERROR FLAG FROM TEST
382 ;
383 001472 032737 000020 001364 ; BIT #SWCTL,PCFLAG
384 001500 001414 SEQ 500115
385 001502 017737 014416 010100 MOV #SWR,TEMP
386 001510 042737 003777 010100 BIC #377,TEMP
387 001516 042737 174000 001364 BIC #174000,PCFLAG
388 001524 053737 010100 001364 BIC TEMP,PCFLAG
389 ;
390 001532 032765 100000 000004 06953 500118:
391 001540 001414 ; BIT #MERR,MFLAGS(R5)
392 001542 016537 000004 002032 SEQ 500126
393 001550 016537 000006 002034 MOV MFLAGS(R5),CFLAGS
394 ; MOV POINT(R5),TSCPTR
395 ;
396 001556 004737 005124 07002 ;
397 001562 042765 100377 000004 07004 ; CALL ERROR HANDLER ROUTINE
398 001570 000421 07006 ;
399 ; JSR PC,ERROR
400 ; BIC #MERR,MFLAGS(R5)
401 ; BR 500135
402 001572 032765 020000 000004 500128:
403 001600 001415 ; BIT #TDONE,MFLAGS(R5)
404 ; BEQ 500148
405 ;
406 001602 005265 000002 ;
407 001606 042765 020000 000004 ; UPDATE THE PASS COUNT THEN REPORT END OF PASS
408 001614 052737 020000 002032 ;
409 001622 016537 000002 002036 ; INC PASCNT(R5)
410 001630 004737 005436 ; BIC #TDONE,MFLAGS(R5)
411 001634 ; BIC #EOP,CFLAGS
412 ; MOV PASCNT(R5),TSCCNT
413 ; JSR PC,REPORT
414 ;
415 ; IF LOOP ON ERROR IS SET AND AN ERROR IS
416 ; DETECTED THE ERROR HANDLER WILL MAKE THE
417 ; RETURN ADDRESS OF THE TEST ODD
418 ;
419 ; CHECK FOR ODD ADDRESS,...IN LOOP MODE...
420 001634 032765 000001 000010 ; BIT #BIT0,RPC(R5)
  
```

```

(9) 001642 001413          BEQ      500158
421                                ;
422                                ; IF THE LOOP ON ERROR IS TURNED OFF THEN
423                                ; CONTINUE TEST AT THE NEXT SUBTEST,
424                                ;
425 001644 032737 040000 001364          BIT      #LOOPOE,PCFLAG
(9) 001652 001006          BNE      500168
426 001654 042765 000001 000010          BIC      #BIT0,RPC(R5)
427 001662 016537 000010 016156          MOV      RPC(R5),TESTAD
428 001670          500165: BR      500178
429 001672          500158:
(3) 001672          07342:
430                                ;
431                                ; CHECK TO SEE IF THE ITERATION COUNT IS COMPLETED
432                                ;
433 001672 026565 000002 000000          CMP      PASCNT(R5),CTLCNT(R5)
(9) 001700 003447          BLE      500208
434 001702 052737 040000 002032          BIS      #EOT,CFLAGS
435 001710 005037 002036          CLR      TSCCNT
436                                ;
437                                ; REPORT END OF TEST CONDITION
438                                ;
439 001714 004737 005436          JSR      PC,REPORT
440 001720 016565 000002 000000          MOV      PASCNT(R5),CTLCNT(R5)
(6) 001726 066565 177776 000000          ADD      ICNT(R5),CTLCNT(R5)
441                                ;
442                                ; IF IN SEQUENCE TESTS MODE SET UP NEXT TEST
443                                ;
444 001734 032737 000100 001364          BIT      #SEQ,PCFLAG
(9) 001742 001423          BEQ      500218
445 001744 013737 016152 016154          MOV      NEXT,INTEST
446 001752 005237 016152          INC      NEXT
447                                ;
448                                ; IF NEXT IS A NON EXISTANT TEST SET EOL
449                                ; AND RETURN TO MONITOR FOR NEW COMMANDS
450                                ;
451                                ;
452 001756 023727 016154 000005          CMP      INTEST,#5
(9) 001764 003407          BLE      500228
453 001766 005037 016152          CLR      NEXT
454 001772 052737 004000 002032          BIS      #EOL,CFLAGS
455 002000 000207          07600: RTS      PC
456 002002 000402          500228: BR      500238
(3) 002004          500238: JSR      PC,SUATEST
457 002004 004737 002056          500236: BR      500246
458 002010          500218:
459 002010 000402          500218:
(3) 002012          07602:
460                                ;
461                                ; SET UP TEST ADDRESS FOR THE SAME TEST AGAIN,
462                                ;
463 002012 004737 002056          JSR      PC,SUATEST
464 002016          500248:
465 002016 000403          500208: BR      500258
(3) 002020          07742:
466                                ;
    
```

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467                                ; RETURN TO TEST VIA ADDRESS SUPPLIED BY TEST
468                                ;
469 002020 016537 000010 016156          MOV      RPC(R5),TESTAD
470 002026          500258:
471 002026          500178:
472 002026 000137 001466          07820: JMP      LOOP2
473                                ;
474                                ;*****
475 002032 000000          08200: CFLAGS: ,WORD 0 ;FLAGS
476 002034 000000          08220: TSCPTM: ,WORD 0 ;POUNTER
477 002036 000000          08240: TSCCNT: ,WORD 0 ;PASCNT
478                                ;
479                                ;
480 002040 010164          08320: TSTBL: TEST0 ; TABLE OF TEST ADDRESSES *****
481 002042 010444          08340: TEST1
482 002044 011526          08360: TEST2
483 002046 013700          08380: TEST3
484 002050 014652          08400: TEST4
485 002052 015354          08420: TEST5
486 002054 177777          08440: =1
487                                ;
488                                ;
489                                ;
490                                ;
491                                ;*****
492 00446: ;SUATEST INITIALIZES THE TEST ADDRESS POUNTER
493                                ; FOR TEST # IN 'INTEST'
494 00448: ;*****
495                                ;
496 002056          SUATEST:
497 002056 006337 016154          ASL      INTEST
498 002062 012700 002040          MOV      #TSTBL,R0
(6) 002066 063700 016154          ADD      INTEST,R0
499 002072 011037 016156          MOV      (R0),TESTAD
500 002076 006237 016154          ASR      INTEST
501 002102 005065 000004          CLR      MFLAGS(R5)
502 002106          500005:
(3) 002106          500018:
(2) 002106 000207          RTS      PC
503                                ;
504                                ;
505                                ;
    
```

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507          08500          ,SBTTL LINE CONTROL & INITIALIZATION
508          08520          ;*****
509          08540          ;THIS SECTION CONTROLS THE SELECTION AND SEQUENCING
510          08560          ;OF SINGLE OR MULTIPLE LINES FOR TESTING,
511          08580          ;*****
512          08600
513          08620
514          002110          LINMON:
515          002110 032737 001000 001364          BIT      #NEWMOD,PCFLAG
516          002116 001427          BEQ      500028
517          08680          ;
518          08700          ; INITIALIZE THE DEVICE HANDLER ;
519          08720          ; SET UP A POINTER AREA WITH THE
520          08740          ; DEVICE ADDRESSES & VECTORS ETC.
521          08760          ;
522          002120 032737 000040 001364          BIT      #MULTI,PCFLAG
523          002126 001407          BEQ      500038
524          002130 004737 002332          JSR      PC,GVL
525          002134 004737 002432          JSR      PC,MTW
526          002140 004737 002556          JSR      PC,GNL
527          002146 000410          BR       500048
528          500038          ;
529          08880          ;
530          08900          ; GET SELECTED LINE NUMBER AND
531          08920          ; PULL THE DATA FROM THE TABLE.
532          08940          ;
533          002146 013737 001370 016160          MOV      LINENO,ONLIN
534          002154 004737 002432          JSR      PC,MTW
535          002160 013737 016160 016162          MOV      ONLIN,NXTLIN
536          500048          ;
537          002160 042737 001000 001364          BIC      #NEWMOD,PCFLAG
538          002176 000402          BR       500058
539          500058          ;
540          09080          ;
541          09100          ; DO LINESEL SECTION FOR EACH DEVICE
542          09120          ; TO BE TESTED.
543          09140          ;
544          002176 004737 002204          JSR      PC,LINESEL
545          002202          ;
546          002202          500058          ;
547          002202          500068          ;
548          002202          500018          ;
549          002202          RTS      PC
550          09220          ;
551          09240          ;
552          09260          ;*****
553          09280          ;LINESEL ROUTINE TO FURNISH THE IQDRIVER WITH DVC POINTERS
554          09300          ;*****
555          09320
556          09360          ;
557          09380          ; MULTIPLE LINES UNDER TEST ?
558          09400          ;
559          002204 032737 000040 001364          BIT      #MULTI,PCFLAG
560          002212 001426          BEQ      500028
561          09440          ;
562          09460          ; SET UP POINTER AREA WITH DATA FOR

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556          09480          ; THE NEXT DEVICE TO BE TESTED
557          09500          ;
558          002214 013737 016162 016160          MOV      NXTLIN,ONLIN
559          09540          ;
560          09560          ; RESET EVERYTHING IF AT THE END OF OUR DEVICE LIST.
561          09580          ;
562          002222 023727 016160 177777          CMP      ONLIN,#-1
563          002230 001012          BNE      500038
564          002232 052737 000400 001364          BIS      #LDONE,PCFLAG
565          002240 004737 002332          JSR      PC,GVL
566          002244 004737 002432          JSR      PC,MTW
567          002250 004737 002556          JSR      PC,GNL
568          002254 000404          BR       500048
569          500038          ;
570          09720          ;
571          09750          ; SET UP POINTER AREA FOR LINE = 'ONLIN'.
572          09780          ;
573          002256 004737 002432          JSR      PC,MTW
574          002262 004737 002556          JSR      PC,GNL
575          500048          ;
576          002266 000420          BR       500058
577          500028          ;
578          002270 004737 002432          JSR      PC,MTW
579          002274 032737 000200 016126          BIT      #SEL,DLFLAG
580          002302 001404          BEQ      500068
581          09920          ;
582          09940          ; CHECK TO SEE IF ALL DEVICES
583          09960          ; HAVE BEEN TESTED YET, SET LDONE FLAG.
584          09980          ;
585          002304 052737 000400 001364          BIS      #LDONE,PCFLAG
586          002312 000406          BR       500078
587          500068          ;
588          10040          ;
589          10060          ; MAKE SHURE THAT WHEN TESTING A SINGLE
590          10080          ; DEVICE , IT DOESN'T GET DROPPED
591          10100          ; BECAUSE OF EXCESSIVE ERRORS.
592          10120          ;
593          002314 052737 000200 016126          BIS      #SEL,DLFLAG
594          002322 012700 020345          MOV      #E20,R0
595          002326 104000          TYPE
596          10180          ;
597          500078          ;
598          500058          ;
599          500008          ;
600          500018          ;
601          002330 000207          RTS      PC
602          10260          ;
603          10280          ;*****
604          10300          ; GVL THIS ROUTINE FINDS A VALID LINE FOR TESTING
605          10320          ;*****
606          10340
607          10360          GVL:
608          002332 010346          MOV      R3,=(SP)
609          002334 012703 016170          MOV      #LIN00,R3          ;GET ADDR OF LINE TABLE
610          002340 005713          TST      (R3)          ;LIN PRESENT?
611          002342 100412          BMI      G1D          ;YES BRANCH
612          002344 062703 000010          ADD      #10,R3          ;POINT TO OTHER WORD

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004 002350 020027 016770 10460 CMP R0,#TABEND ;END OF TABLE ?
005 002354 001371 10480 BNE GIA ;NO BRANCH
006 002356 012700 020306 10500 G1C1 MOV #E19,R0 ;NOTIFY OPERATOR = NO LINES
007 002362 104000 10520 TYPE
008 002364 000137 001372 10540 JMP RESTRT
009 002370 105713 10560 G1D1 TSTB (R3) ;LINE SELECTED?
010 002372 100364 10580 BPL G1B ;NO TRY ANOTHER LINE
011 002374 062703 000006 10600 ADD #6,R3 ;POINT TO OTHER WORD
012 002400 011337 016160 10620 MOV (R3),ONLIN ;GET DATA FROM TABLE
013 002404 000337 016160 10640 SWAB ONLIN
014 002410 105037 016161 10660 CLRR ONLIN+1
015 002414 005037 000174 10680 CLR SWLINE
016 002420 113737 016160 000174 10700 MOVB ONLIN,SWLINE
017 002426 012603 10740 MOV (SP)+,R3
018 002430 000207 10760 RTS PC ;EXIT
019 *****
020 ; MTW THIS ROUTINE TRANSFERS TABLE DATA TO THE WORK AREA
021 *****
022
023 002432 10840 MTW1
(4) 002434 010346 10860 MOV R3,=(SP)
024 002434 013703 016160 10880 MOV ONLIN,R3 ;GET LINE NO,
025 002440 006303 10900 ASL R3
026 002442 006303 10920 ASL R3
027 002444 006303 10940 ASL R3 ;XB FOR OFFSET
028 002446 062703 016170 10960 ADD #LIN00,R3 ;ADD IN BASE ADDR
029 002452 012337 016126 10980 MTW11 MOV (R3)+,DLFLAG ;GET FLAG WORD
030 002456 012337 016130 10980 MOV (R3)+,DLADR ;GET ADDRESS
031 002462 012337 016132 11000 MOV (R3)+,DLVEC ;GET VECTOR
032 002466 011337 016134 11020 MOV (R3),DLOTH ;GET "OTHER WORD"
033 002472 013737 016130 11040 MOV DLADR,DVCRXB
034 002500 062737 000002 016136 11060 ADD #2,DVCRXB
035 002506 013737 016136 016140 11080 MOV DVCRXB,DVCTXS
036 002514 062737 000002 016140 11100 ADD #2,DVCTXS
037 002522 013737 016140 016142 11120 MOV DVCTXS,DVCTXB
038 002530 013737 016132 016144 11140 MOV DLVEC,TVVEC
039 002536 062737 000004 016144 11160 ADD #4,TVVEC
040 002544 062737 000002 016142 11180 ADD #2,DVCTXB
041 002552 012603 11220 MOV (SP)+,R3
042 002554 000207 11240 RTS PC
043 *****
044 ; GNL THIS ROUTINE FINDS THE NEXT VALID LINE TO TEST
045 *****
046
047 002556 11320 GNL1
(4) 002556 010346 11340 MOV R3,=(SP)
048 002560 013703 016160 11360 MOV ONLIN,R3 ;GET CURRENT LINE
049 002564 005203 11380 INC R3 ;CURRENT +1
050 002566 006303 11400 ASL R3
051 002570 006303 11420 ASL R3
052 002572 006303 11440 ASL R3 ;XB FOR OFFSET
053 002574 062703 016170 11460 ADD #LIN00,R3 ;ADD IN BASE ADDR OF TABLE
054 002600 005713 11480 GN11 TST (R3) ;LINE PRESENT?
055 002602 100403 11480 BMI GN3 ;YES = BRANCH
056 002604 062703 000010 11500 GN21 ADD #10,R3 ;POINT TO NEXT LINE ENTRY
057 002610 000773 11520 BR GN1 ;CHECK NEXT

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058 002612 105713 11540 GN31 TSTB (R3) ;LINE SELECTED?
059 002614 100373 11560 BPL GN2 ;NO TRY ANOTHER
060 002616 021327 177777 11580 CMP (R3),#-1 ;END OF TABLE?
061 002622 001412 11600 BEQ GN5 ;YES = BRANCH
062 002624 062703 000006 11620 ADD #6,R3 ;GET "OTHER WORD"
063 002630 011337 016162 11640 MOV (R3),NXTLIN
064 002634 000337 016162 11660 SWAB NXTLIN ; = NEXT AVAILABLE LINE
065 002640 105037 016163 11680 CLRR NXTLIN+1
066 002644 11700 GN41
(4) 002644 012603 11720 MOV (SP)+,R3
067 002646 000207 11740 RTS PC ;EXIT
068 002650 012737 177777 016162 11760 GN51 MOV #-1,NXTLIN ;SET NXTLIN TO -1 = NO SELECT
069 002656 000772 11780 BR GN4
070 11800
071 11820 *****
072 ;UPDATE ROUTINE TO UPDATE INTERFACE TABLE FROM COMMANDS
073 *****
074
075 UPDATE1
076 ; SHIFT THE CONVERTED LINE NO. FOR AN
077 ; OFFSET TO THE LINE TABLE,
078 ;
079
080 002660 006337 004776 11900 ASL DATA
(7) 002664 006337 004776 11920 ASL DATA
(7) 002670 006337 004776 11940 ASL DATA
081 ; ADD IN THE BASE ADDRESS OF THE TABLE,
082 002674 062737 016170 004776 11960 ADD #LIN00,DATA
083 ;
084 ; IF THE LINE SELECTED DOESN'T EXIST =
085 ; SEND AN ERROR MESSAGE,
086 ;
087 002702 032777 100000 002066 11980 BIT #DLP,ADATA
(9) 002710 001003 12000 BNE 500028
088 002712 004737 005100 12020 JSR PC,SELERR
089 002716 000430 500028 BR 500038
(3) 002720
090 12100
091 ; ADDING A LINE SETS IT'S "SELECTED" FLAG
092 ; AND CLEARS OUT THE ERROR COUNT FOR THAT LINE
093 ;
094 002720 032737 000004 005002 12120 BIT #ADDC,TEMPF
(9) 002726 001415 12140 BEQ 500048
095 002730 052777 000200 002040 12160 BIS #SEL,ADATA
096 002736 042777 000037 002032 12180 BIC #AB01#17,ADATA
097 002744 062737 000007 004776 12200 ADD #7,DATA
098 002752 117737 002020 001370 12220 MOVB @DATA,LINENO
099 002760 000407 500048 BR 500056
(3) 002762
100 ;
101 12300
102 ; DROPPING A LINE JUST RESETS IT'S "SELECTED" FLAG
103 ;
104 002762 032737 000010 005002 12320 BIT #DROPC,TEMPF
(9) 002770 001403 12340 BEQ 500068
105 002772 042777 000200 001776 12360 BIC #SEL,ADATA
106 003000 500068

```



```

706 003000          5000551
707 003000          5000351
708 003000 005037 004776          CLR    DATA
709 003004          5000081
(3) 003004          5000151
(2) 003004 000207          RTS    PC
710          12480
711          12500
  
```

```

12540          ,SBTTL SWITCH REGISTER ROUTINES
12560          ;*****
12580          ;SWRTST TESTS FOR HARDWARE SWITCH REGISTER
12600          ;*****
12620          SWRTST: MOV    #48,MACHER    ;SU NXM TRAP TO 48
12640          MOV    #PRI7,MACHER+2
12660          TST    #SWR                ;ACCESS SWITCH REG.
12680          NOP
12700          MOV    #177570,SWR        ;RETAIN HARDWARE POINTER
12720          BR     68
12740          48: MOV    #SSWR,SWR        ;SU FOR SOFTWARE SWITCH REG.
12760          ISP2
12780          68: MOV    #6,MACHER        ;CLEAN THE STACK
12800          CLR    MACHER+2          ;RESET TRAP CATCHER
12820          RTS    PC                ;EXIT
12840
12860          ;*****
12880          ; CTLGX THIS ROUTINE PRINTS THE PROGRAM CONTROL FLAGS ON THE CONSOLE.
12900          ;*****
12920
12940          CTLGX:
12960          MOV    PCFLAG,=(SP)
12980          MOV    #6,=(SP)
13000          MOV    #SW+1,=(SP)
13020          JSR    PC,U2ASC
13040          BICB  #6,SW+1
13060          MOV    #SW,R0
13080          TYPE
13100          MOV    #L1,R0
13120          TYPE
13140          5000081
13160          5000151
13180          RTS    PC
13200
13220          ;*****
13240          ;GETSWS THIS ROUTINE READS THE SWITCH REGISTER AND
13260          ; CONVERTS THE DATA TO THE APPROPRIATE CONTROL
13280          ; FLAGS OR POINTERS.
13300          ;*****
13320          GETSWS:
13340          ;
13360          ; STOP HERE FOR OPERATOR TO ENTER CONTROL SWITCHES
13380          ;
13400          HALT
13420          MOV    #SWR,TEMP
13440          ;
13460          ; IF SWITCHES INDICATE A SINGLE LINE OR A SINGLE TEST
13480          ; TO BE DONE STOP SO OPERATOR CAN ENTER LINE/TEST DATA
13500          ;
13520          BIT    #SEQ,TEMP
13540          BEQ    5000201
13560          BIT    #MULTI,TEMP
13580          BNE    5000301
13600          5000201
13620          HALT
  
```

763	003162	017737	012736	016102		MOV	0SWR,TEMP+2
764	003170	005037	001366			CLR	TESTNO
765	003174	113737	016102	001366		MOVH	TEMP+2,TESTNO
766	003202	005037	001370			CLR	LINENO
767	003206	113737	016103	001370		MOVH	TEMP+3,LINENO
768	003214				500030:		
769	003214	032737	000100	016100		BIT	#SEQ,TEMP
(9)	003222	001406				BEQ	500045
770	003224	052737	000100	001364		BIS	#SEQ,PCFLAG
771	003232	005037	001366			CLR	TESTNO
772	003236	000403				BR	500055
(3)	003240				500040:		
773	003240	042737	000100	001364		BIC	#SEQ,PCFLAG
774	003246				500050:		
775				13780			
776	003246	032737	000040	016100		BIT	#MULTI,TEMP
(9)	003254	001406				BEQ	500065
777	003256	052737	000040	001364		BIS	#MULTI,PCFLAG
778	003264	005037	001370			CLR	LINENO
779	003270	000403				BR	500075
(3)	003272				500060:		
780	003272	042737	000040	001364		BIC	#MULTI,PCFLAG
781	003300				500070:		
782				13940			
783	003300	052737	003200	001364		BIS	#ATTN,NEWMOD,NEWST,PCFLAG
784				13980			
785	003306	042737	174037	001364		BIC	#174037,PCFLAG
786	003314	042737	003777	016100		BIC	#3777,TEMP
787	003322	053737	016100	001364		BIS	TEMP,PCFLAG
788	003330				500080:		
(3)	003330				500010:		
(2)	003330	000207			RTS	PC	
789				14100			

791					14840		,SRTL CONSOLE TERMINAL ROUTINES
792					14860		*****
793					14880		ICONSON== ROUTINE TO INITIALIZE CONSOLE VECTOR AREA
794					14900		*****
795					14920		
796	003332	012737	003466	000060	14940	CONSON:	MOV #READKB,0*TKV ;INTERRUPT TO "READKB"
797	003340	012737	000101	177560	14960	MOV	#01,0*TKS
798	003346	000207			14980	RTS	PC
799					15000		
800					15020		*****
801					15040		GETSRC ; THIS ROUTINE ASKS THE OPERATOR IF HE/SHE
802					15060		WANTS TO USE CONSOLE CONTROL, THEN SETS
803					15080		A CONTROL FLAG ACCORDINGLY,
804					15100		;
805					15120		*****
806					15140		
807	003350					GETSRC:	
808	003350	005077	012610			CLR	0PTR
809	003354	012700	020370			MOV	0CTRL,R0
810	003360	104000			15220	TYPE	
811	003362	012737	000001	177560		MOV	#1,0*TKS
812	003370				500020:		
813	003370	032737	000200	177560		BIT	#READY,0*TKS
(9)	003376	001410				BEQ	500045
814	003400	113777	177562	012556		MOVH	0*TKB,0PTR
815	003406	004737	005044			JSR	PC,ECHO
816	003412	012700	017730			MOV	#L1,R0
817	003416	104000			15360	TYPE	
818	003420				500040:		
819	003420	005777	012540			TST	0PTR
(5)	003424	001001				BNE	500038
820	003426	000760				BR	500025
(3)	003430				500030:		
821	003430	142777	000200	012526		BICB	#200,0PTR
822	003436	027727	012522	000116		CMR	0PTR,#'N
(9)	003444	001007				BNE	500055
(6)	003446	023727	016124	000176		CMR	SWR,SSWR
(9)	003454	001403				BEQ	500055
823	003456	052737	000020	001364		BIS	#SWCTL,PCFLAG
824	003464				500050:		
825	003464				500000:		
(3)	003464				500010:		
(2)	003464	000207			RTS	PC	

```

827      15540 ;*****
828      15560 ; READKB THIS MODULE IS AN INTERRUPT HANDLER
829      15580 ;          FOR THE CONSOLE TERMINAL,
830      15600 ;*****
831      15620
832 003466 READKB:
833 003466 010046      MOV     R0,-(SP)
834
835      15650 ;
836      15655 ; GET CHAR FROM KEYBOARD BUFFER REG.
837      15660 ; CLEAR PARITY BIT IF SET.
838      15665 ;
839 003470 013737 177562 003776      MOV     @TKB,RDSAV
840 003476 142737 000200 003776      BICB   #200,RDSAV
841
842      15680 ;
843      15685 ; CHECK FOR DEVICE ERROR
844      15690 ;
845 003504 032737 100000 003776      BIT     #MERR,RDSAV
846 (9) 003512 001400      BEQ    500028
847 003514 004737 005062      JSR    PC,CMDERR
848 003520 005037 177560      CLR    @TKS
849 003524 000516      BR     500038
850 (3) 003526
851      500028:
852      15715 ;
853      15720 ; IF CMD CHAR WAS A CTL-G DO THE CTLGX ROUTINE.
854      15725 ; PRINT OUT PCFLAGS ON CONSOLE.
855      15730 ;
856 003526 123727 003776 000007      CMPB   RDSAV,#CTLG
857 (9) 003534 001006      BNE    500048
858 003536 004737 003064      JSR    PC,CTLGX
859 003542 012700 017730      MOV     #L1,R0
860 003546 104000      15750 TYPE
861 003550 000504      BR     500050
862 (3) 003552
863      500048:
864      15756 ;
865      15757 ; IF IN I/O MODE PUT DATA IN I/O BUFFER
866      15758 ;
867 003552 032737 000002 001364      BIT     #FLAG2,PCFLAG
868 (9) 003560 001410      BEQ    500068
869 003562 113711 003776      MOVR   RDSAV,(R1)
870 003566 052737 004000 001364      BIS    #DATAIN,PCFLAG
871 003574 005037 007564      CLR    DELAYT
872 (3) 003602      BR     500078
873      500068:
874      15776 ;
875      15777 ; IF IN COMMAND MODE PUT DATA IN INBUF
876      15778 ; AND CALL INTERPRITER
877      15779 ;
878 003602 032737 000001 001364      BIT     #FLAG1,PCFLAG
879 (9) 003610 001406      BEQ    500108
880 003612 113777 003776 012344      MOVB   RDSAV,#PTR
881 003620 004737 004000      JSR    PC,C51
882 (3) 003626      BR     500118
883      500108:
884      15796 ;
885      15797 ; CLEAR AND GO TO READY STATE.
886      15798 ;
  
```

```

875 003626 123727 003776 000003      CMPB   RDSAV,#CTLG
876 (9) 003634 001003      BNE    500128
877 003636 004737 001372      JSR    PC,RESTR
878 003642 000447      BR     500138
879 (3) 003644
880      500128:
881      15811 ;
882      15812 ; CHECK FOR PRINT REPORTS COMMAND
883      15813 ;
884 003644 123727 003776 000020      CMPB   RDSAV,#CTLP
885 (9) 003652 001004      BNE    500148
886 003654 042737 020000 001364      BIC    #INHRPT,PCFLAG
887 (3) 003662      BR     500158
888      500148:
889      15826 ;
890      15827 ; CHECK FOR NO REPORTS COMMAND
891      15828 ;
892 003664 123727 003776 000016      CMPB   RDSAV,#CTLN
893 (9) 003672 001004      BNE    500168
894 003674 052737 020000 001364      BIS    #INHRPT,PCFLAG
895 003702 000427      BR     500178
896 (3) 003704
897      500168:
898      15841 ;
899      15842 ; CHECK FOR HALT ON ERROR COMMAND
900      15843 ;
901 003704 123727 003776 000010      CMPB   RDSAV,#CTLH
902 (9) 003712 001004      BNE    500208
903 003714 052737 100000 001364      BIS    #HALTOE,PCFLAG
904 003722 000417      BR     500218
905 (3) 003724
906      500208:
907      15856 ;
908      15857 ; CHECK FOR LOOP ON ERROR COMMAND
909      15858 ;
910 003724 123727 003776 000014      CMPB   RDSAV,#CTLL
911 (9) 003732 001004      BNE    500228
912 003734 052737 040000 001364      BIS    #LOOPOE,PCFLAG
913 003742 000407      BR     500238
914 (3) 003744
915      500228:
916      15871 ;
917      15872 ; CHECK FOR CLEAR COMMAND
918      15873 ;
919 003744 123727 003776 000013      CMPB   RDSAV,#CTLK
920 (9) 003752 001003      BNE    500248
921 003754 042737 140000 001364      BIC    #HALTOE,#LOOPOE,PCFLAG
922 003762      500248:
923 003762      500238:
924 003762      500218:
925 003762      500178:
926 003762      500158:
927 003762      500138:
928 003762      500118:
929 003762      500078:
930 003762      500058:
931 003762      500038:
932 15935 ;
933 15940 ; TURN CONSOLE BACK ON & EXIT.
934 15945 ;
  
```

```

920 003762 012737 000101 177560          MOV    #101,0#TKS
921 003770 012600          MOV    (5P)+,R0
922 003772 000002          RTI
923 003774          500000#1
(3) 003774          500010#1
(2) 003774 000207          RTS    PC
924          16500
925 003776 000000          RDSAV! ,WORD 0
  
```

```

927          16640 ;*****
928          16660 ; CSI  COMMAND STRING INTERPRETER
929          16680 ;*****
930          16700
931 004000          CSI:
932          16740 ;
933          16760 ; IF CMD CHAR IS AN ESCAPE  ECHO A "S"
934          16780 ; AND SET MODE BACK TO 0,
935          16800 ;
936 004000 123727 003776 000033          CMPB   RDSAV,#ESC
(9) 004006 001030          BNE   500020
937 004010 112777 000044 012146          MOVB  #'S,#PTR
938 004016 004737 005044          JSR   PC,ECHO
939 004022 012700 017051          MOV   #L3,R0
940 004026 104000          16900  TYPE
941 004030 042737 000001 005002          BIC   #FLAG1,TEMPF
942          17100 ;
943          17120 ; MOVE NEW CONTROL FLAGS TO THE PCFLAG WORD,
944          17140 ; RESET THE BUFFER POINTER,
945          17160 ;
946 004036 013737 005002 001364          MOV   TEMPF,PCFLAG
947 004044 013737 005004 001366          MOV   TEMPI,TESTNO
948 004052 012737 016114 010164          MOV   #INBUF,PTR
949          17235 ; CLEAR ATTENTION FLAGS FROM TEMPF
950 004060 042737 017603 005002          BIC   #17003,TEMPF
951          17260
952 004066 000457          BR    500030
(3) 004070          500020#1
953          17300 ;
954          17320 ; IF CMD CHAR WAS A DELETE  RESET THE BUFFER
955          17340 ; POINTER AND ECHO A CR/LF,
956          17360 ;
957 004070 123737 003776 010166          CMPB  RDSAV,DEL
(9) 004076 001007          BNE  500040
958 004100 012737 016114 010164          MOV   #INBUF,PTR
959 004106 012700 017730          MOV   #L1,R0
960 004112 104000          17440  TYPE
961 004114 000444          BR    500030
(3) 004116          500040#1
962          17480 ;
963          17500 ; IF CMD CHAR WAS A RETURN ECHO A CR/LF
964          17520 ; AND CALL THE DECODER,
965          17540 ;
966 004116 123727 003776 000015          CMPB  RDSAV,#CR
(9) 004124 001021          BNE  500060
967 004126 012700 017730          MOV   #L1,R0
968 004132 104000          17600  TYPE
969 004134 004737 004230          JSR   PC,DECODE
970 004140 123727 016114 000121          CMPB  INBUF,#'Q
(9) 004146 001007          BNE  500070
971 004150 005037 001366          CLR  TESTNO
972 004154 005037 005004          CLR  TEMPT
973 004160 012737 000001 016152          MOV   #1,NEXT
974 004166          500070#1
975 004166 000417          BR    500100
(3) 004170          500060#1
  
```

```

976          17800 ;
977          17820 ; IF CMD CHAR WAS A '7' REPLY THE COMMAND
978          17840 ; SUMMARY & GO TO READY CONDITION,
979          17860 ;
980 004170 123727 003776 000077          CMB  RDSAV,#'?
(9) 004176 001007          BNE  500119
981 004200 012700 017103          MOV  #COMSUM,R0
982 004204 104000          17920 TYPE
983 004206 012700 020123          MOV  #RDY,R0
984 004212 104000          17960 TYPE
985 004214 000404          BR   500129
(3) 004216          500119;
986          18000 ;
987          18020 ; ECHO THE INPUT CHARACTER,
988          18040 ;
989 004216 004737 005044          JSR  PC,ECHO
990 004222 005237 016164          INC  PTR
991 004226          500129;
992 004226          500109;
993 004226          500059;
994 004226          500039;
995 004226          500009;
(3) 004226          500019;
(2) 004226 000207          RTS  PC
996          18220
997          18240
  
```

```

999          18280 ;*****
1000         18300 ;DECODE THIS SECTION DECODES THE COMMAND STRING FROM THE
1001         18320 ; CONSOLE, AND SETS THE APPROPRIATE CONTROL FLAGS.
1002         18340 ;*****
1003         18360
1004 004230          DECODE:
1005         18400
1006 004230 010046          MOV  R0,=(SP)
1007 004232 010146          MOV  R1,=(SP)
1008 004234 010246          MOV  R2,=(SP)
1009 004236 012702 001364          MOV  #CTLBLK,R2
1010 004242 012700 004656          MOV  #DECTL,R0
1011 004246 012701 016114          MOV  #INBUF,R1
1012 004252          500029;
1013         18560 ;
1014         18580 ; COMPARE CHAR IN TO FIRST BYTE OF TABLE
1015         18600 ;
1016 004252 121110          CMB  (R1),(R0)
(9) 004254 001145          BNE  500049
1017         18640 ;
1018         18660 ; IF SAME GET FLAGS FROM THE TABLE TO TEMP;
1019         18680 ;
1020 004256 116037 000001 004774          MOV  1(R0),DECSAV
1021 004264 056037 000002 005002          HIS  2(R0),TEMPF
1022 004272 046037 000004 005002          BIC  4(R0),IEMPF
1023 004300 005037 004776          CLR DATA
1024 004304 005037 005000          CLR DATA2
1025         18800 ;
1026         18820 ; SEE IF THIS COMMAND REQUIRES ADDITIONAL DATA
1027         18840 ;
1028 004310 032737 000340 004774          BIT  #NREQ,DECSAV
(9) 004316 001520          BEQ  500059
1029 004320 126127 000001 000015          CMB  1(R1),#CR
(9) 004326 001006          BNE  500069
1030         18900 ;
1031         18920 ; DATA REQUIRED BUT NOT PRESENT...ERROR
1032         18940 ;
1033 004330 004737 005062          JSR  PC,CMDERR
1034         18980 ;
1035         19000 ; IF A OR D COMMAND USE DATA FOR LINE NO.
1036         19020 ;
1037 004334 012737 016114 016164          MOV  #INBUF,PTR
1038 004342 000505          BR   500079
(3) 004344          500069;
1039         19080 ;
1040         19100 ; CONVERT THE CHARS TO OCTAL...DATA
1041         19120 ;
1042 004344 012746 004776          MOV  #DATA,=(SP)
1043 004350 116137 000001 004776          MOV  1(R1),DATA
1044 004356 126127 000002 000015          CMB  2(R1),#CR
(9) 004364 001003          BNE  500109
1045 004366 012746 000001          MOV  #1,=(SP)
1046 004372 000417          BR   500119
(3) 004374          500109;
1047 004374 116137 000002 004777          MOV  2(R1),DATA+1
1048 004402 126127 000003 000015          CMB  3(R1),#CR
  
```

```

1049 004410 001003          BNE 500120
1050 004412 012746 000002    MOV  #2,=(SP)
1051 004416 000405          BR 500130
(3) 004420          500120:
1051 004420 116137 000003 005000    MOV 3(R1),DATA2
1052 004426 012746 000003    MOV  #3,=(SP)
1053 004432          500130:
1054 004432          500110:
1055 004432 012746 004776    MOV  #DATA,=(SP)
1056 004436 004737 007676    JSR  PC,A2BIN
1057          19440 ;
1058          19460 ; IF M COMMAND USE DATA AS A TEST NO.
1059          19480 ;
1060 004442 121027 000122    CMPB (R0),#M
(9) 004446 001020          BNE 500140
1061          19520 ;
1062          19540 ; CHECK THE LIMITS FOR VALID TEST NO.
1063          19560 ;
1064          19580 ;***** #5 BELOW IS HIGHEST TEST NO THIS DIAGNOSTIC *****
1065 004450 005737 004776    TEST DATA
(0) 004454 002404          BIT 500150
(6) 004456 023727 004776 000005    CMP  DATA,#5
(9) 004464 003403          BLE 500160
(6) 004466          500150:
1066 004466 004737 005100    JSR  PC,SELERR
1067          19640 ;
1068          19660 ; OUT OF RANGE ERROR,
1069          19680 ;
1070 004472 000403          BR 500170
(3) 004474          500160:
1071 004474 013737 004776 005004    MOV  DATA,EMPT
1072 004502          500170:
1073 004502 052737 100000 005000    BIS  #MERR,DATA2
1074 004510          500140:
1075          19800 ;
1076          19820 ; IF W COMMAND USE DATA AS WIDTH
1077          19840 ;
1078 004510 121027 000127    CMPB (R0),#W
(9) 004514 001005          BNE 500200
1079          19880 ;
1080          19900 ; GO CHECK FOR VALID LIMITS ON WIDTH ENTRY,
1081          19920 ;
1082 004516 004737 005006    JSR  PC,CHKW
1083 004522 052737 100000 005000    BIS  #MERR,DATA2
1084 004530          500200:
1085          20000 ;
1086          20020 ; IF ADDING OR DROPPING A LINE CALL UPDATE ROUTINE
1087          20040 ;
1088 004530 121027 000101    CMPB (R0),#A
(0) 004534 001403          BEQ 500210
(6) 004536 121027 000104    CMPB (R0),#D
(9) 004542 001005          BNE 500220
(6) 004544          500210:
1089          20080 ;
1090          20100 ; TAKE LINE NO. AND UPDATE INTERFACE TABLE
1091          20120 ;
    
```

```

1092 004544 004737 002660          JSR  PC,UPDATE
1093 004550 052737 100000 005000    BIS  #MERR,DATA2
1094 004556          500220:
1095 004556          500070:
1096 004556 000403          BR 500230
(3) 004560          500050:
1097 004560 052737 100000 005000    BIS  #MERR,DATA2
1098 004566          500230:
1099 004566 000415          BR 500240
(3) 004570          500040:
1100 004570 062700 000006    ADD  #6,R0
1101          20320 ;
1102          20340 ; IF THE CHAR IN DOESN'T COMPARE TO ANY
1103          20360 ; TABLE ENTRY THE COMMAND IS INVALID
1104          20380 ;
1105 004574 020027 004774          CMP  R0,#DTEND
(9) 004600 001010          BNE 500250
1106 004602 004737 005002    JSR  PC,CMDERR
1107 004606 012737 016114 016164    MOV  #INBUF,PTR
1108 004614 052737 100000 005000    BIS  #MERR,DATA2
1109 004622          500250:
1110 004622          500240:
1111          20520 ;
1112          20540 ; KEEP LOOKING AT CHAR UNTIL IT'S
1113          20560 ; DECODED, OR END OF TABLE (ERROR).
1114          20580 ;
1115 004622 032737 100000 005000    BIT  #MERR,DATA2
(5) 004630 001001          BNE 500030
1116 004632 000607          BR 500020
(3) 004634          500030:
1117 004634 005037 005000    CLR  DATA2
1118          20660 ;
1119          20680 ; RESET THE INPUT BUFFER POINTER
1120          20700 ;
1121 004640 012737 016114 016164    MOV  #INBUF,PTR
1122 004646 012602          MOV  (SP)+,R2
1123 004650 012601          MOV  (SP)+,R1
1124 004652 012600          MOV  (SP)+,R0
1125 004654          500000:
(3) 004654          500010:
(2) 004654 000207          RTS  PC
1126          20820 ;
1127          20840 ;*****
1128 004656 123 000          DECTBL ,BYTE 'S,0 ;DECODE TABLE
1129 004660 001200 000040    ,WORD ATTNINEMOD,MULTI
1130 004664 115 000          ,BYTE 'M,0 ;FIRST = CHAR TO BE DECODED
1131 004666 001240 000000    ,WORD ATTNIMULTIINEMOD,0
1132 004672 121 000          ,BYTE '0,0 ;SECOND = CONTROL BITS
1133 004674 000300 000000    ,WORD ATTNISEQ,0
1134 004700 122 200          ,BYTE 'R,200 ;THIRD = SET MASK
1135 004702 000200 000100    ,WORD ATTN,SEQ
1136 004706 104 100          ,BYTE 'D,100 ;FOURTH = CLEAR MASK
1137 004710 002010 000004    ,WORD DROPCINEMTST,ADDC
1138 004714 101 100          ,BYTE 'A,100
1139 004716 002004 000010    ,WORD ADDCINEMTST,DROPC
1140 004722 124 000          ,BYTE 'T,0 ;CONTROL BITS
    
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1141 004724 010000 000000 21120 ,WORD PRINTI,0
1142 004730 114 000 21140 ,BYTE 'L,0
1143 004732 040000 000000 21160 ,WORD LOOPOE,0
1144 004736 110 000 21180 ,BYTE 'H,0 ;5 = GET WIDTH
1145 004740 100000 000000 21200 ,WORD HALTOE,0
1146 004744 116 000 21220 ,BYTE 'N,0 ;6 = GET LINE N
1147 004746 020000 000000 21240 ,WORD INHRRPT,0
1148 004752 120 000 21260 ,BYTE 'P,0 ;7 = GEN TEST NO.
1149 004754 000000 020000 21280 ,WORD 0,INHRRPT
1150 004760 103 000 21300 ,BYTE 'C,0
1151 004762 000000 140000 21320 ,WORD 0,HALTOE,LOOPOE
1152 004766 127 040 000 21340 ,BYTE 'M,40,0,0,0,0
      004771 000 000 000
1153 004774 21360 DTENDI
1154 004774 000000 21380 DECSAVI ,WORD 0
1155 004776 000000 21400 DATA1 ,WORD 0
1156 005000 000000 21420 DATA2 ,WORD 0
1157 005002 000000 21440 TEMPF1 ,WORD 0 ;TEMPORARY PCFLAG WORD
1158 005004 000000 21460 TEMPT1 ,WORD 0 ;TEMPORARY TEST NO.
1159 21480
1160 21500
1161 21520
1162 21540 ;*****
1163 21560 ;CHKW THIS ROUTINE VALIDATES A "N" COMMAND
1164 21580 ;*****
1165 21600
1166 005006 CHKW1
1167 21621 ;
1168 21622 ; RANGE OF 26 THRU 132 CHARACTERS IS VALID
1169 21623 ;
1170 005006 023727 004776 000032 CMP DATA,#32
      (0) 005014 002404 BLT 500020
      (6) 005016 023727 004776 000204 CMP DATA,#132.
      (9) 005024 003403 BLE 500035
      (6) 005026 500028 JSR PC,SELERR
1171 005026 004737 005100 BR 500045
1172 005032 000403 500038 MOV DATA,WIDTH
      (3) 005034 013737 004776 016146 500048
1174 005042 500008
1175 005042 500018
      (3) 005042 500018 RTS PC
      (2) 005042 000207
1176 21760
1177 21780
  
```

```

1179 21820 ;*****
1180 21840 ;ECHO CONSOLE KEYBOARD ECHO ROUTINE; PIR HAS ADDR OF CHAR
1181 21860 ;*****
1182 21880
1183 005044 105737 177564 ECHOI TSTB @*IPB
1184 005050 100375 21900 BPL ECHO
1185 005052 117737 011106 177566 MOVH @PIR,@*IPB
1186 005060 000207 21940 RTS PC
1187 21960
  
```

```

1189          22020          ,SBTTL  ERROR & REPORT ROUTINES
1190          22040          ;*****
1191          22060          ;CMDEMR  ROUTINE TO HANDLE INVALID COMMANDS
1192          22080          ;*****
1193          22100
1194          005062          CMDEMR:
1195          005062  012700  020017          MOV   #R1,R0
1196          005066  104000          22160          TYPE  MOV   #RDY,R0
1197          005070  012700  020123          22200          TYPE
1198          005074  104000          500001
1199          005076          500011
1200          (3) 005076          RTS   PC
1201          (2) 005076  000207          22240          ;*****
1202          1200          22260          ;SELEMR  ROUTINE TO HANDLE SELECTION ERRORS
1203          1204          22280          ;*****
1204          005100          SELEMR:
1205          005100  012700  020032          MOV   #R2,R0
1206          005104  104000          22360          TYPE  MOV   #RDY,R0
1207          005106  012700  020123          22400          TYPE  MOV   #INBUF,PTH
1208          005112  104000          500001
1209          005114  012737  016114  016164          500011
1210          005122          RTS   PC
1211          (3) 005122
1212          (2) 005122  000207          22460
  
```

```

1213          22500          ;*****
1214          22520          ;ERRORS ERROR LOGGER AND TYPEOUT ROUTINE
1215          22540          ;
1216          22560          ;*****
1217          22580
1218          005124          ERROR:
1219          005124  005037  005434          CLR   ERRSAV
1220          005130  032737  020000  001364          BIT   #INHRPT,PCFLAG
1221          (9) 005136  001044          BNE  500028
1222          22642          ;
1223          22644          ;CONVERT TEST NO. FOR OUTPUT
1224          22646          ;
1225          005140  013746  016154          MOV   INTEST,=(SP)
1226          005144  012746  000002          MOV   #2,=(SP)
1227          005150  012746  020001          MOV   #ER0+16,,=(SP)
1228          005154  004737  007566          JSR   PC,02ASC
1229          22722          ;
1230          22724          ;CONVERT ERROR NO. FOR OUTPUT
1231          22726          ;
1232          005160  113737  002032  005434          MOVB  CFLAGS,ERRSAV
1233          005166  013746  005434          MOV   ERRSAV,=(SP)
1234          005172  012746  000003          MOV   #3,=(SP)
1235          005176  012746  017770          MOV   #ER0+7,,=(SP)
1236          005202  004737  007566          JSR   PC,02ASC
1237          22822          ;
1238          22824          ;CONVERT LINE NO. FOR OUTPUT
1239          22826          ;
1240          005206  013746  016160          MOV   ONLIN,=(SP)
1241          005212  012746  000002          MOV   #2,=(SP)
1242          005216  012746  020011          MOV   #ER0+24,,=(SP)
1243          005222  004737  007566          JSR   PC,02ASC
1244          005226  012700  017761          MOV   #ER0,R0
1245          005232  104000          TYPE
1246          22940          ;
1247          22960          ; CLEAR THE ERROR FLAG
1248          22980          ;
1249          23000          ;
1250          005234  042737  100377  002032          BIT   #EMERN,CFLAGS
1251          23040          ;
1252          23060          ; GET THE POINTER SUPPLIED BY THE PROGRAM
1253          23080          ; AND PRINT THE ERROR DESCRIPTION MSG.
1254          23100          ;
1255          005242  013700  002034          MOV   TSCPTR,R0
1256          005246  104000          TYPE
1257          005250          500028
1258          005250  005037  005434          CLR   ERRSAV
1259          23200          ;
1260          23220          ; UPDATE THE ERROR COUNT FOR THE FAILING LINE
1261          23240          ;
1262          005254  013737  016160  005434          MOV   ONLIN,ERRSAV
1263          005262  006337  005434          ASL  ERRSAV
1264          (7) 005266  006337  005434          ASL  ERRSAV
1265          (7) 005272  006337  005434          ASL  ERRSAV
1266          005276  062737  016170  005434          ADD  #LIN0,ERRSAV
1267          005304  005277  000124          INC  @ERRSAV
1268          23340          ;
1269          23360          ;IF LOOP ON ERROR IS SET , MAKE THE
  
```



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1266          23300 ; RETURN ADDRESS OF THE TEST ODD.
1267          23400 ; THE TEST CONTROLLER WILL USE THE OLD
1268          23420 ; RPC TO RE-DO THE SUBTEST,
1269          23440 ;
1270 005310 032737 040000 001364          BIT    #LOOPE,PCFLAG
      (9) 005316 001403          BFO    500038
1271 005320 052765 000001 000010          BIS    #BIT0,RPC(R5)
1272 005326          500038;
1273          23520 ;
1274          23540 ; SEE IF LINE ABORT FLAG IS SET
1275          23560 ;
1276 005326 032777 000020 000100          BIT    #ABO,ERRSAV
      (9) 005334 001431          BEQ    500048
1277          23600 ;
1278          23620 ; IF ABORT IS SET DESELECT THE LINE
1279          23640 ; UNLESS IT'S THE ONLY ONE BEING TESTED
1280          23660 ;
1281 005336 032737 000040 001364          BIT    #MULTI,PCFLAG
      (9) 005344 001417          BEQ    500058
1282 005346 042777 000377 000060          BIC    #SEL1,77,ERRSAV
1283 005354 013746 016160          MOV    ONLIN,=(SP)
1284 005360 012746 000002          MOV    #2,=(SP)
1285 005364 012746 020270          MOV    #DR1,=(SP)
1286 005370 004737 007566          JSR    PC,02ASC
1287          23800 ;
1288          23820 ; NOTIFY OPERATOR THAT LINE WAS DROPPED
1289          23840 ;
1290 005374 012700 020242          MOV    #DR0,R0
1291 005400 104000          TYPE
1292          23880 ;
1293          23900 ;
1294 005402 000406          23920 ; IF TESTING ONLY ONE LINE DONOT ALLOW IT TO BE DESELECTED
      (3) 005404          BR    500068
1295 005404 052777 000200 000022          BIS    #SEL,ERRSAV
1296 005412 042777 000020 000014          BIC    #ABO,ERRSAV
1297 005420          500068;
1298 005420          500048;
1299          24040 ;
1300          24060 ; HALT HERE IF HALT ON ERROR IS SET
1301          24080 ;
1302 005420 032737 100000 001364          BIT    #HALTOE,PCFLAG
      (9) 005426 001401          BEQ    500078
1303 005430 000000          24120 HALT
1304 005432          500078;
1305 005432          500008;
      (3) 005432          500018;
      (2) 005432 000207          RTS    PC
1306          24180 ;
1307 005434 000000          24200 ERRSAV, WORD 0
1308          24220 ;
1309          24240 ;*****
1310          24260 ; REPORT THIS ROUTINE HANDLES END OF TEST AND
1311          24280 ; END OF PASS REPORTS.
1312          24300 ;*****
1313          24320 ;
1314 005436          REPORT;
    
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1315          24345 ; CHECK FOR END OF TEST CONDITION
1316          24350 ;
1317 005436 032737 040000 002032          BIT    #EOT,CFLAGS
      (9) 005444 001423          BEQ    500028
1318          24365 ;
1319          24370 ; CONVERT TEST NO. FOR OUTPUT
1320          24375 ;
1321 005446 013746 016154          MOV    INTEST,=(SP)
1322 005452 012746 000002          MOV    #2,=(SP)
1323 005456 012746 020210          MOV    #EOTM+10,=(SP)
1324 005462 004737 007566          JSR    PC,02ASC
1325          24445 ;
1326          24450 ; SEND END OF TEST MESSAGE
1327 005466 042737 040000 002032          BIC    #EOT,CFLAGS
1328          24455 ;
1329 005474 012700 020166          MOV    #EOTM,R0
1330 005500 004737 007010          JSR    PC,MTYPE
1331 005504 012700 017051          MOV    #L3,R0
1332 005510 004737 007010          JSR    PC,MTYPE
1333 005514          500028;
1334          24505 ;
1335          24510 ; CHECK FOR END OF PASS CONDITION
1336          24515 ;
1337 005514 032737 020000 002032          BIT    #EOP,CFLAGS
      (9) 005522 001425          BEQ    500038
1338 005524 013746 016154          MOV    INTEST,=(SP)
1339          24545 ;
1340          24550 ; CONVERT TEST NO. FOR OUTPUT
1341          24555 ;
1342 005530 012746 000002          MOV    #2,=(SP)
1343 005534 012746 020101          MOV    #EOPM+19,=(SP)
1344 005540 004737 007566          JSR    PC,02ASC
1345          24605 ;
1346          24610 ; CONVERT PASS NUMBER FOR OUTPUT
1347          24615 ;
1348 005544 013746 002036          MOV    TSCCNT,=(SP)
1349 005550 012746 020145          MOV    #EOPM+7,=(SP)
1350 005554 004737 010006          JSR    PC,BIN2DA
1351          24685 ;
1352          24690 ; SEND END OF PASS MESSAGE,
1353          24695 ;
1354 005560 012700 020136          MOV    #EOPM,R0
1355 005564 004737 007010          JSR    PC,MTYPE
1356 005570 042737 020000 002032          BIC    #EOP,CFLAGS
1357 005576          500036;
1358 005576          500008;
      (3) 005576          500018;
      (2) 005576 000207          RTS    PC
1359          24800 ;
1360          24820 ;
1361          24840 ;
1362          ;
    
```

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1363          00050 ;*****
1364          00070 ; SETIO ROUTINE TO SET I/O MODE
1365          00090 ;*****
1366          00110
1367          00130
1368 005600      SETIO1
1369 005600      500020;
1370 005600 012737 000001 001364      BIT      #FLAG1,PCFLAG
      (9) 005606 001003                BNE      500030
1371 005610 052737 000003 001364      BIS      #FLAG1,#FLAG2,PCFLAG
1372 005616
1373 005616 012737 000001 001364      500030;      BIT      #FLAG1,PCFLAG
      (7) 005624 001765                BEQ      500020
      (4) 005626 032737 000002 001364      BIT      #FLAG2,PCFLAG
      (7) 005634 001761                BEQ      500020
1374 005636
      (3) 005636
      (2) 005636 000207
1375          00320
1376          00340

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1378          00700      ,SBTTL INTERFACE SIZER ROUTINES
1379          00900
1380          00950 ;*****
1381          01000 ;BUILD SUBROUTINE TO BUILD THE DEVICE TABLE USED
      ; IN MULTI LINE MODE.
1382          01050 ;*****
1383 005640 012737 000003 000066 01100 BUILD; MOV      #BPT,66      ;SET UP CONSOLE TRAP
1384 005640 012737 000120 000004 01150      MOV      #46,MACHER      ;SET UP NXM TRAP
1385 005654 012701 016170 01200      MOV      #LIN00,K1
1386 005660 012137 016126 01220 151      MOV      (R1)+,DLFLAG
1387 005664 012137 016130 01240      MOV      (R1)+,DLADR
1388 005670 012137 016132 01260      MOV      (R1)+,DLVEC
1389 005674 012137 016134 01280      MOV      (R1)+,DL0TH
1390 005700 013737 016136 016140 01400      MOV      DLADR,DVCTXS
1391 005706 062737 000004 016140 01430      ADD      #4,DVCTXS
1392 005714 013737 016140 016142 01440      MOV      DVCTXS,DVCTXB
1393 005722 062737 000002 016142 01450      ADD      #2,DVCTXB
1394 005730 113737 016135 016160 01500      MOVVB   DL0TH+1,ONLIN
1395 005736 005777 010166 01750      TST      @DLADR
1396 005742 052737 100000 016126 01850      BIS      #DLP,DLFLAG      ;TRY TO ACCESS DVC,
1397 005750 012737 000300 007564 01900      MOV      #J00,DELAYT      ;SET DVC PRESENT FLAG
1398 005756 112777 000076 010156 01925      MOVVB   #'>,@DVCTXS      ;SET UP FOR DELAY
1399 005764 052777 000100 016146 01950      BIS      #100,@DVCTXS      ;TXMIT A ">" CHARACTER
1400 005772 104006 02050      DELAYR      ;SET DVC TX INTR ENABLE
1401 005774 005737 016132 02100      TST      DLVEC      ;WAIT FOR INTERRUPT
1402 006000 001433 02125      BEQ      20      ;IF ZERO NO INTERRUPT OCCURED
1403          02150      ;NO INTERRUPT = BRANCH
1404          02200      ;OTHERWISE DLVEC=ADDR THAT
1405 006002 052737 000200 016126 02300      BIS      #SEL,DLFLAG      ;DVC INTERRUPTED TO VIA INIMAP*
1406 006010 013741 016134 02350 351      MOV      DLOTH,=(R1)      ;SET SELECTED FLAG
1407 006014 013741 016132 02400      MOV      DLVEC,=(R1)
1408 006020 013741 016130 02450      MOV      DLADR,=(R1)      ;PUT NEW INFORMATION
1409 006024 013741 016126 02500      MOV      DLFLAG,=(R1)      ;INTO LINE TABLE
1410          02550
1411 006030 062701 000010 02600      ADD      #10,R1      ;JUMP POINTER TO NEXT LINE
1412 006034 020127 016770 02650 581      CMP      R1,#TABEND      ;ALL DONE ?
1413 006040 001307 02700      BNE      10      ;NO = DO NEXT LINE
1414 006042 162701 000010 02750 661      SUB      #10,R1      ;CHECK LAST ENTRY
1415 006046 005711 02800      TST      (R1)      ;FOR LINE PRESENT
1416 006050 100403 02850      BMI      70
1417 006052 012711 177777 02900      MOV      #-1,(R1)      ;IF NOT SET IT TO END
1418 006056 000771 02950      BR      60      ;OF TABLE
1419 006060 012737 000006 000004 03100 761      MOV      #6,MACHER      ;RESET TRAP CATCHER
1420 006066 000207      RTS      PC
1421 006070 052737 000020 016126 03200 261      BIS      #AB0,DLFLAG      ;SET ABORT FLAG
1422 006076 042737 000200 016126 03250      BIC      #SEL,DLFLAG      ;MAKE SURE LINE IS DESELECTED
1423 006104 004737 005124      JSR      PC,ERROR
1424 006110 012700 020055 03350      MOV      #ERR7,R0      ;SU ERROR MSG
1425 006114 104000 03400      TYPE      ;TYPE MSG ON CONSOLE
1426 006116 000734 03450      RR      30      ;FIX TABLE ENTRIES
1427          03500
1428 006120 062706 000004 03550 481      ADD      #4,SP      ;ERASE INTR FROM STACK
1429 006124 000743 03600      BR      50      ;GET NEXT LINE ENTRY
1430          03650
1431          03700
1432          03750 ;*****
1433          03800 ; CATCH REPLACES TRAP CATCHER FROM 100 TO 1000 .
      ;*****

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1434 006126 012700 001000 03850 CATCHI MOV #1000,R0 ;START AT 1000
1435 006132 005040 03900 ISI CLR =(R0) ;PUT HALI IN PC+2
1436 006134 010037 010100 03950 MOV R0,TEMP
1437 006140 013740 010100 04000 MOV TEMP,=(R0) ;PUT PC+2 IN PC
1438 006144 020027 000100 04050 CMP R0,#100 ;FIN?
1439 006150 002370 04100 BGE 15 ;NO = DO MORE
1440 006152 012737 003466 000060 04200 MOV #READKB,#TKV ;SU CONSOLE
1441 006160 000207 04250 RTS PC
1442 04300
1446 04500
  
```

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1451 05400
1452 05450 ;SBTTL EMT HANDLER
1453 05500 ;*****
1454 05550 ;THIS SECTION CONTAINS THE HANDLER AND MOSI ROUTINES ACCESSED
1455 05600 ;BY TRAPS THROUGH LOCATION 30.
1456 05650 ;*****
1457 05700
1458 006162 05750 EMTROS:
(4) 006162 011637 016112 MOV (SP),TEMP+12
1459 006166 162737 000002 016112 05800 SUB #2,TEMP+12 ;GET REAL PC
1460 006174 017737 007712 016110 05850 MOV @TEMP+12,TEMP+10 ;GET EMT INSTRUCTION
1461 006202 042737 104400 016110 05950 BIC #104400,TEMP+10 ;MASK INSTR BITS
1462 006210 062737 006230 016110 06000 ADD #EMTABL,TEMP+10 ;ADD TABLE ADDR
1463 006216 017737 007666 016112 06050 MOV @TEMP+10,TEMP+12
1464 006224 000177 007662 06100 JMP @TEMP+12
1465 06150
1466 06200 ;EVEN
1467 006230 006240 06250 EMTABL: ETYPE ;NSOLE TYPE ROUTINE
1468 006232 007212 06300 PRTLTB ;LINE TABLE PRINTER
1469 006234 007162 06350 INTRAP ;DL INTERRUPT CAICHER
1470 006236 007530 06400 DELAYM ;DELAY ROUTINE
  
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1472                                06650          ,SBTTL  I/O DRIVERS
1473                                06700          ;*****
1474                                06750          ;ETYPE CONSOLE OUTPUT ROUTINE, ENTER WITH ADDRESS OF
1475                                06800          ;      DATA IN R0, NULL TERMINATES OUTPUT.
1476                                06850          ;*****
1477                                06900
1478 006240 105710                    ETYPE!  TSTB  (R0)          ;CHECK FOR NULL
1479 006242 001406                    BEQ    38          ;EXIT ROUTINE
1480 006244 105737 177564            181    TSTB  0#TPS          ;CHECK FOR TRANSMIT READY
1481 006245 100375                    BPL    18          ;WAIT
1482 006252 112037 177566            MOVB  (R0)+,0#TPB      ;TRANSMIT CHARACTER
1483 006256 000770                    BR     ETYPE          ;GET NEXT CHAR
1484 006260 105737 177564            381    TSTB  0#TPS          ;WAIT TILL ALL DONE
1485 006264 100375                    BPL    38
1486 006266 000002                    RTI    38          ; EXIT...
1487                                07300
1488                                07350
1489                                07400          ;*****
1490                                07450          ;MECHO TERMINAL OUTPUT ROUTINE = SINGLE CHAR
1491                                07500          ;      CHAR IN R2
1492                                07550          ;      INTERRUPT DRIVEN ALL LINES
1493                                07600          ;*****
1494 006270 010237 006646            MECHO!  MOV    R2,MSAVE
1495 006274 010446                    MOV    R4,=(SP)
1496 006276 012702 020434            MOV    #STACK2,R2          ;INITIALIZE STACK2
1497 006302 000037 020670            CLR    ENDS              ;ZERO COUNT
1498 006306 012704 020574            MOV    #STACK3,R4          ;INITIALIZE STACK3
1499 006312 013722 016132            181    MOV    DLVEC,(R2)+    ;GET THE BASE VECTOR ADDR
1500 006316 013737 016144 016150    MOV    TXVEC,SAVE        ;SAVE THE VECTOR
1501 006324 062737 000002 016150    ADD    #2,SAVE          ;
1502 006332 013777 016150 007504    MOV    SAVE,RTXVEC        ;PUT ADDR+2 INTO ADDR
1503 006340 012777 000004 007502 001000  MOV    #IOT,MSAVE        ;PUT TRAP INTO ADDR+2
1504 006346 012737 000310 007564 001500  MOV    #200,DELAYT        ;WAIT FOR 200 MS.
1505 006354 113777 006646 007560 002000  MOVB  MSAVE,0DVCTXB      ;PUT CHAR IN BUF REG
1506 006362 012777 000100 007550    MOV    #100,0DVCTXS      ;ENABLE TX INTERRUPT
1507 006370 005237 020670            INC    ENDS              ;ADD 1 TO INTR PENDING COUNT
1508 006374 004737 002110            JSR    PC,LINMON
1509 006400 032737 000400 001364 004000  BIT    #LDONE,PCFLAG      ;END OF DVC LIST ?
1510 006406 001741                    BEQ    18              ;NO DO THIS LINE
1511 006410 042737 000400 001364 005000  BIC    #LDONE,PCFLAG
1512 006416 010237 006650            MOV    R2,MSAVE+2        ;SAVE STACK2 POINTER
1513 006422 104006                    DELAYR
1514 006424 005737 020670            TST    ENDS              ;ALL PENDING INTERRUPTS SHOULD
1515 006430 001004                    BNE    38              ;BE COUNTED DOWN BY TXTRAP,
1516 006432
1517 (4) 006432 012604                    1281   MOV    (SP)+,R4
1518 006434 013702 006646            MOV    MSAVE,R2
1519 006440 000207                    281   RTS    PC              ;EXIT...
1520 006442 010437 006652            381   MOV    R4,MSAVE+4        ;SAVE STACK 3 LIMIT
1521 006446 012704 020574            MOV    #STACK3,R4        ;RESET STACK3 POINTER
1522 006452 012702 020434            MOV    #STACK2,R2        ;RESET STACK2 POINTER
1523 006456 021224                    481   CMP    (R2),(R4)+        ;VECTOR MATCH ?
1524 006460 001404                    BEQ    58              ;YES = BRANCH
1525 006462 020437 006652            CMP    R4,MSAVE+4        ;STACK END ?
1526 006466 001403                    BEQ    68              ;YES = BRANCH
1527 006470 000772                    BR     48              ;COMPARE NEXT VECT.
    
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1527 006472 005012                    581   CLR    (R2)              ;ERASE VECT 001
1528 006474 005044                    CLR    =(R4)           ;ERASE VECT IN
1529 006476 062702 000002 000002    681   ADD    #2,R2            ;MOVE STACK POINTER
1530 006502 020237 006650            CMP    R2,MSAVE+2        ;END OF OUT STACK ?
1531 006506 001403 005000            BEQ    78              ;YES = GO GET ODD VECTOR
1532 006510 012704 020574            MOV    #STACK3,R4        ;RESET STACK3 POINTER
1533 006514 000760                    BR     48              ;KEEP SORTING
1534 006516 012702 020434            781   MOV    #STACK2,R2        ;RESET STACK2 POINTER
1535 006522 005712                    881   TST    (R2)            ;CHECK FOR NON ZERO
1536 006524 001003                    BNE    98
1537 006526 062702 000002 000002    981   ADD    #2,R2            ;GET VECT FROM TABLE
1538 006532 000773                    BR     88
1539 006534 012737 016174 006652    981   MOV    #LIN00+4,MSAVE+4
1540 006542 027712 000104 009950    1081  CMP    #MSAVE+4,(R2)    ;MATCH ?
1541 006546 001404                    BEQ    118              ;YES THIS LINE IS N,G.
1542 006550 062737 000010 006652    1081  ADD    #10,MSAVE+4        ;MOVE POINTER TO NEXT
1543 006556 000771                    BR     108
1544 006560 062737 000002 006652    1181  ADD    #2,MSAVE+4        ;GET LINE NUMBER
1545 006566 017737 000060 016160    MOV    #MSAVE+4,ONLIN
1546 006574 105037 016160            CLR    ONLIN
1547 006600 000337 016160            SWAB  ONLIN              ;ERASE JUNK BITS
1548 006604 004737 002432            JSR    PC,MTW            ;MOVE TABLE TO WORK AREAS
1549 006610 012737 020055 002034    MOV    #ER7,TSCPTR        ;POINT TO ERROR MESSAGE
1550 006616 112737 000377 002032    MOVR  #377,CFLAGS        ;ERROR NO.
1551 006624 052737 100000 002032    BIS    #MERR,CFLAGS      ;SET ERROR FLAG
1552                                10551          ;*****
1553                                10600          ;      ERROR 377
1554                                10650          ;*****
1555 006632 004737 005124                    JSR    PC,ERROR
1556 006636 042737 100377 002032 107500  BIC    #MERR,CFLAGS      ;ERASE ERROR DATA
1557 006644 000672                    BR     125              ;CLEAN HOUSE & EXIT
1558 006646 000000 000000 108500  MSAVE!  ,WORD  0,0,0
1559                                10855
1560                                10856
1561                                10857
1562                                10860          ;*****
1563                                10861          ;      SECHO SINGLE LINE ECHO ROUTINE
1564                                10862          ;      ENTER WITH CHAR IN R2
1565                                10863          ;      TRANSMITS TO DVC VIA I/O DRIVER WORK AREA
1566                                10864          ;*****
1567                                10865
1568                                10866
1569 SECHO!  MOV    TXVEC,SAVE
1570 006654 013737 016144 016150    MOV    ADD    #2,SAVE
1571 006662 062737 000002 016150    MOV    #STRAP,RTXVEC
1572 006670 012777 007510 007246    MOV    #PR14,MSAVE
1573 006676 012777 000200 007244    MOV    #100,DELAYT
1574 006704 012737 000144 007564    MOVB  R2,0DVCTXB
1575 006712 110277 007224            MOV    #100,0DVCTXS
1576 006716 012777 000100 007214    INC    ENDS
1577 006724 005237 020670            INC    ENDS
1578 006730 104006                    10877  DELAYR
1579 006732 005737 020670            TST    ENDS
1580 (9) 006736 001413                    BEQ    500028
1581 006740 012737 020055 002034    MOV    #ER7,TSCPTR
1582 006746 052737 100376 002032    BIS    #376,#MERR,CFLAGS
    
```

```

1434 006126 012700 001000 03850 CATCH: MOV #1000,R0 ;START AT 1000
1435 006132 005040 03900 181 CLR =(R0) ;PUT HALI IN PC+2
1436 006134 010037 016100 03950 MOV R0,TEMP
1437 006140 013740 016100 04000 MOV TEMP,=(R0) ;PUT PC+2 IN PC
1438 006144 020027 000100 04050 CMP R0,#100 ;FIN?
1439 006150 002370 04100 BGE 18 ;NO = DO MORE
1440 006152 012737 003466 000060 04200 MOV #READKB,0*TKV ;SU CONSULE
1441 006160 000207 04250 RTS PC
1442 04300
1446 04500
  
```

```

1451 05400
1452 05450 ;SBTTL EMT HANDLER
1453 05500 ;*****
1454 05550 ;THIS SECTION CONTAINS THE HANDLER AND MOSI ROUTINES ACCESSED
1455 05600 ;BY TRAPS THROUGH LOCATION 30.
1456 05650 ;*****
1457 05700
1458 006162 05750 EMTROS:
(4) 006162 011637 016112 MOV (SP),TEMP+12
1459 006166 162737 000002 016112 05800 SUB #2,TEMP+12 ;GET REAL PC
1460 006174 017737 007712 016110 05850 MOV @TEMP+12,TEMP+10 ;GET EMT INSTRUCTION
1461 006202 042737 104400 016110 05950 BIC #104400,TEMP+10 ;MASK INSTR BITS
1462 006210 062737 006230 016110 06000 ADD @EMTABL,TEMP+10 ;ADD TABLE ADDR
1463 006216 017737 007666 016112 06050 MOV @TEMP+10,TEMP+12
1464 006224 000177 007662 06100 JMP @TEMP+12
1465 06150
1466 06200 ;EVEN
1467 006230 006240 06250 EMTABL: ETYPE ;NSOLE TYPE ROUTINE
1468 006232 007212 06300 PRTLTB ;LINE TABLE PRINTER
1469 006234 007162 06350 INTRAP ;DL INTERRUPT CAICHER
1470 006236 007530 06400 DELAYM ;DELAY ROUTINE
  
```

```
1472          06650          ,SBTTL  I/O DRIVERS
1473          06700          ;*****
1474          06750          ;ECHO  CONSOLE OUTPUT ROUTINE, ENTER WITH ADDRESS OF
1475          06800          ;      DATA IN R0, NULL TERMINATES OUTPUT.
1476          06850          ;*****
1477          06900
1478          06950          ETYPE!  ISTRB  (R0)          ;CHECK FOR NULL
1479          07000          BEQ    30          ;EXIT ROUTINE
1480          07050          101    ISTRB  0#TPS          ;CHECK FOR TRANSMIT READY
1481          07100          BPL    10          ;WAIT
1482          07150          MOVB   (R0)+,0#TPB          ;TRANSMIT CHARACTER
1483          07200          BR     ETYPE          ;GET NEXT CHAR
1484          07250          301    TSTRB 0#TPS          ;WAIT TILL ALL DONE
1485          07275          BPL    30          ;
1486          07287          RTI    30          ; EXIT...
1487          07300
1488          07350
1489          07400          ;*****
1490          07450          ;MECHO  TERMINAL OUTPUT ROUTINE = SINGLE CHAR
1491          07500          ;      CHAR IN R2
1492          07550          ;      INTERRUPT DRIVEN ALL LINES
1493          07600          ;*****
1494          07650          MECHO!  MOV    R2,MSAVE          ;
1495          07700          MOV    R4,=(SP)          ;
1496          07750          MOV    #STACK2,R2          ;INITIALIZE STACK2
1497          07800          CLR    ENDS          ;ZERO COUNT
1498          07850          MOV    #STACK3,R4          ;INITIALIZE STACK3
1499          07900          101    MOV    DLVEC,(R2)+          ;GET THE BASE VECTOR ADDR
1500          07950          MOV    TXVEC,SAVE          ;SAVE THE VECTOR
1501          08000          ADD    #2,SAVE          ;
1502          08050          MOV    SAVE,0TXVEC          ;PUT ADDR+2 INTO ADDR
1503          08100          MOV    #IOT,0SAVE          ;PUT TRAP INTO ADDR+2
1504          08150          MOV    #200,,DELAYT          ;WAIT FOR 200 MS.
1505          08200          MOV    MSAVE,0DVCTXB          ;PUT CHAR IN BUF REG
1506          08250          MOV    #100,0DVCTXS          ;ENABLE TX INTERRUPT
1507          08300          INC    ENDS          ;ADD 1 TO INTR PENDING COUNT
1508          08350          JSR    PC,LINMON          ;
1509          08400          BIT    #LDONE,PCFLAG          ;END OF DVC LIST ?
1510          08450          BEQ    10          ;NO DO THIS LINE
1511          08500          BIC    #LDONE,PCFLAG          ;
1512          08550          MOV    R2,MSAVE+2          ;SAVE STACK2 POINTER
1513          08600          DELAYR          ;
1514          08650          TST    ENDS          ;ALL PENDING INTERRUPTS SHOULD
1515          08700          BNE    30          ;BE COUNTED DOWN BY TXTRAP,
1516          08750          1201
1517          08800          MOV    (SP)+,R4          ;
1518          08850          MOV    MSAVE,R2          ;
1519          08900          201    RTS    PC          ;EXIT...
1520          08950          301    MOV    R4,MSAVE+4          ;SAVE STACK 3 LIMIT
1521          09000          MOV    #STACK3,R4          ;RESET STACK3 POINTER
1522          09050          MOV    #STACK2,R2          ;RESET STACK2 POINTER
1523          09100          401    CMP    (R2),(R4)+          ;VECTOR MATCH ?
1524          09150          BEQ    50          ;YES = BRANCH
1525          09200          CMP    R4,MSAVE+4          ;STACK END ?
1526          09250          BEQ    60          ;YES = BRANCH
1527          09300          BR     40          ;COMPARE NEXT VECT,
```

```
1527          09300          501    CLR    (R2)          ;ERASE VECT 001
1528          09350          CLR    =(R4)          ;ERASE VECT IN
1529          09400          601    ADD    #2,R2          ;MOVE STACK POINTER
1530          09450          CMP    R2,MSAVE+2          ;END OF OUI STACK ?
1531          09500          BEQ    70          ;YES = GO GET ODD VECTOR
1532          09550          MOV    #STACK3,R4          ;RESET STACK3 POINTER
1533          09600          BR     40          ;KEEP SORTING
1534          09650          701    MOV    #STACK2,R2          ;RESET STACK2 POINTER
1535          09700          801    TST    (R2)          ;CHECK FOR NON ZERO
1536          09750          BNE    90          ;
1537          09800          ADD    #2,R2          ;
1538          09850          BR     80          ;
1539          09900          901    MOV    #LIN00+4,MSAVE+4          ;GET VECT FROM TABLE
1540          09950          1001    CMP    0MSAVE+4,(R2)          ;MATCH ?
1541          10000          BEQ    110          ;YES THIS LINE IS N,G,
1542          10050          ADD    #10,MSAVE+4          ;MOVE POINTER TO NEXT
1543          10100          BR     100          ;
1544          10150          1101    ADD    #2,MSAVE+4          ;GET LINE NUMBER
1545          10200          MOV    0MSAVE+4,ONLIN          ;
1546          10250          CLR    ONLIN          ;
1547          10300          SWAB  ONLIN          ;ERASE JUNK BITS
1548          10350          JSR    PC,MTW          ;MOVE TABLE TO WORK AREAS
1549          10400          MOV    #ER7,TSCPTR          ;POINT TO ERROR MESSAGE
1550          10450          MOV    #377,CFLAGS          ;ERROR NO.
1551          10500          BIS    #MERR,CFLAGS          ;SET ERROR FLAG
1552          10551          ;*****
1553          10600          ;      ERROR 377
1554          10650          ;*****
1555          10700          JSR    PC,ERROR          ;
1556          10750          BIC    #MERRN,CFLAGS          ;ERASE ERROR DATA
1557          10800          BR     120          ;CLEAN HOUSE & EXIT
1558          10850          MSAVE!  ,WORD  0,0,0
1559          10855
1560          10856
1561          10857
1562          10860          ;*****
1563          10861          ; SECHO  SINGLE LINE ECHO ROUTINE
1564          10862          ;      ENTER WITH CHAR IN R2
1565          10863          ;      TRANSMITS TO DVC VIA I/O DRIVER WORK AREA
1566          10864          ;*****
1567          10865
1568          10866
1569          10867          SECHO!  MOV    TXVEC,SAVE          ;
1570          10868          ADD    #2,SAVE          ;
1571          10869          MOV    #STRAP,0TXVEC          ;
1572          10870          MOV    #PRI4,0SAVE          ;
1573          10871          MOV    #100,,DELAYT          ;
1574          10872          MOV    R2,0DVCTXB          ;
1575          10873          MOV    #100,0DVCTXS          ;
1576          10874          INC    ENDS          ;
1577          10875          DELAYR          ;
1578          10876          TST    ENDS          ;
1579          10877          BEQ    500020          ;
1580          10878          MOV    #ER7,TSCPTR          ;
1581          10879          BIS    #376,#MERR,CFLAGS          ;
```

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1502 006754 004737 005124 JSR PC,ERROR
1503 006760 042737 100377 002032 BIC #MERRN,CFLAG6
1504 006766 500020:
1505 006766 013777 016150 007150 MOV SAVE,@TAVEC
1506 006774 005077 007150 CLR @SAVE
1507 007000 013737 016160 016150 MOV ONLIN,SAVE
1508 007006 500000:
(3) 007006 500010:
(2) 007006 000207 RTS PC
1509 10900
1510 10950
1511 11000 ;*****
1512 11050 ;MTYPE TERMINAL OUTPUT ROUTINE = LINE TABLE VERSION
1513 11100 ; ENTER WITH ADDR OF MSG IN R0
1514 11150
1515 11200 ;*****
1516 007010 112002 MTYPE: MOVB (R0)+,R2 ;GET CHAR TO PRINT
1517 007012 001403 BEQ 1$ ;EXIT IF NULL CHAR
1518 007014 004737 006270 JSR PC,MECHO
1519 007020 000773 11400 BR MTYPE ;GET NEXT CHAR
1600 007022 000207 11450 1$: RTS PC ;EXIT...
1601 11460
1602 11462
1603 11464
1604 11466 ;*****
1605 11468 ; READIO THIS ROUTINE MONITORS AN I/O READ OPERATION
1606 11470 ;*****
1607 11472
1608 11474
1609 007024 READIO:
1610 007024 042737 004000 001364 BIC #DATAIN,PCFLAG
1611 007032 010237 007564 MOV R2,DELAY
1612 007036 012777 000101 007064 MOV #101,@DLADR
1613 007044 104006 11400 DELAY
1614 11481 ; IF NO CHAR RECVD WITHIN (R2) MS SET ERROR FLAG
1615 007046 032737 004000 001364 BIT #DATAIN,PCFLAG
(9) 007054 001003 BNE 500020
1616 007056 052765 100000 000004 BIS #MERR,MFLAG6(R5)
1617 007064 500020:
1618 11487 ; IF ON LINE=0 CLEAR I/O MODE FLAGS
1619 007064 105737 016160 TSTB ONLIN
(9) 007070 001004 HNE 500030
1620 007072 042737 000003 001364 BIC #FLAG1,#FLAG2,PCFLAG
1621 007100 000402 BR 500040
(3) 007102 500030: CLR @DLADR
1622 007102 005077 007022 500040:
1623 007106 500000:
1624 007106 500010:
(3) 007106 000207 RTS PC
(2) 007106 11500
1625 11502
1626 11504
1627 11506 ;*****
1628 11508 ; TYPES TERMINAL OUTPUT ROUTINE SINGLE LINE
1629 11510 ;*****
1630

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1631 11512
1632 11514
1633 007110 112002 TYPES: MOVB (R0)+,R2 ;GET CHAR TO PRINT
1634 007112 001403 BEQ 1$ ;EXIT IF NULL
1635 007114 004737 006654 JSR PC,SECHO ;SEND THE MESSAGE
1636 007120 000773 11520 BR TYPES
1637 007122 000207 11524 1$: RTS PC ;EXIT
1638 11526
1639 11528
1640 11530 ;*****
1641 11532 ; READS THIS ROUTINE SETS UP DVC RECVR VECTOR AREAS
1642 11533 ; IF THE CURRENT LINE IS NOT LINE=00
1643 11534 ;*****
1644 11536
1645 11538
1646 007124 READS:
1647 007124 105737 016160 TSTB ONLIN
(9) 007130 001003 BNE 500020
1648 007132 004737 005600 JSR PC,SET10
1649 007136 000410 BR 500030
(3) 007140 500020:
1650 007140 010277 006766 MOV R2,@DLVEC
1651 007144 013702 016132 MOV DLVEC,R2
1652 007150 002702 000002 ADD #2,R2
1653 007154 012712 000200 MOV #PRI4,(R2)
1654 007160 500030:
1655 007160 500000:
(3) 007160 500010:
(2) 007160 000207 RTS PC
1656 11554
1657 11556

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1659          11595          ,SBTTL TRAP ROUTINES
1660          11600          ;*****
1661          11650          ;INTRAP: USED BY TABLE BUILD TO GET ADDRESS THAT A LINE
1662          11700          ; INTERRUPTS TO AN STORE IT IN = DLVEC,
1663          11750          ; TRANSMIT INTERRUPT USED, DLV HAS NO MAINT MODE,
1664          11800          ;*****
1665 007162 005077 006752          11875          INTRAP: CLR      @DVCTXS      ;DISABLE THE INTERRUPTS,
1666 007166 012637 016132          MOV      (SP)+,DLVEC
1667 007172 062706 000002          ADD      #2,SP      ;SP+2 ADJUST STACK POINTER
1668 007176 162737 000010          SUB      #10,DLVEC  ;ADJUST TO RCVR INTR ADDR
1669 007204 005037 007564          CLR      DELAYT     ;RESET TIMER
1670 007210 000002          RTI      ;GO BACK TO BUILD ROUTINE
1671          12100
1672          12150          ;*****
1673          12200          ;PRTLTB THIS ROUTINE TYPES THE LINE TABLE ON THE CONSOLE
1674          12250          ; DEVICE, DROPPED FLAGS ARE DECODED AND THE
1675          12300          ; APPROPRIATE INFORMATION IS PRINTED FOR EACH LINE,
1676          12350          ;*****
1677          12400
1678 007212          12450          PRTLTB:
(2) 007212 013746 016100          MOV      TEMP,=(SP)
1679 007216 013746 016102          MOV      TEMP+2,=(SP)
1680 007222 012702 016170          MOV      #LIN00,R2   ;POINTER TO ;START OF TABLE
1681 007226 012700 017676          MOV      #HEADR2,R0
1682 007232 104000          TYPE
1683 007234 005712          181      TST      (R2)   ;PRINT HEADER
1684 007236 100400          BMI      28         ;LINE PRESENT?
1685 007240 062702 000010          ADD      #10,R2     ;YES = BRANCH
1686 007244 021227 177777          CMP      (R2),#-1   ;MOVE POINTER TO NEXT ENTRY
1687 007250 001452          BEQ      108        ;END OF TABLE?
1688 007252 000770          BR       18         ;YES = BRANCH
1689 007254 012237 016100          281      MOV      (R2)+,TEMP  ;SAVE FLAG WORD
1690 007260 012246          MOV      (R2)+,=(SP)
1691 007262 012746 000004          MOV      #4,=(SP)
1692 007266 012746 017743          MOV      @DLAD,=(SP)
1693 007272 004737 007566          JSR      PC,02ASC   ;CONVERT ADDRESS TO ASCII
1694 007276 012246          MOV      (R2)+,=(SP)
1695 007300 012746 000003          MOV      #3,=(SP)
1696 007304 012746 017752          MOV      @DLV,=(SP)
1697 007310 004737 007566          JSR      PC,02ASC   ;CONVERT LINE NO,
1698 007314 012237 016102          MOV      (R2)+,TEMP+2
1699 007320 000337 016102          SWAB    TEMP+2
1700 007324 013746 016102          MOV      TEMP+2,=(SP)
1701 007330 012746 000002          MOV      #2,=(SP)
1702 007334 012746 017733          MOV      #LIN,=(SP)
1703 007340 004737 007566          JSR      PC,02ASC
1704 007344 012700 017733          MOV      #LIN,R0    ;TYPE FORMATTED LINE
1705 007350 104000          TYPE
1706 007352 105737 016100          381      TSTB    TEMP     ;SELECTED?
1707 007356 001403          BEQ      48         ;NO = BRANCH
1708 007360 012700 020117          MOV      #S1,R0    ;SEND STAR
1709 007364 000402          BR       58
1710 007366 012700 020105          481      MOV      #DR,R0  ;SEND DROPPED MSG
1711 007372 104000          581      TYPE
1712 007374 000723          14250    BR       68
1713 007376          14300    1081
  
```

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(2) 007376 012637 016102          MOV      (SP)+,TEMP+2
1714 007402 012637 016100          MOV      (SP)+,TEMP
1715 007406 012700 017051          MOV      #L3,R0
1716 007412 104000          14400    TYPE
1717 007414 000002          14450    RTI
1718          14500
          14550
  
```



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1720          14600 ;*****
1721          14650 ;TXTRAP THIS ROUTINE CATCHES THE INTERRUPTS FROM
1722          14700 ;      DLI1'S IN USE BY THE MECH0 ROUTINE .
1723          14750 ;*****
1727          14950
1728 007416 162716 000010 15000 TXTRAP: SUB #10,(SP) ;SUB 10 FROM UPDATED PC ON STACK
1729 007422 011614 15020 MOV (SP),(R4) ;PUT BASE VECTOR INTO STACK
1730 007424 012746 016174 15040 MOV #LIM00+4,-(SP) ;GET POINTER TO LINE TABLE VECTORS
1731 007430 027614 000000 15060 10: CMP 00(SP),(R4) ;COMPARE TABLE TO STACK
1732 007434 001403 15080 BEQ 20 ;SAME = BRANCH
1733 007436 062716 000010 15100 ADD #10,(SP) ;POINT TO NEXT TABLE ENTRY
1734 007442 000772 15120 BR 18 ;KEEP LOOKING FOR A MATCH
1735 007444 162716 000002 15130 20: SUB #2,(SP) ;ADDR OF DLADR NOW ON STACK
1736 007450 017637 000000 15140 MOV 00(SP),TEMP ;GET DLADR FROM TABLE
1737 007456 062737 000004 15160 ADD #4,TEMP ;POINT TO DVCTXS REGISTER
1738 007464 005077 006410 15180 CLR 0TEMP ;DISABLE INTERRUPTS
1739 007470 062706 000006 15200 ADD #6,SP ;SET STACK POINTER TO DRIVER PC
1740 007474 005337 020670 15220 DEC ENDS ;DECREMENT INTERRUPT PENDING COUNT
1741 007500 003002 15240 BGT 30 ;ABORT TIMEOUT IF ALL ACCOUNTED FOR
1742 007502 005037 007564 15260 CLR DELAYT ;RETURN TO I/O DRIVER
1743 007506 000002 15280 30: RTI
1744          15300
1745          15500
1746          15501
1747          15502
1748          15503
1749          15504 ;*****
1750          15505 ; STRAP SINGLE LINE TRANSMIT INTERRUPT CATCHER
1751          15506 ;      USED IN CONJUNCTION WITH SECH0 ROUTINE.
1752          15507 ;*****
1753          15508
1754          15509
1755 007510          STRAP:
1756 007510 005077 006424 CLR 0DVCTXS
1757 007514 005337 020670 DEC ENDS
1758 007520 005037 007564 CLR DELAYT
1759 007524 000002 15515 RTI
1760 007526          500000:
1761 (3) 007526          500010:
1762 (2) 007526 000207          RTS PC
1763          15517
1764          15518
1765          15519
1766          15550
1767          15750 ;*****
1768          15800 ;DELAYM DELAYS FOR X MILLI SECONDS, X STORED IN = DELAYT
1769          15850 ;
1770          15900 ;*****
1771          15950
1772          15955 DELAYM: TST DELAYT
1773 007530 005737 007564 15960 BEQ 30
1774 007534 001411 MOV R3,-(SP)
1775 007536 010346 (2) ;MEXIT
1776 007540 013703 007562 16050 10: MOV TIMER,R3 ;JMS LOOP TIME
1777 007544 005303 16100 20: DEC R3
1778 007546 001376 16150 BNE 20
    
```

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1779 007550 005337 007564 16200 DEC DELAYT
1780 007554 003371 16250 BGT 18
1781 007556 012603 (2) MOV (SP)+,R3 ;MEXIT
1782 007560 000002 16350 30: RTI
1783          16400
1784 007562 000554 16450 TIMER: ,WORD 554 ;SET FOR 11/35 = 11/40
1785          16500 ;SET TO 202 IF 11/03
1786          16550 ;      251 11/05 = 11/10
1787          16600 ;      314 11/15 = 11/20
1788          16650 ;      2127 11/45 RIPPOLAH
1789          16700 ;      1237 11/45 = 11/70
1790          16750 ;      755 11/45 00S
1791 007564 000000 16800 DELAYT: ,WORD 0 ;DELAY TIME BUFFER
    
```

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1793          16900          ,SBTTL CONVERSION ROUTINES
1794          16950
1795          17000
1796          17050 ;O2ASC1 OCTAL TO ASCII CONVERSION ROUTINE - ENTER WITH
1797          17100 ; NUMBER TO BE CONVERTED ON THE STACK, FOLLOWED
1798          17150 ; BY THE NUMBER OF DIGITS TO CONVERT, FOLLOWED
1799          17200 ; BY THE STORAGE ADDRESS FOR THE ASCII STRING.
1800          17250 ;*****
1801 007666 016637 000006 016150 17300 O2ASC1 MOV 6(SP),SAVE ;GET WORK COPY OF NUMBER
1802 007574 013746 016150 17350 MOV SAVE,-(SP)
1803 007600 066666 000006 000004 17400 ADD 6(SP),4(SP) ;ADD COUNT TO POINTER
1804 007606 005366 000004 17450 DEC 4(SP) ;DEC FOR END ADDR
1805 007612 042715 177770 281 BIC #177770,(SP) ;MASK OUT ALL BUT 3 BITS
1806 007616 052716 000000 17500 BIS #60,(SP) ;MAKE CHAR ASCII
1807 007622 111676 000004 17600 MOVB (SP),#4(SP) ;PUT ASCII CHAR IN BUFFER
1808 007626 005366 000004 17650 DEC 4(SP) ;INC POINTER
1809 007632 005366 000006 17700 DEC 6(SP) ;DEC DIGIT COUNT
1810 007636 001411 17750 BEQ 18 ;BRANCH IF DONE
1811 007640 006266 000010 17800 ASR 10(SP) ;
1812 007644 006266 000010 17850 ASR 10(SP) ;GET NEXT DIGIT
1813 007650 006266 000010 17900 ASR 10(SP) ;
1814 007654 016616 000010 17950 MOV 10(SP),(SP)
1815 007660 000754 18000 BF 28 ;DO NEXT CHAR FOR CONVERSION
1816 007662 016666 000002 000010 18050 181 MOV 2(SP),10(SP) ;PUT RETURN PC AT TOP OF JUNK
1817 007670 062706 000010 18100 ADD #10,SP ;POINT TO RETURN PC
1818 007674 000207 18150 RTS PC ;EXIT,..
1819          18200
1820          18250 ;*****
1821          18300 ;A2BIN CONVERTS INPUT ASCII TO BINARY NUMBER
1822          18350 ; ENTER WITH ADDR OF ASCII STRING ON STACK
1823          18400 ; FOLLOWED BY 4 DIGITS TO CONVERT
1824          18450 ; FOLLOWED BY ADDR OF WORD FOR ANSWER,
1825          18500 ;*****
1826          18700
1830 007676          A2BIN!
1831 007676 010046          MOV R0,-(SP)
1832 007700 005037 010004          CLR A2SAV
1833 007704 016600 000010          MOV 10(SP),R0
1834 007710          500028!
1835 007710 142710 000370          BICB #370,(R0)
1836 007714 005366 000006          DEC 6(SP)
1837 007720 152037 010004          BISB (R0)+,A2SAV
1838 007724 005766 000006          TST 6(SP)
1839 007730 001407          BEQ 500038
1840 007732 006337 010004          ASL A2SAV
1841 007736 006337 010004          ASL A2SAV
1842 007742 006337 010004          ASL A2SAV
1843 007746 000760 000010          BR 500028
1844 007750          500035!
1845 007750 016600 000004          MOV 4(SP),R0
1846 007754 013710 010004          MOV A2SAV,(R0)
1847 007760 011637 010004          MOV (SP),A2SAV
1848 007764 016600 000002          MOV 2(SP),R0
1849 007770 062706 000010          ADD #10,SP
1850 007774 010016          MOV R0,(SP)
1851 007776 013700 010004          MOV A2SAV,R0
    
```

```

1848 010002          500005!
1849 010002          500018!
1850 010002 000207          RTS PC
1851          19600
1852          19650 A2SAV! ,WORD 0 ;STORAGE AREA
1853          19750
1854          19850
1855          19950
1856          20050 ;*****
1857          20150 ;BIN2DA BINARY TO DECIMAL ASCII CONVERSION ROUTINE
1858          20250 ; ENTER WITH NUMBER TO CONVERT ON THE STACK,
1859          20350 ; FOLLOWED BY THE ADDRESS OF THE ASCII BUFFER,
1860          20450 ; 5 DIGITS WILL BE CONVERTED
1861          20550 ;*****
1862          20650
1863          20750
1864 010006 012700 010146          BIN2DA! MOV #TABDA,R0 ;INITIALIZE TABLE POINTER
1865 010012 112737 000005 010162          MOVB #5,DIGITS
1866 010020 005037 010160          CLR CNTDA
1867 010024 021066 000004          181 CMP (R0),4(SP)
1868 010030 003005          RGT 28
1869 010032 161066 000004          SUB (R0),4(SP)
1870 010036 105237 010160          INCB CNTDA
1871 010042 000770          BR 18
1872 010044 152737 000006 010160          281 BISR #60,CNTDA
1873 010052 105737 010161          TSTR FLAGDA
1874 010056 001012          BNE 48
1875 010060 123727 010160 000000          CMPB CNTDA,#0
1876 010066 001004          BNE 38
1877 010070 112737 000177 010160          MOVB #177,CNTDA
1878 010076 000402          BR 48
1879 010100 105137 010161          381 COMB FLAGDA
1880 010104 113776 010160 000002          481 MOVB CNTDA,#2(SP)
1881 010112 005266 000002          INC 2(SP)
1882 010116 062700 000002          ADD #2,R0
1883 010122 105037 010160          CLRB CNTDA
1884 010126 105337 010162          DECB DIGITS
1885 010132 001334          BNE 18
1886 010134 011666 000004          MOV (SP),4(SP)
1887 010140 062706 000004          ADD #4,SP
1888 010144 000207          RTS PC
1889          22950
1890 010146 023420 001750 000144          TABDA! ,WORD 10000,,1000,,1000,,10,,1
1891          23150
1892 010154 000012 000001          CNTDA! ,BYTE 0
1893 010160 000          FLAGDA! ,BYTE 0
1894 010162 000 000          DIGITS! ,BYTE 0,0
    
```

```
1898 00250 ,SBTTL LA36 OPTION TESTS
1899 00300 ;* * * * *
1900 00350 ;
1901 00400 ;TEST0 SECONDARY CHARACTER SET OPTION
1902 00450 ; NO MANUAL INTERVENTION REQUIRED
1903 00500 ;* * * * *
1904 00550 ;
1905 010164 012705 010352 00600 TEST01 MOV #T00BLK,R5 ;SET UP POINTER TO MODULE BLOCK
1906 010170 012700 010366 00650 MOV #T0,R0 ;SU TEST ID
1907 010174 004737 007010 JSR PC,MYTYPE
1908 010200 012700 010424 00750 T011 MOV #PRI,R0 ;SU PRIMARY MSG
1909 010204 004737 007010 JSR PC,MYTYPE
1910 010210 004737 010310 00850 JSR PC,CHARS ;SEND ALL CHARACTERS
1911 010214 012700 017730 MOV #L1,R0
1912 010220 004737 007010 JSR PC,MYTYPE
1913 010224 012700 010434 01000 MOV #SEC,R0 ;SU SECONDARY MSG,
1914 010230 004737 007010 JSR PC,MYTYPE
1915 010234 012702 000016 01100 MOV #SO,R2 ;SEND SO - SELECT APL SET
1916 010240 004737 006270 JSR PC,MECHO
1917 010244 004737 010310 JSR PC,CHARS ;SEND ALL CHARS AGAIN
1918 010250 012700 017730 MOV #L1,R0
1919 010254 004737 007010 JSR PC,MYTYPE
1920 010260 012702 000017 01350 MOV #SI,R2 ;SEND SI-SELECT ASCII
1921 010264 004737 006270 JSR PC,MECHO
1922 01450
1923 010270 052765 020000 000004 01500 BIS #TDONE,MFLAGS(R5) ;SET DONE AND ATTENTION FLAGS
1924 010276 012702 000012 01550 MOV #I2,R2 ;SU FOR LF
1925 010302 004737 006270 JSR PC,MECHO
1926 010306 000207 01650 RTS PC
1927 01700 ;*****
1928 01750 ; SUBROUTINE TO FILL OUTPUT LINE WITH ALL CHARACTERS
1929 01800 ;
1930 010310 013701 016146 01850 CHARS1 MOV WIDTH,R1 ;SAVE WIDTH
1931 010314 012702 000040 01900 MOV #40,R2 ;SAVE ;START CHAR
1932 010320 162701 000007 01950 SUB #7,R1 ;ADJUST WIDTH FOR PRI/SEC MSG
1933 010324 02000 251
1934 010324 004737 006270 JSR PC,MECHO
1935 02050 INC R2 ;NEXT CHAR
1936 010332 20237 010364 02100 CMP R2,RUB ;LAST CHAR?
1937 010336 001403 02150 BEQ 36 ;YES = EXIT
1938 010340 005301 02200 DEC R1 ;END OF PAPER?
1939 010342 001401 02250 BEQ 36 ;YES = EX11
1940 010344 000767 02300 BR 26 ;SEND NEXT
1941 010346 000207 02350 381 RTS PC
1942 02400 ,WORD 6 ;ITERATION COUNT
1943 010352 000000 02450 T00BLK1 ,WORD 0 ;CTLCNT
1944 010354 000000 02500 ,WORD 0 ;PASS COUNT
1945 010356 000000 02550 ,WORD 0 ;STATUS FLAGS
1946 010362 010200 02600 ,WORD 0 ;POINTER
1947 010364 000177 02650 ,WORD T01 ;RETURN PC
1948 02700 RUB1 ,WORD 177
1949 02750 ,NLIST BEX
1950 010366 042524 052123 030040 02800 T01 ,ASCIZ *TEST 0 APL/ASCII CHAR SET=<15><12><12>
1951 010424 051501 044503 026511 02850 PRI1 ,ASCIZ /ASCII=~/
1952 010434 050101 026514 026455 02900 SEC1 ,ASCIZ /APL=~/
, EVEN
02950
```

```
1954 03050
1955 03100 ,LIST BEX
1956 03150 ;* * * * *
1957 03200 ;
1958 03250 ;TEST1 SELECTIVE ADDRESSING OPTION
1959 03300 ; OPERATOR MUST COMPARE TYPEOUT AND SWITCHES ON THE M7737
1960 03350 ; TO VERIFY CORRECT OPERATION,
1961 03400 ; IF A GROUP OR UNIT SELECT CODE OF LESS THAN 20(H)
1962 03450 ; IS USED MODIFY LOCATION GSEL ACCORDINGLY.
1963 03500 ;
1964 03550 ;* * * * *
1965 03600 ;
1966 010444
1967 010444 012705 010722 03700 TEST11 MOV #T01BLK,R5 ;SET UP POINTER TO MODULE BLOCK
1968 010450 012700 010742 03750 MOV #T1,R0
1969 010454 004737 007010 JSR PC,MYTYPE
1970 010460 03850 T111 ;DESELECT ALL TERMINALS, THEN TRY TO
03900 ;PRINT ERROR MESSAGES,..,SHOULD NOT PRINT
03950 ;TRANSMIT A BAD SELECT SEQUENCE, THEN TRY TO
04000 ;PRINT ERROR MESSAGES,..,SHOULD NOT PRINT
04050 ;SELECT ALL TERMINALS, PRINT GP MESSAGE,
04100
1971 04150 MOV #T13,RPC(R5)
1972 010466 013701 010734 04200 MOV GSEL,R1
1973 010472 012737 011176 010740 04250 MOV #TABL1,T1TEMP+2
1974 010500 012737 000001 010736 04300 MOV #1,T1TEMP
04350 BR 500028
1975 04400 500038; INC T1TEMP
1976 010460 012765 010566 000010 500028; CMP T1TEMP,#8.
1977 010466 013701 010734 000010 500048 BGT 500048
1978 010472 012737 011176 010740 04450 MOV #L1,R0
1979 010500 012737 000001 010736 04500 JSR PC,MYTYPE
04550 MOV #T1TEMP+2,R0
04600 JSR PC,MYTYPE
04650 ADD #2,T1TEMP+2
04700 BR 500038
1980 010524 012700 017730 500048; ; TRANSMIT SELECT CODES TO ALL TERMINALS
1981 010530 004737 007010 04750 T121 ; FOLLOWED BY ASCII EQUIV OF CODE.
1982 010534 017700 000200 04800 MOV #T11,RPC(R5)
1983 010540 004737 007010 04850 T131 ;OUTPUT ALL CODES AND ACCII EQUIVELANTS
1984 010544 062737 000002 010740 500058; CMP R1,#200
1985 010552 000756 04900 BEQ 500068
1986 04950 MOV #SCODE,R0
1987 04700 T121 MOV #1,3(R0)
1988 010554 012765 010460 000010 04850 JSR PC,MYTYPE
1989 010562 013701 010734 04850 MOV #STX,R2
1990 010566 010737 000002 04850 JSR PC,MECHO
1991 010566 020127 000200 05250 ; NOW CONVERT SELECT CODE TO ASCII FOR OUTPUT
1992 010572 001420 04900 JSR PC,CON
1993 010574 012700 020421 04950 JSR PC,MYTYPE
1994 010600 110160 000003 05000 MOV #R1,3(R0)
1995 010604 004737 007010 JSR PC,MYTYPE
1996 010610 112702 000002 05050 MOV #R1,3(R0)
1997 010614 004737 006270 JSR PC,MECHO
1998 05250 ; NOW CONVERT SELECT CODE TO ASCII FOR OUTPUT
1999 010620 004737 010662 JSR PC,CON
2000 010624 004737 007010 JSR PC,MYTYPE
```

```
2001 010630 005201          INC      R1  
2002 010632 000755          BR       500050  
(3) 010634  
2003          055000          ; TURN ALL TERMINALS ON AND EXIT TEST  
2004 010634          500050 T16: ;  
(4) 010634 012765 010460 000010      MOV      #T11, R1(R5)  
2005 010642 052765 020000 000004      HIS      #TDONE, MFLAGS(R5)  
2006 010650 012700 020411      MOV      #ALLOFF, R0  
2007 010654 004737 007010      JSR      PC, MTYPE  
2008 010660          500000  
(3) 010660          500010  
(2) 010660 000207          RTS      PC  
2009          057500          ; THIS ROUTINE CONVERTS THE SELECT CODE  
2010          058000          ; TO ASCII FOR OUTPUT IN OCTALC MESSAGE,  
2011          010662          CON: ;  
2012 010662 005037 010736          CLR      T1TEMP  
2013 010666 110137 010736          MOV      R1, T1TEMP  
2014 010672 013746 010736          MOV      T1TEMP, -(SP)  
2015 010676 012746 000003          MOV      #3, -(SP)  
2016 010702 012746 011520          MOV      #OCTALC, -(SP)  
2017 010706 004737 007566          JSR      PC, O2ASC  
2018 010712 012700 011520          MOV      #OCTALC, R0  
2019 010716          500000  
(3) 010716          500010  
(2) 010716 000207          RTS      PC  
2020          063000  
2021 010720 000002          063500 T01BLK: ,WORD 2          ; ITERATION COUNT  
2022 010722 000000          064000          ,WORD 0          ; CILCNT  
2023 010724 000000          064500          ,WORD 0          ; PASS COUNT  
2024 010726 000000          065000          ,WORD 0          ; STATUS FLAGS  
2025 010730 000000          065500          ,WORD 0          ; POINTER  
2026 010732 010460          066000          ,WORD T11          ; RETURN PC  
2027 010734 000020          066500 GSEL: ,WORD 20          ; START OF SELECT CODES  
2028 010736 000000 000000          067000 T1TEMP: ,WORD 0,0  
2029          067500          ;  
2030 010742 005015 052012 051505          068000 #LIST BEX  
2031 011005 105 051122 051117          068500 #11 ,ASCIZ <15><12><12>/TEST 1 SELECTIVE ADDRESSING/<15><12><12>  
2032 011045 116 020117 042523          069000 E9: ,ASCIZ /ERROR = THIS SHOULD NOT PRINT #/  
2033 011100 042523 042514 052103          069500 E12: ,ASCIZ /NO SELECT CHARACTER SENT/<15><12>  
2034 011137 101 046114 052040          070000 GP: ,ASCIZ /SELECT CHARACTERS RECOGNIZED =/  
2035          011176          070500 E10: ,ASCIZ /ALL TERMINALS SHOULD BE OFF/<15><12>  
2036 011176 020416 011005 011137          071000 TABLE: ,WORD ALLOFF, E9, E10, NSELC, E9, E12, ALLOFF, GP  
2037          071500  
2038          072000  
2039          072500  
2040          073000  
2041          073500  
2042          074000 ;*****  
2043          074500 ; GETANS          THIS ROUTINE SETS UP AND READS THE ANSWERBACK  
2044          075000 ;          MESSAGE FROM THE TERMINAL UNDER TEST.  
2045          075500 ;*****  
2046          076000  
2047          076500  
2048 011216          GETANS:  
2049 011216 010337 011304          MOV      R3, 28  
2050 011222 012702 013144          MOV      #T220, R2
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2051 011226 004737 007124          JSR      PC, READS  
2052 011232 012702 000005          MOV      #ENO, R2  
2053 011236 004737 006654          JSR      PC, SECHO  
2054 011242          000000 15: ;  
(4) 011242 013702 011304          MOV      28, R2  
2055 011246 004737 007024          JSR      PC, READIO  
2056 011252 032765 100000 000004      BIT      #MERR, MFLAGS(R5)  
(9) 011260 001405          BEQ      500025  
2057 011262 042765 100000 000004      BIC      #MERR, MFLAGS(R5)  
2058 011270 105011          CLR      (R1)  
2059 011272 000403          BR       500036  
(3) 011274          500028: INCR   T2CNT1  
2060 011274 105237 013212          003500 BP     18  
2061 011300 000760          500036: 500008:  
2062 011302          500018:  
2063 011302          ;  
(3) 011302          ;  
(2) 011302 000207          ;  
2064 011304 000000          085000 25: ,WORD 0  
2065          085500  
2066          086000  
2067          086500 ;*****  
2068          087000 ; TYPANS          THIS ROUTINE PRINTS THE ANSWERBACK MESSAGE  
2069          087500 ;          IN OCTAL FORMATT, AND ASCII FORMATT,  
2070          088000 ;  
2071          088500 ;*****  
2072          089000  
2073 011306          TYPANS:  
2074          090000  
2075 011306 012700 020421          MOV      #SCODE, R0  
2076 011312 004737 007110          JSR      PC, TYPES  
2077 011316 012702 000002          MOV      #SIX, R2  
2078 011322 004737 006654          JSR      PC, SECHO  
2079 011326 012700 011472          MOV      #ANSHDR, R0  
2080 011332 004737 007110          JSR      PC, TYPES  
2081 011336 013746 013212          MOV      T2CNT1, -(SP)  
2082 011342          09337 16: ;  
(4) 011342 005046          CLR      -(SP)  
2083 011344 112116          MOV      (R1)+, (SP)  
2084 011346 012746 000003          MOV      #3, -(SP)  
2085 011352 012746 011520          MOV      #OCTALC, -(SP)  
2086 011356 004737 007566          JSR      PC, O2ASC  
2087 011362 012700 011520          MOV      #OCTALC, R0  
2088 011366 004737 007110          JSR      PC, TYPELS  
2089 011372 105337 013212          DECH   T2CNT1  
2090 011376 105737 013212          TSTB   T2CNT1  
(9) 011402 003402          BLE   500028  
2091 011404 000756          097000 BR     18  
2092 011406 000426          BR     500036  
(3) 011410          500028: ;  
2093 011410 012700 017730          MOV      #L1, R0  
2094 011414 004737 007110          JSR      PC, TYPES  
2095 011420 012700 000023          MOV      #19, R0  
2096 011424          500048: ;  
2097 011424 012702 000040          MOV      #40, R2  
2098 011430 004737 006654          JSR      PC, SECHO
```



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2162 011726          5000201
2163          12600
2164 011726          12650 T22: ;IF THE LINE HAS BEEN SIZED, BUT NO
2165          12700 ;SELECT CODE HAS BEEN MAPPED NOTIFY THE
2166          12750 ;OPERATOR .
2167          12800
2168 011726 123727 016134 000200
2169 (9) 011734 001015
2170          12900
2171          12950
2172          13000
2172 011736 012765 012530 000010
2173 011744 052765 100000 000004
2174 011752 105065 000004
2175 011756 012765 013356 000006
2176 011764 000207
2177 011766 000463
2178 (3) 011770
2178 011770 012701 013220
2179          13400
2180          13450
2181          13500
2182 011774 113737 016134 020424
2183 012002 012700 020421
2184 012006 004737 007110
2185 012012 012703 000310
2186 012016 105037 013212
2187 012022 004737 011216
2188          13850
2189          13900
2190 012026 105737 013212
2191 (9) 012032 001015
2192          14000
2193          14050
2194          14100
2194 012034 012765 012530 000010
2195 012042 052765 100000 000004
2196 012050 112765 000001 000004
2197 012056 012765 013356 000006
2198 012064 000424
2199 (3) 012066
2199 012066 012765 012140 000010
2200          14450
2201          14500
2202 012074 123727 013212 000024
2203 (9) 012102 003411
2204          14600
2205          14650
2206          14700
2206 012104 052765 100000 000004
2207 012112 112765 000002 000004
2208 012120 012765 013404 000006
2209 012126
2210 012126 012701 013220
2211 012132 004737 011306
2212 012136          5001381
          5001201
    
```

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2213 012136          5001001
2214 012136 000207          15150
2215 012140          15200 T23: RTS PC
2216          15250 ;SAVE COPIES OF THE ANSWERBACK AND IT'S LENGTH
2217          15300 ;THEN READ ANSWERBACKS 10 TIMES MORE,
2218          15350 ;VERIFY THEY ARE ALL THE SAME.
2219 012140 012765 012206 000010
2220 012146 113737 013212 013213
2221 012154 012701 013220
2222 012160 012700 020574
2223 012164
2224          15650
2225 012164 105737 013212
2226 (5) 012170 001404
2227 012172 112120
2228 012174 105337 013212
2229 012200 000771
2230 (3) 012202
2230 012202 105037 013210
2231          15950
2232          16000
2233          16050
2233 012206
2234 012206 012701 013220
2235 012212 105037 013212
2236          16200
2237          16250
2237 012216 012700 020421
2238 012222 012703 000310
2239 012226 004737 011216
2240 012232 105237 013210
2241          16500
2242          16550
2243          16600
2244          16650
2245 012236 105737 013212
2246 (9) 012242 001012
2247          16750
2248          16800
2249          16850
2249 012244 052765 100000 000004
2250 012252 012765 013356 000006
2251 012260 112765 000030 000004
2252 012266 000500
2253 (3) 012270
2253 012270 105011
2254          17150
2255 012272 123737 013212 013213
2256 (9) 012300 001410
2257 012302 012701 013220
2258 012306 004737 007110
2259          17350
2260          17400
2261          17450
2261 012312 052765 100000 000004
2262 012320 112765 000031 000004
2263 012326 012765 013356 000006
    
```

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2264 012334 000455 BR 50021$
(3) 012336 50020$1
2265 012336 012701 013220 MOV #T2BUF,R1
2266 012342 012700 020574 MOV #STACK3,R0
2267 17800 ;COMPARE MESSAGES FOR SAME DATA
2268 17850
2269 012346 005037 013214 CLR T2TEMP
2270 012352 113737 013212 013214 MOV# T2CNT1,T2TEMP
2271 012360 005037 013216 CLR T2TEMP+2
2272 012364 012737 000001 013216 MOV #1,T2TEMP+2
(5) 012372 000402 BR 50022$
(4) 012374 50023$1
(7) 012374 005237 013216 INC T2TEMP+2
(5) 012400 50022$1
(5) 012400 023737 013216 013214 CMP T2TEMP+2,T2TEMP
(7) 012400 003024 BGT 50024$
2273 012410 122021 CMPB (R0),(R1)+
(9) 012412 001421 BEQ 50025$
2274 18150 ;ERROR #32 INCONSISTANT ANSWERBACKS
2275 18200 ;*****
2276 18250
2277 012414 052765 100000 000004 BIS #MERR,MFLAGS(R5)
2278 012422 112765 000032 000004 MOV# #32,MFLAGS(R5)
2279 012430 012765 013575 000006 MOV #E21,POINT(R5)
2280 012436 012701 013220 MOV #T2BUF,R1
2281 012442 113737 013213 013212 MOV# T2CNT2,T2CNT1
2282 012450 013737 013214 013216 MOV T2TEMP,T2TEMP+2
2283 012456 50025$1
2284 012456 000746 BR 50023$
(3) 012460 50024$1
2285 18700 ;ECHO ANSWER TO TERMINAL IN ASCII AND
2286 18750 ;OCTAL FORMATS.
2287 012460 012701 013220 MOV #T2BUF,R1
2288 012464 004737 011306 JSR PC,TYPANS
2289 012470 50021$1
2290 012470 50017$1
2291 012470 032765 100000 000004 BIT #MERR,MFLAGS(R5)
(9) 012476 001401 BEQ 50026$
2292 012500 000207 19050 RTS PC
2293 012502 50026$1
2294 19150 ;CHECK FOR TEN ITERATIONS
2295 012502 123727 013210 000010 CMPB T2SAV1,#10
(9) 012510 001005 BNE 50027$
2296 012512 012755 012530 000010 MOV #T24,RPC(R5)
2297 012520 000207 19300 RTS PC
2298 012522 000402 BR 50030$
(3) 012524 50027$1
2299 012524 000137 012206 19400 JMP T23A
2300 012530 50030$1
2301 19500
2302 012530 T24: ;RESTORE POINTERS & TEST THE BROADCAST (BELL)
2303 19550 ;WON'T ACTIVATE THE AUTOANSWER,
2304 19600 ;
2305 012530 012701 013220 MOV #T2BUF,R1
2306 012534 105037 013212 CLRB T2CNT1
2307 012540 012702 000002 MOV #STX,R2

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2308 012544 004737 006654 JSR PC,SECHO
2309 012550 012700 020421 MOV #SCODE,R0
2310 012554 112737 000007 020424 MOV# #7,SCODE+3
2311 012562 004737 007110 JSR PC,TYPES
2312 012566 012703 000310 MOV #200,R3
2313 012572 004737 011216 JSR PC,GETANS
2314 012576 105737 013212 TSTB T2CNT1
(9) 012602 001415 BEQ 50031$
2315 20050 ;ERROR #4 RECVD ANSWERBACK FROM BROADCAST
2316 20100 ;*****
2317 20150
2318 012604 052765 100000 000004 BIS #MERR,MFLAGS(R5)
2319 012612 112765 000004 000004 MOV# #4,MFLAGS(R5)
2320 012620 012765 013632 000006 MOV #E22,POINT(R5)
2321 012626 012765 012636 000010 MOV #T25,RPC(R5)
2322 012634 000207 20400 RTS PC
2323 012636 50031$1
2324 20500 T25: ;IF IN MULTI LINE MODE SETUP NEXT LINE POINTERS
2325 20550 ;IF SINGLE LINE MODE TEST KEYBOARD STUFF.
2326 20600
2327 012636 032737 000040 001364 ;
(9) 012644 001424 BIT #MULTI,PCFLAG
2328 012646 004737 002110 BEQ 50032$
2329 012652 012765 012734 000010 JSR PC,LINMON
2330 012660 032737 000400 001364 MOV #T25A,RPC(R5)
(9) 012666 001406 BIT #LDONE,PCFLAG
2331 012670 042737 000400 001364 BEQ 50033$
2332 012676 052765 020000 000004 BIC #LDONE,PCFLAG
2333 012704 50033$1 BIS #TDONE,MFLAGS(R5)
2334 012704 012765 011546 000010 MOV #T21,RPC(R5)
2335 012712 000207 21000 RTS PC
2336 012714 000512 BR 50034$
(3) 012716 50032$1
2337 012716 113737 016134 020424 MOV# DLOTH,SCODE+3
2338 012724 012700 020421 MOV #SCODE,R0
2339 012730 004737 007110 JSR PC,TYPES
2340 012734 21100 T25A:
(4) 012734 012765 013034 000010 MOV #T26,RPC(R5)
2341 012742 012701 013220 MOV #T2BUF,R1
2342 012746 012702 000002 MOV #STX,R2
2343 012752 004737 006654 JSR PC,SECHO
2344 21300 ;SET UP TO TEST HERE-IS KEY SINGLE LINE ONLY
2345 21350 ;
2346 012756 012700 013252 MOV #HI,R0
2347 012762 004737 007110 JSR PC,TYPES
2348 012766 105037 013212 CLRB T2CNT1
2349 012772 012703 007640 MOV #4000,R3
2350 21600 ;READ ANSWERBACK
2351 012776 004737 011216 JSR PC,GETANS
2352 013002 105737 013212 TSTB T2CNT1
(9) 013006 001012 BNE 50035$
2353 21750 ;ERROR #5 NO ANSWERBACK FROM HERE-IS KEY
2354 21800 ;*****
2355 21850
2356 013010 052765 100000 000004 BIS #MERR,MFLAGS(R5)
2357 013016 112765 000005 000004 MOV# #5,MFLAGS(R5)

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```
2358 013024 012765 013475 000006          MOV      #E17,POINT(R5)
2359 013032 000207          RTS      PC
2360 013034          500350:
2361          22150
2362 013034          22200 T26: ;TEST CTL-E FUNCTION
2363          22250
2364          22300 ;SET UP TO TEST CTL-E FUNCTION
2365          22350
2366 013034 012765 011726 000010          J
2367 013042 012701 013220          MOV      #T22,RPC(R5)
2368 013046 012700 013305          MOV      #T2BUF,R1
2369 013052 012703 007640          MOV      #CE,R0
2370 013056 105037 013212          MOV      #4000,,R3
2371 013062 004737 007110          CLR     T2CNT1
2372 013066 004737 011216          JSR     PC,TYPE5
2373 013072 105737 013212          JSR     PC,GETANS
2374 (9) 013076 001013          TST     T2CNT1
2375          BNE     500360
2376          ; ERROR #6 NO ANSWERBACK FROM CTL-E KEY
2377          ;*****
2378          J
2379 013100 052765 100000 000004          BIS     #MERR,MFLAGS(R5)
2380 013106 112765 000006 000004          MOV     #6,MFLAGS(R5)
2381 013114 012765 013540 000006          MOV     #E18,POINT(R5)
2382 013122 000207          23100 RTS     PC
2383 013124 000406          BR      500370
2384 (3) 013126          500360:
2385          BIS     #TDONE,MFLAGS(R5)
2386 013126 052765 020000 000004          MOV     #T22,RPC(R5)
2387 013134 012765 011726 000010          500370:
2388          500340:
2389          RTS     PC
2390          ;*****
2391          ;THIS ROUTINE IS THE KEYBOARD INTERRUPT HANDLER
2392          ;FOR TESTS #1 AND #2
2393          ;*****
2394 013144 117721 002766          T220: MOV     #0VCRXB,(R1)+ ;STORE CHAR IN POINTER
2395 013150 052737 004000 001364          BIS     #DATAIN,PCFLAG ;SET DATA-IN FLAG
2396 013156 012777 000101 002744          MOV     #101,0DLADR ;REENABLE THE RCVR
2397 013164 005037 007564          CLR     DELAYT ;ABORT THE TIMEOUT
2398 013170 000002          RTI
2399          23950
2400          500000:
2401          500100:
2402          RTS     PC
2403          ;*****
2404          ;DSABL LSB
2405 013174 000 003          24050 T02BLK: .BYTE 0,3 ;ITERATION COUNTS
2406 013176 000000          24100 .WORD 0 ;CTLCNT
2407 013200 000000          24150 .WORD 0 ;PASS COUNT
2408 013202 000000          24200 .WORD 0 ;STATUS FLAGS
2409 013204 000000          24250 .WORD 0 ;POINTER
2410 013206 011546          24300 .WORD T21 ;RETURN PC
2411 013210 000000          T2SAV1: .WORD 0
2412 013212 000          T2CNT1: .BYTE 0
2413 013213 000          T2CNT2: .BYTE 0
```

```
2410 013214 000000 000000          24600 T2TEMP: .WORD 0,0
2411 013220 000000 000000 000000          24605 T2BUF: .WORD 0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0 ; BUFFER FOR ANSWERBACK
2412 013226 000000 000000 000000          013234 000000 000000 000000
2413 013242 000000 000000 000000          013250 000000
2414          24650
2415          24700 ;LIST BEX
2416 013252 005015 042504 051120 051120          H1: .ASCIZ <15><12>/DEPRESS HERE IS -- KEY/<15><12>
2417 013305 015 052012 050131 24800          CE: .ASCIZ <15><12>/TYPE COUNT/E/<15><12>
2418          24850
2419 013326 005015 052012 051505 24900          T2: .ASCIZ <15><12><12>/TEST 2 AUTO ANSWER/<15><12>
2420 013356 047516 040440 051516 24950          E14: .ASCIZ /NO ANSWERBACK RECVD/<15><12>
2421 013404 047101 041123 041501 25000          E15: .ASCIZ /ANSBACK MSG OVER 20 CHARS/<15><12>
2422 013440 047101 041123 041501 25050          E16: .ASCIZ /ANSBACK MSG CONTAINED NULL/<15><12>
2423 013475 110 051105 020105 25100          E17: .ASCIZ /HERE IS KEY DIDN'T TXMIT ANSBACK/<15><12>
2424 013540 052103 026514 020105 25150          E18: .ASCIZ /CTL-E DIDN'T TXMIT ANSBACK/<15><12>
2425 013575 101 051516 042527 25200          E21: .ASCIZ /ANSWERBACKS DIDN'T COMPARE/<15><12>
2426 013632 047101 041123 041501 25250          E22: .ASCIZ /ANSBACK RECVD FROM BROADCAST SELECT/<15><12>
2427          25300
2428          25350 ;LIST BEX
```


2540	014621	040	033040	31050		,ASCII / 6/
2541	014624	020040	067	31100		,ASCII / 7/
2542	014627	040	034040	31150		,ASCII / 8/
2543	014632	027070	065	31200		,ASCII / 8,5/
2544	014635	040	030401	31250	HDR5B1	,ASCII / 11/
2545	014640	030440	062	31300		,ASCII / 12/
2546	014643	040	032001	31350		,ASCII / 14/
2547	014646	020040	063	31400	HDR5A1	,ASCII / 3/
2548	014651			31450	HDR5E1	
2549	014652			31500		,EVEN
2550				31550		,LIST BEX

2552				31650		,DSABL L5B
2553				31700		
2554				31750		*****
2555				31800		;
2556				31850	TEST4	HORIZONTAL TAB OPTION
2557				31900		IF USING OTHER THAN 132 COL PAPER CHANGE LUC "WIDTH"
2558				31950		TO APPROPRIATE VALUE, SEE WJ COMMAND
2559				32000		;
2560				32050		*****
2561				32100		;
2562	014652	012705	015246	32150	TEST41	MOV #T04BLK,R5 ;SET UP POINTER TO MODULE BLOCK
2563	014656	012700	015320	32200		MOV #T4,R0
2564				32250		; PRINT TEST HEADR
2565	014662	004737	007010		JSR	PC,MTYPE
2566	014666	012737	015270	015264	T411	MOV #TABL4,T4SAV2
2567	014674	012765	014702	000010		MOV #T42,RPC(R5)
2568	014702	012702	000033		T421	MOV #ESC,R2
2569	014706	013737	010146	015262		MOV #WIDTH,T4SAV1
2570				32550		; SEND ESC-2 TO RESET ALL TABS.
2571	014714	004737	006270		JSR	PC,MECHO
2572	014720	012702	000062		MOV	#2,R2
2573	014724	004737	006270		JSR	PC,MECHO
2574	014730	117737	000330	015316	MOV#	#T4SAV2,IAB ;GET TAB COUNT FROM IABL4
2575	014736	005237	015264		INC	T4SAV2
2576	014742	105077	000316		CLRB	#T4SAV2 ;INITIALIZE COUNT TO ZIP
2577	014746	013701	015316		MOV	IAB,R1
2578	014752	012700	017730		MOV	#L1,R0
2579				33000		; SEND CR/LF
2580	014756	004737	007010		JSR	PC,MTYPE
2581	014762	163737	015316	015262	361	SUB TAB,T4SAV1 ;SU TAB COUNT PER LINE
2582	014770	002434		33150	BLT	60 ;FINISHED THIS LINE - BRANCH
2583				33200		;
2584	014772	005301		33250	481	DEC R1 ;TYPE (TAB=1) PERIODS
2585	014774	001405		33300	BEQ	58 ;AS A FORMATT FOR
2586	014776	012702	000056	33350	MOV	#*,R2 ;COMPARISON
2587	015002	004737	006270		JSR	PC,MECHO ;PRINT PERIOD
2588	015006	000771		33450	BR	45
2589	015010	012702	000033	33500	561	MOV #ESC,R2 ;SET TAB
2590				33550		;
2591	015014	004737	006270		JSR	PC,MECHO ; SEND ESC-1 TO SET A TAB
2592	015020	012702	000061	33650	MOV	#1,R2
2593	015024	004737	006270		JSR	PC,MECHO
2594				33750		; SEND A BACKSPACE
2595	015030	012702	000010	33800	MOV	#10,R2 ;
2596	015034	004737	006270		JSR	PC,MECHO
2597				33900		;
2598	015040	012702	000126	33950		; PRINT A V FOR REFERENCE
2599	015044	004737	006270		MOV	#V,R2
2600	015050	105277	000210	34050	JSR	PC,MECHO
2601	015054	013701	015316	34100	INCB	#T4SAV2 ;INCB TAB COUNT
2602	015060	000740		34150	MOV	IAB,R1 ;GET TAB POS AGAIN
2603				34200	BR	38 ;FORMAT NEXT SECTION
2604				34250		;
2605	015062	012737	000003	015266	681	MOV #3,COUNT ;DO 3 LINES OF TABS
2606	015070	117737	000170	015316	781	MOV# #T4SAV2,TAB ;GET TAB COUNT
2607	015076	001440		34400	BEQ	118 ;#0? = BRANCH OUT

2608	015100	005237	015264		34450	INC	T4SAV2	
2609	015104	012700	017730		34500	MOV	#L1,R0	
2610					34550			; SEND A CR/LF
2611	015110	004737	007010			JSR	PC,MYTYPE	
2612	015114	012702	000011		34650	MOV	#11,R2	
2613					34700			; SEND A HORIZ-TAB
2614	015120	004737	006270			JSR	PC,MECHO	
2615	015124	117737	000134	015260	34800	MOVB	#T4SAV2,T4SAV	;GET FILL COUNT IABS/2
2616	015132	012702	000006		34850	MOV	#ACK,R2	
2617					34900			; SEND FILL CHARACTERS
2618	015136	004737	006270			JSR	PC,MECHO	
2619	015142	005337	015260		35000	DEC	T4SAV	
2620	015146	001371			35050	BNE	98	
2621	015150	012702	000130		35100	MOV	#*X,R2	
2622					35150			; PRINT AN X UNDER EACH V
2623	015154	004737	006270			JSR	PC,MECHO	
2624	015160	005337	015316		35250	DEC	TAB	;DEC TAB COUNT
2625	015164	001353			35300	BNE	88	;MORE TABS = BRANCH
2626	015166	005337	015264		35350	DEC	T4SAV2	;FIX POINTER
2627	015172	005337	015266		35400	DEC	COUNT	;DO 3 LINES
2628	015176	001334			35450	BNE	78	;NOT DONE = BRANCH
2629	015200	012700	017051		35500	MOV	#L3,R0	
2630	015204	004737	007010		35550	JSR	PC,MYTYPE	
2631	015210	062737	000002	015264	35600	ADD	#2,T4SAV2	;GET NEXT TABLE ENTRY
2632	015216	023727	015264	015316	35650	CMP	T4SAV2,#TAB-1	;END OF TABLE?
2633	015224	001226			35700	BNE	T42	;NO = DO NEXT SET
2634					35750			
2635	015226	052765	020000	000004	36000	BIS	#TDONE,MFLAGS(R5)	;SET ATTENTION AND DONE FLAGS
2636	015234	012765	014666	000010	36000	MOV	#T41,RPC(R5)	
2637	015242	000207			36050	RTS	PC	
2638					36100			
2639	015244	000	004		36150		,BYTE 0,4	;ITERATION COUNTS
2640	015246	000000			36200	T#4BLK;	,WORD 0	;CFLCNT
2641	015250	000000			36250		,WORD 0	;PASS COUNT
2642	015252	000000			36300		,WORD 0	;STATUS FLAGS
2643	015254	000000			36350		,WORD 0	;POINTER
2644	015256	014702			36400		,WORD T42	;RETURN PC
2645					36450			
2646	015260	000000			36500	T4SAV1;	,WORD 0	;STORAGE
2647	015262	000000			36550	T4SAV1;	,WORD 0	
2648	015264	000000			36600	T4SAV2;	,WORD 0	
2649					36650			
2650					36700			
2651	015266	000002			36750	COUNT1;	,WORD 2	
2652	015270	004	000	002	36800	TABL4i	,BYTE 4,0,2	;TAB, TAB COUNT, FILL COUNT
2653	015273	010	000	004	36850		,BYTE 8,0,4	
2654	015276	011	000	005	36900		,BYTE 9,0,5	;TABLE FOR TEST 4
2655	015301	020	000	010	36950		,BYTE 10,0,8	
2656	015304	022	000	012	37000		,BYTE 18,0,10	
2657	015307	040	000	021	37050		,BYTE 32,0,17	
2658	015312	100	000	041	37100		,BYTE 64,0,33,0	
2659	015315	000			37150	TAB1	,WORD 0	
2660	015316	000000			37200	T41	,ASCIZ <15><12><12>/TEST 4 HORIZONTAL TAB/<15><12>	
	015326	020124	020064	047510				
	015334	044522	047532	052116				

	015342	046101	052040	041101				
	015350	005015	000					
2661	015354				37250	,EVEN		

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2663          37350
2664          37400
2665          37450
2666          37500
2667          37550
2668          37600
2669          37650
2670          37700
2671          37750
2672 015354 012700 016040 37800
2673 015360 012705 016014 37850
2674 015364 004737 007010
2675 015370 032737 000040 001364 37950
2676 015376 001046 38000
2677 015400 012700 014635 38050
2678 015404 112037 014564 38100
2679 015410 112037 014565 38150
2680 015414 112037 014566 38200
2681          38250
2682 015420 012700 014443 38300
2683 015424 004737 007110
2684 015430 012700 014557 38400
2685 015434 004737 007110
2686 015440 012702 015500 38500
2687 015444 004737 007124 38550
2688 015450 012702 015230 38600
2689 015454 004737 007024 38650
2690 015460 032737 004000 001364 38700
2691 015466 001770 38750
2692 015470 042737 004000 001364 38800
2693 015476 000406 38850
2694          38900
2695          38950
2696 015500 005037 007564 39000
2697 015504 052737 004000 001364 39050
2698 015512 000002 39100
2699          39150
2700          39200
2701          39250
2702 015514 012737 000002 015266 39300
2703 015522 012765 015514 000010 39350
2704 015530 012737 000001 016032 39400
2705 015536 005037 016036 39450
2706 015542 012737 000014 016034 39500
2707 015550 012702 000033 39550
2708          39600
2709 015554 004737 006270 39700
2710 015560 012702 000064 39750
2711 015564 004737 006270
2712 015570 013701 016032 39800
2713 015574 012702 000012 39850
2714          39900
2715 015600 004737 006270
2716 015604 005301 40000
2717 015606 001372 40050
2718 015610 012702 000033 40100
    
```

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;*****
;TEST5 VERTICAL TAB OPTION
; SINGLE LINE TEST REQUIRES OPERATOR INTERVENTION
;*****
,ENABL LSB
TEST5: MOV #T5,R0 ;SU TEST HEADEK
MOV #T05BLK,R5 ;SET UP POINTER TO MODULE BLOCK
JSR PC,MYTYPE
BIT #MULTI,PCFLAG ;MULTI LINE MODE?
BNE 48 ;YES - BRANCH OVER INTERVENTION
MOV #HDR5B,R0 ;SET UP INSTRUCTIONS
MOVB (R0)+,HDR4+5
MOVB (R0)+,HDR4+6
MOVB (R0)+,HDR4+7
; TYPE INSTRUCTIONS
T51: MOV #HDR3,R0
JSR PC,TYPE5
MOV #HDR4,R0
JSR PC,TYPE5
MOV #J3,R2 ;SU FOR INTERRUPT TO J3
JSR PC,READ5 ;INITIALIZE VECTOR AREA
MOV #I5000,R2 ;ALLOW 15 SEC.
JSR PC,READ10
BIT #DATAIN,PCFLAG
BEQ 28
BIC #DATAIN,PCFLAG
BR 48
;***** THIS SECTION HANDLES RECVH INTERRUPTS*****
;
38: CLR DELAYT ;ABORT THE TIMEOUT
BIS #DATAIN,PCFLAG ; FLAG RECEIVED CHAR.
100: RTI
;*****
48: MOV #2,COUNT
MOV #48,RPC(R5) ;SET RETURN TO 48
MOV #1,LINES
CLR TABS
MOV #12,MAX
MOV #ESC,R2 ;RESET ALL TABS
; ESC=4 RESETS THE TABS,
JSR PC,MECHO
MOV #*4,R2
JSR PC,MECHO
58: MOV #LINES,R1 ;GET LINE COUNT
68: MOV #12,R2
; SEND LINE FEED,
JSR PC,MECHO
DEC R1
BNE 68
MOV #ESC,R2 ;SET TAB
    
```

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2719 015614 004737 006270 JSR PC,MECHO
2720 015620 012702 000063 40200 MOV #*3,R2
2721 015624 004737 006270 ; ESC=3 SETS A TAB LOCATION,
2722 015628 004737 006270 JSR PC,MECHO
2723 015630 012700 014427 40350 MOV #DAS,P0
2724 015634 004737 007010 JSR PC,MYTYPE
2725 015640 005237 016032 40500 INC LINES
2726 015644 023737 016032 016034 40550 CMP LINES,MAX ;11 TABS YET?
2727 015652 001346 006050 40600 BNE 58 ;NO - BRANCH
2728 015654 012737 000001 016032 40650 MOV #1,LINES ;RESET LINE COUNT
2729 015662 012737 000001 016030 40700 MOV #1,T5SAV1 ;FILL COUNT
2730 015670 012702 000013 40750 MOV #13,R2
2731 015674 004737 006270 ; SEND A VERT-TAB COMMAND,
2732 015678 012702 000006 40800 JSR PC,MECHO
2733 015684 004737 006270 JSR PC,MECHO
2734 015690 012702 000006 40900 MOV #ACK,R2
2735 015694 004737 006270 ; SEND A FILL CHARACTER,
2736 015700 004737 006270 JSR PC,MECHO
2737 015710 005337 016030 41050 DEC T5SAV1
2738 015714 001371 41100 BNE 98
2739 41150
2740 41200 ; CONVERT NO. OF LINES FOR OUTPUT MSG.
2741 41250
2742 015716 013746 016032 41300 MOV LINES,=(SP)
2743 015722 012746 016071 41400 MOV #T52,=(SP)
2744 015726 004737 010006 41450 JSR PC,BIN2DA
2745 015732 012700 016071 41500 MOV #T52,R0
2746 015736 004737 007010 41550 JSR PC,MYTYPE
2747 015742 012700 014427 41600 MOV #DAS,R0 ;SU LINE OF DASHES
2748 015746 004737 007010 JSR PC,MYTYPE
2749 015752 005237 016032 41700 INC LINES ;NEW LINE COUNT
2750 015756 013737 016032 016034 41750 MOV LINES,T5SAV1 ;FILL COUNT =#LINES
2751 015764 023737 016032 016034 41800 CMP LINES,MAX ;11 TABS DONE?
2752 015772 001336 41850 BNE 88 ;NO - CONTINUE
2753 015774 005337 015266 41900 DEC COUNT ;DU 2 PAGES TOTAL
2754 016000 001325 41950 BNE 78 ;RE=00 PAGE
2755 016002 052765 020000 000004 42000 BIS #TDONE,#FLAGS(R5) ;SET ATTENTION & DONE FLAGS
2756 42050
2757 016010 000207 42100 RTS PC
2758 42150
2759 42200
2760 016012 000 002 42250 ;*****
;*****
T05BLK: .BYTE 0,2 ;ITERATION COUNTS
;WORD 0 ;CTL CNT
;WORD 0 ;PASS COUNT
;WORD 0 ;STATUS FLAGS
;WORD 0 ;POINTER
;WORD T51 ;RETURN PC
2766 42550
2767 016026 000000 42600 T5SAV1 ,WORD 0
2768 016030 000000 42650 T5SAV11 ,WORD 0
2769 42700
2770 016032 000000 42750 LINES! ,WORD 0
2771 016034 000000 42800 MAX! ,WORD 0
2772 016036 000000 42850 TABS! ,WORD 0
2773 016040 005015 052012 051505 42900 T5! ,ASCII <15><12><12>/TEST 5 VERTICAL TAB/<15><12>
016046 020124 020405 042526
    
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	016054	052122	041511	046101			
	016062	052040	041101	045015			
	016070	000					
2774	016071	060	030060	030060	42950	T521	,ASCIZ /00000/
	016076	000					
2775		016100			43000		,EVEN
2776					43050		,DBABL LSB

2778					43150		,SBTTL STORAGE & CONSTANTS
2779					43200		;
2780					43250		PROGRAM STORAGE, CONSTANTS, AND VARIABLES
2781					43300		;
2782					43350		,EVEN
2783	016100	000000	000000	000000	43400	TEMP:	,WORD 0,0,0,0,0 ;TEMPORARY WORK AREA
	016106	000000	000000	000000			
2784	016114	000004			43450	INBUF:	,BLKW 4 ;INPUT BUFFER
2785	016124	177570			43500	SWR:	177570 ;SWIICH REGISTER POINTER
2786					43550		;MAY BE CHANGED TO 176
2787					43600		;***** I/O DRIVER WORK AREA *****
2788					43650		
2789	016126	000000			43700	DLFLAG:	,WORD 0 ;LINE FLAG WORD
2790	016130	000000			43750	DLADRI:	,WORD 0 ;LINE ADDR WORD
2791	016132	000000			43800	DLVEC:	,WORD 0 ;LINE VECTOR WORD
2792	016134	000000			43850	DL0TH:	,WORD 0 ;LINE "OTHER" WORD
2793	016136	000000			43900	DVCRXB:	,WORD 0 ;RECEIVER DATA BUFFER
2794	016140	000000			43950	DVCTXS:	,WORD 0 ;TRANSMI STATUS REGISTER
2795	016142	000000			44000	DVCTXB:	,WORD 0 ;TRANSMIT DATA BUFFER
2796	016144	000000			44050	TXVEC:	,WORD 0 ;TRANSMIT INTERRUPT VECTOR
2797					44100		
2798					44150		;***** GENERAL USE *****
2799	016146	000204			44200	WIDTH:	,WORD 132
2800	016150	000000			44250	SAVE:	,WORD 0
2801	016152	000000			44300	NEXT:	,WORD 0 ;NEXT TEST NO.
2802	016154	000000			44350	INTEST:	,WORD 0 ;CURRENT TEST
2803	016156	000000			44400	TESTAD:	,WORD 0 ;CURRENT TEST PC
2804	016160	000000			44450	ONLIN:	,WORD 0 ;CURRENT LINE UNDER TEST
2805	016162	000000			44500	NXTLIN:	,WORD 0 ;NEXT LINE TO TEST
2806	016164	016114			44550	PTR:	INBUF ;INPUT BUFFER POINTER
2807	016166	177			44600	DEL:	,BYTE 177
2808	016167	014			44650	FF:	,BYTE 14
2809					44750		,EVEN
2810					44800		

Address	Word	Value	Label	Description
2812		44900		
2813		44950		
2814		45000		LINE INTERFACE TABLE
2815		45050		
2816	016170	000000	177560	000000 45100 LIN001 ,WORD 0,177560,0,0 ;CONSOLE INTERFACE
2817	016200	000000	175610	000000 45150 LIN011 ,WORD 0,175610,0,400 ;OL11-C,0,E LINES
2818	016210	000000	175620	000000 45200 LIN021 ,WORD 0,175620,0,1000
2819	016220	000000	175630	000000 45250 LIN031 ,WORD 0,175630,0,1400
2820	016230	000000	175640	000000 45300 LIN041 ,WORD 0,175640,0,2000
2821	016240	000000	176500	000000 45350 LIN051 ,WORD 0,176500,0,2400
2822	016250	000000	176510	000000 45400 LIN061 ,WORD 0,176510,0,3000
2823	016260	000000	176520	000000 45450 LIN071 ,WORD 0,176520,0,3400 ;FIRST WORD I FLAGS
2824	016270	000000	176530	000000 45500 LIN101 ,WORD 0,176530,0,4000 ;BIT 15 = DVC PRESENT
2825	016300	000000	175650	000000 45550 LIN111 ,WORD 0,175650,0,4400 ;BIT 7 = DVC SELECTED
2826	016310	000000	175660	000000 45600 LIN121 ,WORD 0,175660,0,5000 ;BIT 4 = ABORT FLAG
2827	016320	000000	175670	000000 45650 LIN131 ,WORD 0,175670,0,5400 ;BIT 3 THRU
2828	016330	000000	175700	000000 45700 LIN141 ,WORD 0,175700,0,6000 ;BIT 0 = ERROR COUNT
2829	016340	000000	175710	000000 45750 LIN151 ,WORD 0,175710,0,6400
2830	016350	000000	175720	000000 45800 LIN161 ,WORD 0,175720,0,7000
2831	016360	000000	175730	000000 45850 LIN171 ,WORD 0,175730,0,7400 ;THIRD WORD WILL CONTAIN
2832	016370	000000	175740	000000 45900 LIN201 ,WORD 0,175740,0,10000 ;THE DEVICES INTERRUPT
2833	016400	000000	176540	000000 45950 LIN211 ,WORD 0,176540,0,10400 ;VECTOR (SUPPLIED BY PROGRAM)
2834	016410	000000	176550	000000 46000 LIN221 ,WORD 0,176550,0,11000
2835				
2836	016420	000000	176560	000000 46050 LIN231 ,WORD 0,176560,0,11400
2837	016430	000000	176570	000000 46100 LIN241 ,WORD 0,176570,0,12000 ;WORD FOUR I
2838	016440	000000	176600	000000 46200 LIN251 ,WORD 0,176600,0,12400 ;BITS 7 THRU 0
2839	016450	000000	176610	000000 46250 LIN261 ,WORD 0,176610,0,13000 ;MILL BE SET TO
2840	016460	000000	175750	000000 46300 LIN271 ,WORD 0,175750,0,13400 ;UNIQUE SELECT CODE
2841	016470	000000	175760	000000 46350 LIN301 ,WORD 0,175760,0,14000
2842	016500	000000	175770	000000 46400 LIN311 ,WORD 0,175770,0,14400 ;BITS 13 THRU 8

Address	Word	Value	Label	Description
2843	016500	014400	176000	000000 46450 LIN321 ,WORD 0,176000,0,15000 ;PRIMARY LINE NO.
2844	016510	000000	176010	000000 46500 LIN331 ,WORD 0,176010,0,15400
2845	016520	000000	176020	000000 46550 LIN341 ,WORD 0,176020,0,16000
2846	016530	000000	176030	000000 46600 LIN351 ,WORD 0,176030,0,16400
2847	016540	000000	176040	000000 46650 LIN361 ,WORD 0,176040,0,17000
2848	016550	000000	176620	000000 46700 LIN371 ,WORD 0,176620,0,17400
2849	016560	000000	176630	000000 46750 LIN401 ,WORD 0,176630,0,20000
2850	016570	000000	176640	000000 46800 LIN411 ,WORD 0,176640,0,20400
2851	016580	000000	176650	000000 46850 LIN421 ,WORD 0,176650,0,21000
2852	016590	000000	176660	000000 46900 LIN431 ,WORD 0,176660,0,21400
2853	016600	000000	176670	000000 46950 LIN441 ,WORD 0,176670,0,22000
2854	016610	000000	176680	000000 47000 LIN451 ,WORD 0,176680,0,22400
2855	016620	000000	176690	000000 47050 LIN461 ,WORD 0,176690,0,23000
2856	016630	000000	176700	000000 47100 LIN471 ,WORD 0,176700,0,23400
2857	016640	000000	176100	000000 47150 LIN501 ,WORD 0,176100,0,24000
2858	016650	000000	176110	000000 47200 LIN511 ,WORD 0,176110,0,24400
2859	016660	000000	176120	000000 47250 LIN521 ,WORD 0,176120,0,25000
2860	016670	000000	176130	000000 47300 LIN531 ,WORD 0,176130,0,25400
2861	016680	000000	176140	000000 47350 LIN541 ,WORD 0,176140,0,26000
2862	016690	000000	176150	000000 47400 LIN551 ,WORD 0,176150,0,26400
2863	016700	000000	176160	000000 47450 LIN561 ,WORD 0,176160,0,27000
2864	016710	000000	176170	000000 47500 LIN571 ,WORD 0,176170,0,27400
2865	016720	177777		47550 TABEND1 ,WORD =1
2866				47600

PRLTB = 007212	SWR = 016124	TKV = 000060	T4SAV1 = 015262	SNSK0 = 000100
PRTTBL = 104062	SWRTST = 003006	TKVEC = 000060	T4SAV2 = 015264	SNSK1 = 000110
PR0 = 000000	SWTEST = 000172	TPB = 177566	T41 = 014666	SNSK10 = 000110
PR1 = 000100	SW0 = 000001	TPS = 177564	T42 = 014702	SNSK11 = 000110
PR2 = 000100	SW00 = 000001	TPVEC = 000064	T5 = 016044	SNSK12 = 000110
PR3 = 000140	SW01 = 000002	TPAVE = 000034	T5SAV = 016026	SNSK2 = 000110
PR4 = 000200	SW02 = 000004	TRRYEC = 000014	T5SAV1 = 016030	SNSK3 = 000210
PR5 = 000240	SW03 = 000010	TSCCNT = 002036	T51 = 015420	SNSK4 = 000110
PR6 = 000300	SW04 = 000020	TSCPTR = 002034	T52 = 016071	SNSK5 = 000110
PR7 = 000340	SW05 = 000040	TSTHL = 002040	UPDATE = 002660	SNSK6 = 000110
PS = 177776	SW06 = 000100	TSTCIL = 001446	WIDTH = 016146	SNSK7 = 000110
PSM = 177776	SW07 = 000200	TSTMON = 050000	SGNLE = 177777	SSAVLE = 177777
PTR = 016164	SW08 = 000400	TXTNAP = 007416	SRFLG = 000400	SSSK0 = 050023
PWRVEC = 000024	SW09 = 001000	IXVEC = 016144	SRAND = 000310	SSVPC = 000204
RDSAV = 003776	SW1 = 000002	IXPANS = 011306	SRBAD = 000401	SSWR = 160000
RDY = 020123	SW10 = 002000	IYPE = 104000	SRBLA = 000170	STAGLE = 177777
READIO = 007024	SW11 = 004000	IYPES = 007110	SRCAS = 000150	STAGNU = 050040
READKB = 003466	SW12 = 010000	T0 = 010366	SRDEC = 000220	STEMP = 000300
READS = 007124	SW13 = 020000	T00BLK = 010352	SRDO = 000340	STN = 000001
READY = 000200	SW14 = 040000	T01 = 010200	SRFAL = 000405	STSK0 = 050034
REPORT = 005436	SW15 = 100000	T01BLK = 010722	SRGOD = 000400	STSK1 = 050037
RESTR1 = 001372	SW2 = 000004	T02MLK = 013176	SRIF = 000110	STSK10 = 050023
RESVEC = 000010	SW3 = 000010	T03BLK = 014342	SRINC = 000210	STSK11 = 050024
RPC = 000010	SW4 = 000020	T04BLK = 015246	SRLOO = 000200	STSK2 = 050024
RUB = 010364	SW5 = 000040	T05BLK = 016014	SRNAM = 000160	STSK3 = 050023
R6 = 0000005	SW6 = 000100	T0742 = 010742	SRNO = 000403	STSK4 = 050025
R7 = 0000007	SW7 = 000200	T1TEMP = 010736	SROR = 000320	STSK5 = 050022
SAVE = 016150	SW8 = 000400	T11 = 010460	SRPTH = 000300	STSK6 = 050017
SCODE = 020421	SW9 = 001000	T12 = 010554	SRSEL = 000140	STSK7 = 050021
SEC = 010434	S1 = 020117	T13 = 010566	SRTHE = 000330	SSARGC = 000000
SECHO = 006654	TAB = 015316	T16 = 010634	SRTRU = 000404	SSBYTE = 000402
SEL = 000200	TABDA = 010146	T2 = 013326	SRUNT = 000130	SSCASE = 000000
SELERR = 005100	TABEND = 016770	T2BUF = 013220	SRWHI = 000120	SSDST = 000000
SEQ = 000100	TABL1 = 011176	T2CNT1 = 013212	SRYES = 000402	SSLOC = 000402
SETIO = 005000	TABL4 = 015270	T2CNT2 = 013213	SHD = 000003	SSERFL = 000000
SI = 000017	TAB5 = 016036	T2SAV1 = 013210	SIFLEV = 177777	SSFLAG = 000001
S0 = 000016	TBITVE = 000014	T2TEMP = 013214	SISK0 = 000001	SSFNUM = 000000
SOH = 000001	IDONE = 020000	T21 = 011546	SISK1 = 000001	SSLOC = 013076
SSWR = 000176	TEMP = 016100	T22 = 011726	SISK10 = 000001	SSLOCN = 000000
STACK = 001100	TEMPF = 005002	T220 = 013144	SISK11 = 000001	SSREG = 177777
STACK2 = 020434	TEMPT = 005004	T23 = 012140	SISK2 = 000001	SSRETU = 000000
STACK3 = 020574	TESTAD = 016156	T23A = 012206	SISK3 = 000001	SSRTN1 = 050000
STANT = 001102	TESTNO = 001366	T24 = 012530	SISK4 = 000001	SSRTN2 = 050001
START1 = 001172	TEST0 = 010164	T25 = 012636	SISK5 = 000001	SSSNC = 000000
START2 = 001230	TEST1 = 010444	T25A = 012734	SISK6 = 000001	SSIGSV = 000000
STKLMT = 177774	TEST2 = 011526	T26 = 013034	SISK7 = 000001	SSITGS1 = 000000
STRAP = 007510	TEST3 = 013700	T3 = 014376	SLOCTA = 177777	SSITGS2 = 000000
STX = 000002	TEST4 = 014652	T3SAV = 014354	SLSTCN = 177777	SSCO = 000000
STXEST = 002056	TEST5 = 015354	T3SAV1 = 014356	SLSTIN = 000000	SSSTAG = 050000
SW = 020215	TIMER = 007562	T3SAV2 = 014360	SLSTST = 177777	= 020072
SWCTL = 000020	TKB = 177562	T4 = 015320	SLSTIA = 000000	
SWLINE = 000174	TKS = 177560	T4SAV = 015200	SNESIL = 177777	

, ABS, 020672 000

ERRORS DETECTED: 0
CZLAFAB,CZLAFAB,LST=SYSMAC,SMU/ML,SPNAC,SMU/ML,CZLAFAB,P11
RUN=TIME: 120 101 .6 SECONDS
RUN=TIME RATIO: 00033/222=272.2
CORE USED: 19K (37 PAGES)