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IDENTIFICATION

PRODUCT CODE: AC-F836E-MC
PRODUCT NAME: CXCBBE0 CB11 DISTRIBUTE MOD
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

CBB IS A BKMOD THAT EXERCISES UP TO "NM" CB11 DISTRIBUTOR MODULES HAVING CONTIGUOUS UNIBUS ADDRESSES. THE MAXIMUM VALUE IF "NM" IS THE SAME AS THE MAXIMUM NO. OF DISTRIBUTOR MODULES ALLOWED FOR A SINGLE CB11. NON-CONTIGUOUS GROUPS OF DISTRIBUTOR MODULES MAY BE EXERCISED BY CONFIGURATING THE CBB MODULE FOR EACH GROUP. THE MODULE SIMPLY TESTS THE ABILITY TO SET AND CLEAR ALL BITS IN ALL AVAILABLE DISTRIBUTE REGISTERS SELECTED FOR TEST. IF ANY BIT FAILS TO SET OR CLEAR PROPERLY THE ERROR IS REPORTED ON THE CONSOLE TTY.

2. REQUIREMENTS:

HARDWARE: A CB11 INTERFACE WITH AT LEAST ONE DISTRIBUTE MODULE.

STORAGE:: CBB REQUIRES:
1. DECIMAL WORDS: 126
2. OCTAL WORDS: 0176
3. OCTAL BYTES: 374

3. PASS DEFINITION

ONE PASS OF THE CBB MODULE RESULTS IN 100. ITERATIONS OF THE BASIC TEST SEQUENCE WHICH CLEARS AND SETS ALL DISTRIBUTOR REGISTERS SELECTED FOR TEST

4. EXECUTION TIME

CBB RUNNING ALONE ON A PDP 11/05 SYSTEM WITH ONE DISTRIBUTE MODULE SELECTED TAKES APPROXIMATELY 10 SECONDS.

5. CONFIGURATION PARAMETERS

DEFAULT PARAMETERS:

DVADR: 0, VECTOR: 0, BR1: 0, BR2: 0, DVECNT: 1, SR1: 0

REQUIRED PARAMETERS:

FOR EACH COPY OF CBB CONFIGURED THE
USER MUST SPECIFY THE FOLLOWING PARAMETERS:

DEVADR: EQUAL TO THE FIRST ADDRESS IN A
CONTIGUOUS GROUP
SR1: NUMBER OF MODULES (SEE "OPERATION OPTIONS")

6. DEVICE OPTION SETUP:

NONE REQUIRED

7. MODULE OPERATION

TEST SEQUENCE.

- A. SET UP THE PASS COUNTER FOR 100. ITERATIONS
- B. GET THE CONTENTS OF SR1 TO FIND OUT HOW
MANY REGISTERS TO TEST
- C. CLEAR & DISTRIBUTE REGISTER
- D. COUNT IT
- E. REPORT ANY ERROR
- F. GENERATE NEXT ADDRESS
- G. REPEAT B-F UNTIL ALL REGISTERS TESTED
FOR ALL ZEROES
- H. REPEAT B-G BUT SET ALL REGISTERS TO
ALL ONES AND TEST
- I. COUNT ONE ITERATION
- J. IF NOT 100. REPEAT B-I
- K. REPEAT END PAS, RESTART AT A.

8. OPERATION OPTIONS

- A. USER CAN MODIFY "ADDR" AND "SR1" TO
SELECT ANY GROUP OF DISTRIBUTE MODULES

SR1: THE NUMBER (OCTAL) OF DISTRIBUTE MODULES TO BE TESTED
(MUST BE CONTIGUOUS). THIS NUMBER MUST BE A STRAIGHT OCTAL NUMBER,
NOT A BIT MAP AS IS NORMALLY USED IN DEVCNT.
DEVCNT WAS NOT USED BECAUSE IT CAN HOLD A MAXIMUM
OF 16 DEVICES AND CBB CAN RUN UP TO 256 DEVICES.
IF SR1 IS LEFT AT ZERO OR OTHERWISE IMPROPERLY SET
UP, THE SYSTEM WILL EVENTUALLY CRASH.

9. NON-STANDARD PRINTOUTS

NONE-ALL PRINTOUTS HAVE THE STANDARD
DEC/K11 FORMATS.

JCR11 DISTRIBUTE MODULE - DEC/X11 EXERCISER MODULE

000000- BKMOD <CBBE> 100-34
000000- MODULE 40020, CBBE 100-34
; .TITLE CBBE DEC/X11 SYSTEM EXERCISER MODULE
; DDXCOM VERSION 6 23-MAY-78
***** BIN *****
BEGIN:
MODNAM: -ASCII / ;MODULE NAME
XFLAG: -BYTE OPEN ;USED TO KEEP TRACK OF WBOFF USAGE
ADDR: +0 ;1ST DEVICE ADDR
VECTOR: +0 ;1ST DEVICE VECTOR.
BR1: -BYTE PRTV+0 ;1ST BR LEVEL.
BR2: -BYTE PRTV+0 ;2ND BR LEVEL.
DVID1: + ;DEVICE INDICATOR 1.
SR1: OPEN ;SWITCH REGISTER 1.
SR2: OPEN ;SWITCH REGISTER 2.
SR3: OPEN ;SWITCH REGISTER 3.
SR4: OPEN ;SWITCH REGISTER 4.

STAT: 40020 ;STATUS WORD.
START: ;MODULE START ADDR.
SPOINT: MODSP ;MODULE STACK POINTER.
PASCNT: 0 ;PASS COUNTER.
ICONT: 100. ;# OF ITERATIONS PER PASS=100.
LOC: 0 ;LOC TO COUNT ITERATIONS.
SOFCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS.
HRDCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS.
SOPPAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS.
HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS.
SVSCHK: 0 ;# OF SYS ERRORS ACCUMULATED.
RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED.
CONFIG: 0 ;RESERVED FOR MONITOR USE.
RES2: 0 ;RESERVED FOR MONITOR USE.
SVR0: OPEN ;LOC TO SAVE R0.
SVR1: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVR3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVR5: OPEN ;LOC TO SAVE R5.
SVR6: OPEN ;LOC TO SAVE R6.
CSRA: OPEN ;ADDR OF CURRENT CSR.
SADDR: ;ADDR OF GOOD DATA, OR
ACSR: OPEN ;CONTENTS OF MONITOR USE
WASADR: ;ADDR OF BAD DATA, OR
ASTAT: OPEN ;STATUS REG CONTENTS.
ERRTYP: ;TYPE OF ERROR.
ASB: ;EXPECTED DATA.
ACTUAL: ;ACTUAL DATA.
RSTART: RESTRT ;RESTART ADDRESS AFTER END OF PASS
WDTO: OPEN ;WORDS TO MEMORY PER ITERATION
WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
INTR: OPEN ;# OF INTERRUPTS PER ITERATION

000122- 000034 IDNUM: 34 ;MODULE IDENTIFICATION NUMBER=34
000040 ;MODULE STACK STARTS HERE.
;REPT SPSIZ
;NLIST
;WORD 0
;LIST
;ENDR
000224- MODSP:

223 000224- 012767 000002 177664 START: MOV #2, WDFR ;2 WORDS FROM MEM/ITERATION
224 000223- 016700 177550 RESTRT: MOV ADDR, R0 ;GET THE 1ST REG. ADDRESS
225 000239- 016700 177554 MOV SR1, R2 ;GET THE DISTR. MODULE COUNT
226 000244- 005010 1S: CLR R2 ;MAKE IT A REG. COUNT
227 000244- 005010 BNE (R0) ;BR IF IT DIDN'T CLEAR ALL BITS
228 000246- 001024 4S: DEC R2 ;COUNT ONE REG.
229 000250- 005302 BRZ (R0)+ ;BR IF ALL REGS CLEARED
230 000254- 005720 IS ;GENERATE NXT ADDRESS
231 000254- 000772 BR ;GO CLEAR THE NEXT ONE
232 000264- 016700 177522 3S: MOV ADDR, R0 ;GET THE 1ST REG. ADDRESS
233 000264- 016702 177526 MOV SR1, R2 ;GET THE DISTR. MODULE COUNT
234 000270- 006302 ASL R2 ;MAKE IT A REG. COUNT
235 000277- 005110 8S: COM (R0) ;SET ALL ONES
236 000277- 022710 CMP #177777, (R0) ;DID ALL BITS SET ?
237 000280- 005302 BNE (R0) ;BR IF NOT
238 000280- 001402 7S: DEC R2 ;COUNT ONE REG.
239 000300- 005302 BRZ (R0)+ ;BR IF ALL REGS SET
240 000300- 005720 ;GENERATE THE NXT ADDRESS
241 000310- 000770 2S: BR #5 ;GO DO ANOTHER ONE
242 000312- 104413 000000- ENDDITS, BEGIN ;SIGNAL END OF ITERATION.
243 000316- 000745 ;MONITOR SHALL TEST END OF PASS
244 000320- 010067 177554 5S: BR RESTRT ;BR TIL 100. TIMES THROUGH
245 000324- 011067 177552 MOV (R0), ACSR ;SAVE THE ADDRESS OF THE REG.
246 000330- 012767 000025 177550 MOV #25, ERRTYP ;SAVE THE CONTENTS
247 000336- 104405 000000- 000000 ;BIT STUCK IN REG.
248 000344- 000741 HRDERS, BEGIN, NULL ;FAILED TO CLEAR ALL BITS
249 000346- 010067 177526 6S: BR #4 ;*****
250 000350- 011067 177524 MOV (R0), ACSR ;GO TRY THE NEXT GUY
251 000356- 012767 000025 177522 MOV #25, ERRTYP ;SAVE THE ADDRESS OF THE REG
252 000364- 104405 000000- 000000 ;BIT STUCK IN REG
253 000372- 000743 HRDERS, BEGIN, NULL ;FAILED TO SET ALL BITS
254 000372- 000743 BR #7 ;*****
255 000001 ;GO TRY THE NEXT GUY
- END

CBBE DEC/X11 SYSTEM EXERCISER MODULR NACY11 30A(1052) 12-OCT-78 16:23 PAGE 10
XCBBEO.P11 12-OCT-78 11:54 CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0008

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

XCBBEO, XCBBEO/SQL/CRF:SYM=DDXCOM, XCBBEO
RUN-TIME: 1 1 .2 SECONDS
RUN-TIME RATIO: 8/2=3.3
CORE USED: 7K (13 PAGES)