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IDENTIFICATION

PRODUCT CODE: AC-E9238-MC
PRODUCT NAME: CXADCBO ADV11 MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1.0 ABSTRACT

ADC IS AN IOMOD THAT EXERCISES THE ADV11 ANOLOG MODULE. THIS MODULE REQUIRES ONLY AN ANOLOG GROUND ON CHANNEL ZERO IN ORDER TO BE RUN, HOWEVER, WITH SPECIAL SETUP, MORE OPTIONS CAN BE CHOSEN. ONE OPTION IS THE USE OF THE KVV11 (REAL TIME CLOCK) DEVICE. THIS OPTION ALLOWS EXERCISING THE ADV11 ASYNCHRONOUSLY WITH THE LSI-11 CPU, THAT IS, ADV11 CONVERSIONS WILL BE STARTED AT RANDOM TIMES TO ALLOW FOR MAXIMUM BUS NOISE DURING THE CONVERSION. IF THIS OPTION IS SELECTED, YOU MUST DESELECT KWE FROM A DEC/X11 RUN. WITH NORMAL OPERATION, RMS NOISE AND PEAK NOISE ARE SAMPLED ON CHANNEL ZERO AND COMPARED AGAINST A LIMIT. WITH THE SECOND OPERATION OPTION, MORE CHANNELS MAY BE SPECIFIED TO RUN THE NOISE TESTS ON. THE THIRD OPTION ALLOWS FOR SAMPLING OF ONE TO ALL THE CHANNELS OF THE ADV11. A CHECK IS MADE TO SEE THAT THE INPUT VOLTAGE REMAINS STABLE WITHIN AN ALLOWED TOLERANCE. LOCATIONS WITHIN THIS MODULE ARE PROVIDED TO CHANGE ANY LIMIT, OR TO FORCE TYPEOUT OF ANY VALUE.

2.0 REQUIREMENTS

HARDWARE: ONE ADV11
ONE BERG CONNECTOR (OPTIONAL)
ONE KVV11 (OPTIONAL)

STORAGE:: ADC REQUIRES:
1. DECIMAL WORDS: 933
2. OCTAL WORDS: 1645
3. OCTAL BYTES: 3512

3.0 PASS DEFINITION

ONE PASS OF THE ADC MODULE CONSISTS OF GENERATING 8244(DECIMAL) INTERRUPTS (CONVERSIONS).

4.0 EXECUTION TIME

ONE PASS OF THE ADC MODULE RUNNING ALONE TAKES APPROXIMATELY ONE MINUTE.

5.0 CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

DEVADR: 170400, VECTOR: 400, BR1: 6

DEVCNT: 1, SR1: 0

REQUIRED PARAMETERS:

NONE IF SR1=1

IF SR1 BITS 1 2=1 THEN SEE OPERATION OPTIONS

6.0 DEVICE/OPTION SETUP

SR1=000 AN ANALOG GROUND MUST BE PUT ON CH. 0.

SR1 BIT0=1 THE KVV11 OPTION MUST BE CONNECTED TO THE ADV11 OPTION.

SR1 BIT1=1 ALL CHANNELS SPECIFIED MUST HAVE AN ANALOG GROUND.

7.0 MODULE OPERATION

1. (START) BIT EXERCISE CSR
2. SET TEST CHANNEL TO ZERO
3. (RSTRT) PERFORM RMS NOISE CHECK ON SPECIFIED CHANNEL.
(ADRMS1)
WE FIRST USE SAR TO FIND THE DAC VALUE THAT PRODUCES A 16/84 SPLIT FOR THE LEFT BOUNDARY OF NOISE. THEN WE USE SAR TO FIND THE DAC VALUE THAT PRODUCES A 84/16 SPLIT FOR THE RIGHT VALUE. WE THEN SUBTRACT THE TWO DAC VALUES AND WE HAVE A VALUE FOR THE 68% AREA OF NOISE (RMS). IT IS THEN COMPARED AGAINST THE ALLOWED LIMIT TO SEE IF EXCESSIVE NOISE IS ON THE CHANNEL.
4. (ADPK1) PERFORM PEAK NOISE CHECK ON SPECIFIED CHANNEL.
9. (ADPK1) THIS IS A PEAK NOISE TEST.
WE FIRST USE SAR TO FIND THE DAC VALUE THAT PRODUCES A .6% SPLIT FOR THE LEFT BOUNDARY OF NOISE. THEN WE USE SAR TO FIND THE DAC VALUE THAT PRODUCES A .6% SPLIT FOR THE RIGHT BOUNDARY. WE THEN SUBTRACT THE TWO DAC VALUES AND WE HAVE A

VALUE OF 98% AREA OF NOISE (PEAK). IT IS THEN COMPARED AGAINST THE ALLOWED LIMIT TO SEE IF EXCESSIVE NOISE IS ON THE CHANNEL.
PAGE 4

CHANNEL.

5. IF MULTIPLE CHANNELS ARE SELECTED FOR NOISE TESTING TEST NEXT CHANNEL, IF SINGULAR RETEST CHAN. 0.
6. IF MULTI-CHANNEL SAMPLING IS SELECTED, TAKE SAMPLES ON EACH CHANNEL SPECIFIED, AND COMPARE THE AVERAGE OF THE SAMPLES AGAINST THE OLD AVERAGE FOR THE CHANNEL. IF THE DIFFERENCE IS GREATER THAN THE TOLERANCE, REPORT THE DATA ON THAT CHANNEL.
7. REPORT END PASS.
8. (SAR) SAR IS A SUCCESSIVE APPROXIMATION ROUTINE. IT IS USED TO FIND A DAC VALUE THAT PRODUCES A DESIRED SPLIT. IT DOES THIS BY TRYING A DAC VALUE AND TAKING 512 CONVERSIONS ON THE A/D. IF THE AMOUNT OF THE SAMPLES IS LOWER THEN SPECIFIED IT INCREASES THE DAC VALUE, IF THE AMOUNT OF SAMPLES IS HIGHER THEN SPECIFIED, IT DECREASES THE DAC VALUE. IF THE END DAC VALUE IS EITHER 000 OR 377 WE HAVE A "WARPAROUND" ERROR. THIS OCCURS WHEN WE ARE UNABLE TO ADJUST THE DAC TO PRODUCE A DESIRED SPLIT, AND INDICATES EXCESSIVE NOISE ON A CHANNEL.
9. (RANDY) THIS IS A RANDOM NUMBER GENERATOR. IF THE KVV11 CLOCK OPTION IS SELECTED WE GET THE NUMBER THAT WE PUT INTO THE CLOCK PRESET REGISTER FROM THIS ROUTINE.

8.0 OPERATION OPTIONS

1. VALID SRI VALUES

SRI BIT	ENABLE/DISABLE	FUNCTION
0	0	INHIBIT USE OF CLOCK OPTION.
0	1	ENABLE USE OF CLOCK OPTION. NOTE: IF ENABLED, YOU MUST DESELECT KWEA FROM DEC/X11 RUN.
1	0	INHIBIT SAMPLING OTHER CHANNELS FOR STABLE INPUT.
1	1	ENABLE SAMPLING CHANNEL ZERO THROUGH CHANNEL SPECIFIED BY CLSTCH FOR STABLE INPUT (+-) TOLERANCE SPECIFIED BY OFFALL
2	0	USE CHANNEL ZERO ONLY FOR NOISE TESTING.
2	1	USE CHANNEL ZERO THROUGH THE CHANNEL SPECIFIED IN NLSTCH FOR NOISE TESTING.

2. THE FOLLOWING ARE LOCATIONS WITHIN THIS MODULE THAT ENABLE THE USER TO CHANGE LIMITS AND SPECIFY CHANNELS.

LOCATION	FUNCTION
ARMLIM	SPECIFIES MAXIMUM LIMIT FOR RMS NOISE. MAY BE CHANGED TO ZERO TO FORCE TIMEOUT OF RMS NOISE ON A CHANNEL RUNNING IN A SYSTEM ENVIRONMENT.
APKLM	SPECIFIES MAXIMUM LIMIT FOR PEAK NOISE. MAY BE CHANGED TO ZERO TO FORCE TIMEOUT OF PEAK NOISE ON A CHANNEL RUNNING IN A SYSTEM ENVIRONMENT.
CLSTCH	IF SRI BIT1=1, USED TO SPECIFY END CHANNEL FOR SAMPLING STABLE INPUT.
OFFALL	IF SRI BIT1=1, USED TO SPECIFY TOLERANCE OF STABLE CHANNEL. PRESET BY MODULE TO "000002".
NLSTCH	166 IF SRI BIT2=1, USED TO SPECIFY END CHANNEL FOR NOISE TESTING.

9.0 NON-STANDARD PRINTOUTS

1. IF A CHANNEL HAS EXCESSIVE RMS NOISE, IT REPORTS IT IN AN ERROR CALL AND A MSGN CALL:

(EXAMPLE)

ON CH. 00 A/D RMS NOISE=0.52 LSB (LIMIT=.25LSB)

2. IF A CHANNEL HAS EXCESSIVE PEAK NOISE, IT REPORTS IT IN AN ERROR CALL AND A MSGN CALL:

(EXAMPLE)

ON CH. 00 A/D PEAK NOISE=2.57LSB(LIMIT=2.00LSB)

3. IF THERE IS AN EXCESSIVE AMOUNT OF NOISE SO THAT THE DAC CANNOT BE ADJUSTED, IT REPORTS IT IN AN ERROR CALL AND A MSGN CALL:

(EXAMPLE)

PEAK WRAPAROUND ERROR ON CHAN. 00

4. IF A CHANNEL IS FOUND TO BE UNSTABLE IN STABLE INPUT SAMPLING, IT REPORTS IT IN A MSGN CALL:

(EXAMPLE)

ON CHAN 14 OLD AVERAGE=4066 NEW AVERAGE=4000

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      ;
      ;.TITLE ADCB DEC/X11 SYSTEM EXERCISER MODULE
      ;DDXCOM VERSION 6 23-NOV-78
      ;*****LIST BIN*****
000000-
000008- 042101 041103 040
000005- 000
000006- 170400
000010- 000400
000017- 300
000011- 200
000014- 000001
000016- 000000
000020- 000000
000022- 000000
000024- 000000
000026- 140000
000030- 000274-
000032- 000224-
000034- 000000
000036- 000001
000040- 000000
000042- 000000
000044- 000000
000046- 000000
000050- 000000
000052- 000000
000054- 000000
000056- 000000
000060- 000000
000062- 000000
000064- 000000
000066- 000000
000070- 000000
000072- 000000
000074- 000001
000076- 000000
000100- 000000
000102-
000104- 000000
000106- 000000
000110- 000000
000112- 001272-
000114- 000000
000116- 000000
000120- 000000
;*****LIST BIN*****
BEGIN:
MODNAM: -ASCII /ADCB / ;MODULE NAME
RFLAG: -BYTE OPEN ;USED TO KEEP TRACK OF WBUF USAGE
ADDR: 170400+0 ;1ST DEVICE ADDR.
VECTOR: 400+0 ;1ST DEVICE VECTOR.
BR1: -BYTE PRTY6+0 ;1ST RR LEVEL-
BR2: -BYTE PRTY4+0 ;2ND RR LEVEL-
DVID1: +1 ;DEVICE INDICATOR 1.
SR1: OPEN ;SWITCH REGISTER 1
SR2: OPEN ;SWITCH REGISTER 2
SR3: OPEN ;SWITCH REGISTER 3
SR4: OPEN ;SWITCH REGISTER 4
;*****LIST BIN*****
STAR: 140000 ;STATUS WORD.
INIT: START ;MODULE START ADDR.
SPOINT: MODDSP ;MODULE STACK POINTER.
PASSCNT: 0 ;PASS COUNTER.
ICOUNT: 0 ;# OF ITERATIONS PER PASS=1
SOPCNT: 0 ;LOC TO COUNT ITERATIONS
HRDCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
SOPFAS: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
HRDFAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
RANNUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
CONFIG:
RES1: 0 ;RESERVED FOR MONITOR USE
RES2: 0 ;RESERVED FOR MONITOR USE
SVR0: OPEN ;LOC TO SAVE R0.
SVR1: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVR3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVR5: OPEN ;LOC TO SAVE R5.
SVR6: OPEN ;LOC TO SAVE R6.
CSRA: OPEN ;ADDR OF CURRENT CSR.
SBADR: ;ADDR OF GOOD DATA, OR
CSR: OPEN ;CONTENTS OF CSR.
ASADR: ;ADDR OF BAD DATA, OR
ASTAT: OPEN ;STATUS REG CONTENTS.
ERRTYP: ;TYPE OF ERROR
ASB: OPEN ;EXPECTED DATA.
RSTRT: OPEN ;ACTUAL DATA.
WDTO: OPEN ;RESTART ADDRESS AFTER END OF PASS
WDFR: OPEN ;WORDS TO MEMORY PER ITERATION
WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
INTR: OPEN ;# OF INTERRUPTS PER ITERATION

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000122- 000120
000224-
332
333
334 000234- 000000
335 000230- 000000
336 000230- 000002
337
338
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341 000232- 170400
342 000234-
343 000234- 170402
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346 000236- 170420
348 000240- 170422
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354 000242- 000000
355 000244- 000000
356 000246- 000000
357 000250- 000000
358 000252- 000000
359 000254- 000000
360 000256- 000062
361 000260- 000000
362 000262- 000310
363 000264- 000000
364 000266- 000000
365 000270- 000000
366 000272- 000000
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370 000274- 012767 020064 177616
371 000302- 012767 013000 177604
372 000310- 012767 013000 177600
373 000316- 016700 177464
374 000322- 010067 177704
375 000326- 052700 000092
376 000332- 010067 177816
377 000336- 005067 177722
378
379
380
381
382 000342- 104421 000000 000256-
383 000350- 002740-
384
;*****LIST BIN*****
IDNUM: 120 ;MODULE IDENTIFICATION NUMBER=120
MODDSP:
;*****LIST BIN*****
;*OPTIONAL USER SUPPLIED IN
CLSTCH: -WORD 0 ;USER SUPPLIED LAST SAMPLED CH
WLSFCB: -WORD 0 ;USER SUPPLIED LAST NOISE CH.
OFFALL: -WORD 2 ;USER SUPPLIED TOLERANCE FOR SAMPLE
;*
;*REGISTER AND VECTOR ADDRESS
;*
ADSR: -WORD 170400 ;A/D CSR ADDRESS
DAC: -WORD 170402 ;DAC (WRITE ONLY) AND BUFFER REG (READ ONLY)
ABR: -WORD 170402
;THE FOLLOWING ARE KW1LK ADDRESSES IF A KW1LK EXISTS.
ASR: -WORD 170420 ;CLOCK A CSR.
ABR: -WORD 170422 ;CLOCK A PRESET BUFFER.
;*
;*FLAGS, COUNTERS AND OTHER REGISTERS
;*
WHO: -WORD 0 ;0=RMS NOISE, 1=PEAK NOISE
INTPLG: -WORD 0 ;USED IN LOGIC TEST TO INDICATE AN A/D INTR.
FRED: -WORD 0 ;USED TO HOLD CURRENT DAC VALUE.
EDGE: -WORD 0 ;HOLD CURRENT EDGE VALUE.
TMP: -WORD 0 ;TEMP WORKING AREA.
ARMX: -WORD 0 ;USED TO HOLD RMS NOISE VALUE.
ARMLIM: -WORD 50. ;RMS NOISE LIMIT.
APKX: -WORD 0 ;USED TO HOLD A/D PEAK NOISE VALUR.
APKLM: -WORD 200. ;A/D PEAK NOISE LIMIT.
NCCB: -WORD 0 ;CURRENT NOISE CHAN.
CCH: -WORD 0 ;CURRENT SAMPLE CHAN.
PASSCNT: -WORD 0 ;PASS CNT.
TMP1: -WORD 0 ;TEMPORARY STORAGE FOR ASCII CONVERSION
;*
START: MOV #8244, INTR ;8244 INTERRUPTS/ITERATION
MOV #5632, WDTO ;5632 WORDS TO MEM/ITERATION
MOV #5632, WDFR ;5632 WORDS FROM MEM/ITERATION
MOV ADDR, RO ;GET BASE ADDR OF A/D.
MOV RO, ADNR ;PTX A/D CSR ADDR.
ADD #2, RO ;BUFFER REG=CSR+2.
MOV RO, ADNR ;PTX BUFFER REG ADDR.
CLR NCCB ;CLEAR 1ST CHAN. FOR NOISE.
;*****LIST BIN*****
;CONVERT ARMLIM TO ASCII AND
;STORE AT DECIM
BTDS$, BEGIN, ARMLIM, DECIM
;*****LIST BIN*****

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385  
386  
387 000352 116767 002364 002571      MOVB   DECIM+2,P7      ;NOW WE WILL PUT IT  
388 000360 116767 002357 002569      MOVB   DECIM+3,P7+2    ;INTO THE ASCII  
389 000366 116767 002352 002560      MOVB   DECIM+4,P7+3    ;MESSAGE THAT WE TYPE OUT.  
390  
391  
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393  
394 000374 104421 000000 000262      BTODS,BEGIN,APKLIM,DECIM ;CONVERT APKLIM TO ASCII AND  
395 000402 002740  
396  
397  
398 000404 116767 002332 002551      MOVB   DECIM+2,P8      ;NOW WE WILL PUT IT  
399 000412 116767 002325 002545      MOVB   DECIM+3,P8+2    ;INTO THE ASCII MESSAGE  
400 000420 116767 002320 002540      MOVB   DECIM+4,P8+3    ;THAT WE TYPE OUT.  
401  
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409 000426 005777 177600      LOG1:  TST   @ADSR      ;ADDRESS THE A/D  
410  
411  
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416  
417 000432 104407 000000      LOG2:  BREAKS,BEGIN      ;TEMPORARY RETURN TO MONITOR....  
418 000436 104407 000000      BREAKS,BEGIN      ;THEN CONTINUE AT NEXT INSTRUCTION.  
419 000442 032767 000001 177346      BIT    #BIT0,SRI      ;IS CLOCK OPTION SELECTED?  
420 000450 001402      BEQ    LOG3           ;BR IF NOT TO NEXT TEST.  
421  
422 000452 005777 177560      TST   @ASR           ;CLOCK WAS SELECTED, WILL IT RESPOND?  
423  
424  
425  
426  
427  
428 000456 005067 177562      LOG3:  CLR    INTFLG      ;CLEAR A/D DID INTR. FLAG.  
429 000462 012777 000554 177320      MOV    #15,OVECTOR    ;SET UP VECTOR ADDR.  
430 000470 012777 000101 177534      MOV    #101,@ADSR     ;START A CONVERSION, SHOULD INTERRUPT.  
431  
432  
433 000476 104407 000000      BREAKS,BEGIN      ;TEMPORARY RETURN TO MONITOR....  
434 000502 104407 000000      BREAKS,BEGIN      ;THEN CONTINUE AT NEXT INSTRUCTION.  
435  
436 000506 005767 177532      TST   INTFLG      ;DID THE A/D INTERRUPT?  
437 000512 001027      BNE   LOG4           ;IF SO, NEXT TEST  
438 000514 016767 177512 177356      MOV    @ADSR,CSRA     ;SET ADDR. OF AD CSR FOR ERROR TYPEOUT.  
439 000522 017767 177504 177352      MOV    @ADSR,ACSR     ;RECORD CONTENTS OF CSR.  
440 000530 005077 177476      CLR    @ADSR         ;STOP A/D
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441 000534 012767 000023 177344      MOV    #23,ERRTYP     ;DEV FAILED TO INTERRUPT  
442  
443 000542 104405 000000 000000      HRDERS,BEGIN,NULL     ;A/D FAILED TO INTERRUPT  
444  
445 000550 104410 000000      ENDS,BEGIN           ;  
446  
447  
448 000554 005077 177452      1S:   CLR    @ADSR      ;STOP A/D.  
449 000560 005777 177450      TST   @ADSR      ;CLEAR CSR BIT07.  
450 000564 005267 177454      INC   INTFLG     ;INDICATE THAT IT DID INTR.  
451 000570 000002      RTI              ;EXIT INTR.  
452  
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459  
460 000572 032767 000001 177216      LOG4:  BIT    #BIT0,SRI      ;IS THE CLOCK OPTION SELECTED?  
461 000600 001451      BFD    LOG5        ;IF NOT, GOTO NEXT TEST.  
462  
463 000602 012777 177777 177430      MOV    #177777,@ABR   ;PRESET CLOCK FOR ALL ONES.  
464 000610 012777 000040 177414      MOV    #BITS,@ADSR    ;SET OVERFLOW ENABLE IN A/D.  
465 000616 012777 000011 177412      MOV    #1,@ASR       ;START CLOCK, IMBZ, GO.  
466 000624 005000      CLR    R0          ;SET FOR DELAY LOOP.  
467  
468 000626 104407 000000      1S:   BREAKS,BEGIN      ;TEMPORARY RETURN TO MONITOR....  
469 000632 104407 000000      BREAKS,BEGIN      ;THEN CONTINUE AT NEXT INSTRUCTION.  
470 000636 105777 177374      TSTB   @ASR        ;IS THE CLOCK OVERFLOW SET?  
471 000642 100402      BMT    2S          ;YES-EXIT LOOP  
472 000644 105200      INCB   R0          ;NO-IS DELAY EXCEEDED?  
473 000646 001367      BNE   1S          ;NO-CONTINUE DELAY.  
474  
475 000650 017767 177362 177226      2S:   MOV    @ASR,ASTAT    ;RECORD CONTENTS OF CLOCK CSR.  
476 000656 005077 177354      CLR    @ASR        ;CLEAR THE CLOCK.  
477  
478  
479 000662 105777 177344      TSTB   @ADSR      ;IS DONE FLAG SET?  
480 000668 100416      BMI   LOG5        ;IF YES NEXT TEST.  
481  
482 000670 016767 177336 177202      MOV    @ADSR,CSRA   ;IF NOT, ERROR  
483 000676 017767 177330 177176      MOV    @ADSR,ACSR   ;RECORD A/D CSR ADDR.  
484 000704 012767 000046 177174      MOV    #46,ERRTYP   ;RECORD CONTENTS OF A/D CSR.  
485  
486 000712 104405 000000 000000      HRDERS,BEGIN,NULL     ;CLK FAILED TO TRIGGER A/D CONVERSION  
487  
488  
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491  
492 000720 104410 000000      ENDS,BEGIN      ;CLOCK OVERFLOW FAILED TO TRIGGER A/D CONVERSION  
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000724- 104407 000000-
000730- 104407 000000-
000734- 005777 177274
000740- 012777 001016- 177042
000746- 012777 000105- 177256
000754- 104400 000000-
000760- 005777 177250

000764- 001417
000766- 016767 177240 177104
000774- 017767 177232 177100
001002- 012767 000026 177076

001010- 104405 000000- 000000

001016-
001016- 000004 000000- 000760-

001024- 012777 001104- 176756
001032- 012777 00505- 177172
001040- 104400 000000-
001044- 022777 007777 177162
001054- 001417
001064- 017767 177152 177016
001066- 017767 177144 177012
001070- 012767 000026 177010

001076- 104405 000000- 000000

001104-
001104- 000004 000000- 001044-

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LOG5: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
TST @ADDR ;FALSE READ OF A/D BUFFER.
MOV #25,@VECTOR ;SET UP INTERRUPT VECTOR
MOV #105,@ADDR ;START A/D WITH MAINTENANCE SET
EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
TST @ADDR ;TEST FOR ALL 0'S RESULT
;NOTE: INTERRUPT SERVICE WILL
;BEGIN USING HERE AFTER A PIRO.
BRQ LOG6 ;IF RESULT IS ALL 0'S, GOTO NEXT TEST
MOV @ADDR,CSRA ;RECORD A/D CSR ADDRESS
MOV @ADDR,ACSR ;RECORD CONTENTS OF A/D CSR
MOV #26,ERRTP ;A/D CONV. OUT OF SPEC
;*****
HRDERS,BEGIN,NULL ;FAILED TO GET ALL 0'S RESULT FROM CONVERSION
;*****
;A/D INTERRUPTS TO HERE

PIRQS,BEGIN,1S ;QUEUE UP TO CONTINUE AT 1S AND RTI

LOG6: MOV #26,@VECTOR ;SET UP INTERRUPT VECTOR
MOV #505,@ADDR ;START A/D WITH MAINTENANCE SET
EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
CMP #1777,@ADDR ;TEST FOR ALL 1'S RESULT
BRQ LOG7 ;IF RESULT IS ALL 1'S, GOTO NEXT TEST
MOV @ADDR,CSRA ;RECORD A/D CSR ADDRESS
MOV @ADDR,ACSR ;RECORD CONTENTS OF A/D CSR
MOV #26,ERRTP ;A/D CONV. OUT OF SPEC
;*****
HRDERS,BEGIN,NULL ;FAILED TO GET ALL 1'S RESULT FROM CONVERSION
;*****
;A/D INTERRUPTS TO HERE

PIRQS,BEGIN,1S ;QUEUE UP TO CONTINUE AT 1S AND RTI

LOG7: ;*
;IN THIS TEST WE WILL SAMPLE ALL CHANNELS SELECTED
;FOR TEST AND STORE AWAY THEIR RESULTS.
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001112- 104407 000000-
001116- 104407 000000-
001122- 005777 177108
001126- 005077 177100
001136- 012777 001264- 176644

001144- 012700 177770
001154- 016767 177110 177072
001160- 000367 177066
001164- 052767 000101 177060
001172- 016777 177054 177032
001200- 104400 000000-
001204- 067701 177024

001210- 005200
001212- 100767

001214- 006201
001216- 006201
001220- 006201
001224- 005501
001228- 016702 177036
001230- 006302
001232- 010162 003262-

001236- 005267 177024
001242- 026767 177020 176756
001250- 003735
001254- 026767 177010 176744
001260- 003731
001262- 000403

001264-
001264- 000004 000000- 001204-

001272- 005077 176734
001276- 005067 176766

001302-
001302- 016700 176502
001306- 012720 002236-
001312- 116710 176474

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LOG7: BREAKS,BEGIN ;TEMPORARY RETURN TO MONITOR...
BREAKS,BEGIN ;THEN CONTINUE AT NEXT INSTRUCTION.
TST @ADDR ;FALSE READ OF A/D BUFFER.
CLR @ADDR ;CLEAR A/D'S CSR
CLR CCH ;START ON CH. 0.
MOV #45,@VECTOR ;SET UP INTR. VECTOR.
MOV #-8,R0 ;SET TO DO 8 CONVERSIONS.
CLR R1 ;R1 WILL CONTAIN SUM OF CONVERSIONS.
MOV CCH, TMP ;GET CH. NUMBER.
SWAB TMP ;PUT IN CORRECT CSR POSITION.
BTS #BIT6BIT0,TMP ;ADD INTR. ENABLE AND GO.
MOV #26,@ADDR ;START A/D.
EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
ADD @ADDR,R1 ;SUM THIS RESULT.
;NOTE: INTR SERVICE WILL
;BEING USE HERE AFTER A PIRO
INC R0 ;DONE 8 CONVERSIONS?
BMI 2S ;NO - DO ANOTHER ONE.
ASR R1 ;NOW WE MUST
ASR R1 ;CALCULATE THE AVERAGE
ASR R1 ;OF THE SAMPLES THAT
ADC R1 ;WE JUST TOOK.
MOV CCH,R2 ;PICK UP CH. NUMBER.
ASL R2 ;USE IT AS AN OFFSET.
MOV R1,RECSAM(2) ;STORE THIS AVERAGE.
INC CCH ;UPDATE CH. NUMBER.
CMP CCH,NLSTCH ;DONE ALL NOISE CHANNELS?
BLE IS ;NO-DO NEXT ONE.
BLE CCH, CLSTCH ;DONE ALL STABLE CHS?
BLE IS ;NO -DO NEXT ONE.
BR RESTRT ;YES-GOTO NOISE TESTS.

;A/D INTERRUPTS TO HERE

4S: PIRQS,BEGIN,3S ;QUEUE UP TO CONTINUE AT 3S AND RTI

RESTRT: CLR @ADDR ;CLEAR A/D'S CSR.
CLR PASSCNT

1S: MOV VECTOR,R0 ;SET VECTOR AND PSW
MOV #WAIT,(R0)+ ;LOAD INTR. ADDR. INTO VECTOR ADDR.
MOVBR #R1,(R0) ;LOAD PRIORITY

;CALCULATE A/D RMS NOISE USING VERNIER DAC.
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ADCB0.P11 12-OCT-78 11:44
609 001316 012700 000657 ADRMS1: MOV #431,RO ;RO = 84.13% OF 512 CONVERSIONS
610 001322 016767 176722 MOV NCCH,TMP ;GET THE CHAN #
611 001330 000367 176716 SWAB TMP ;PUT IN PROPER CSR POSITION.
612 001336 016767 176710 BLS #100,TMP ;ADD INTR. ENABLE
613 001342 016701 176716 MOV NCCH,R1 ;PICK UP CH. NUMBER.
614 001346 006301 ASL R1 ;FORM AN OFFSET.
615 001350 016167 003262 MOV RECSAM(1),EDGE ;GET EDGE VALUE FOR THIS CH.
616 001356 016767 176642 INC ;INDICATE RMS NOISE TEST.
617 001362 016777 176642 MOV TMP,ADSR ;CH.#0, AND INTERRUPT ENABLE
618 001370 016701 176640 MOV DAC,R1 ;R1 = ADDRESS OF SAR DAC
619 001374 004767 000516 JSR PC,SAR ;GET DAC VALUE THAT PRODUCES 16/84 SPLIT
620 001400 016700 000131 MOV FRD,R5 ;RO = LEFT BOUNDARY
621 001404 016700 000131 MOV FRD,R5 ;RO = LEFT BOUNDARY
622 001410 004767 000502 JSR PC,SAR ;GET DAC VALUE THAT PRODUCES 84/16 SPLIT
623 001414 166705 176626 SUB FRD,R5 ;RS = BREADTH OF NOISE @ 68% AREA
624 001420 010567 176630 MOV RS,ARMK ;SAVE FOR DAC NOISE CALCULATIONS
625 001424 020567 176626 CMP RS,ARMLIN ;< OR = RMS LIMIT?
626 001430 003413 BLE ADPR1 ;IF WITHIN LIMIT THEN CONTINUE AT ADRMS2
627 001432 004767 001210 JSR PC,ERCOM ;GET ERROR PARAMETERS
628 001436 012767 000031 176442 MOV #31,ERRTYP ;ERROR PARAMETERS LOADED BY ".ERCOM"
629 ***** ;NOISE EXCEEDED
630 *****
631 001444 104405 000000 000000 HDRRS,BEGIN,NULL ;RMS NOISE ERROR-SEE NEXT TYPEOUT
632 *****
633 001452 104403 000000 002746 MSGN$,BGIN,MSG1 ;ASCII MESSAGE CALL WITH COMMON HEADER
634 *****
635 ;CALCULATE A/D PEAK NOISE USING VERNIER DAC.
636 *****
637 001460 012700 000775 ADPK1: MOV #509,RO ;FOR EAK OF 2 1/2 SIGMA
638 001464 016767 176574 MOV NCCH,TMP ;GET CHAN. NUMBER
639 001472 000367 176554 SWAB TMP ;PUT IN CORRECT CSR POSITION.
640 001476 052767 176546 BLS #100,TMP ;ADD INTR. ENABLE
641 001504 050567 176532 CLR WHO ;INDICATE PEAK NOISE TEST.
642 001510 016777 176536 MOV TMP,ADSR ;CH.#0 AND INTERRUPT ENABLE, AND CH
643 001516 016701 176532 MOV DAC,R1 ;R1 = ADDRESS OF SAR DAC
644 001522 016705 176524 JSR PC,SAR ;GET DAC VALUE THAT GIVES .6% LOW COUNT
645 001526 016705 176514 MOV FRD,R5 ;RS = LEFT BOUNDARY
646 001532 012700 000003 MOV #3,RO ;CHANGE SPLIT FOR RIGHT BOUNDARY
647 001536 004767 000354 JSR PC,SAR ;GET DAC VALUE THAT GIVES .6% HIGH COUNT
648 001542 166705 176500 SUB FRD,R5 ;RS = A/D PEAK TO PEAK NOISE
649 001546 010567 176506 MOV RS,APKX ;SAVE FOR DAC NOISE CALCULATIONS
650 001552 020567 176504 CMP RS,APKLM ;< OR = A/D PEAK NOISE LIMIT?
651 001556 003413 BLE ENDP ;BRANCH IF NO ERROR
652 001560 004767 001062 JSR PC,ERCOM ;GET ERROR PARAMETERS
653 ***** ;ERROR PARAMETERS LOADED BY "ERCOM"
654 ***** ;A/D NOISE LIMIT EXCEEDED
655 *****
656 001572 104405 000000 000000 HDRRS,BEGIN,NULL ;PEAK NOISE ERROR-SEE NEXT TYPEOUT
657 *****
658 001600 104403 000000 002766 MSGN$,BGIN,MSG5 ;ASCII MESSAGE CALL WITH COMMON HEADER
659 *****
660 *****
661 001606 005267 176456 ENDP: INC PASSCNT ;UPDATE PASS COUNT.
662 001612 032767 000004 176176 BIT #BIT2,SRI ;DOING MULTIPLE NOISE CHANNELS?
663 001620 001012 BNE ZS ;YES - GET THE NEXT ONE
664 001622 026727 176442 000013 1S: CMP PASSCNT,#13 ;DONE ENOUGH PASSES?
    
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ADCB0.P11 12-OCT-78 11:44
665 001630 001417 BEQ ZS ;YES-DO ENDPASS.
666 001632 032767 000001 176156 BIT #BIT0,SRI ;ARE WE CONNECTED TO THE KW11K OPTION?
667 001640 001013 BNE ZS ;IF SO THE 1 MIN IS UP ON ONE PASS.
668 *****
669 001642 000167 177450 JMP ADRMS1 ;NO DO AGAIN.
670 *****
671 001646 005267 176412 2S: INC NCCH,NLSTCH ;POINT TO NEXT CH.
672 001652 026767 176406 CMP BLOS,NCCH ;EXCEED LAST NOISE CH?
673 001660 001760 BLS ZS ;NO-TEST THIS CH.
674 001662 005067 176376 CLR NCCH ;YES - START AGAIN ON CH. 0.
675 001666 000755 BR 1S ;GO TEST CH 0.
676 *****
677 001670 032767 000002 176120 3S: BIT #BIT1,SRI ;ARE WE DOING VOLTAGE SAMPLING?
678 001676 001505 BEQ ENDP ;NO - END PASS!
679 001700 005067 176362 CLR CCH ;YES - START WITH CH 0.
680 *****
681 001704 026767 176356 4S: CMP CCH,CLSTCH ;DONE ALL CHANNELS?
682 001712 003077 BGT ENDP ;YES - REPORT END PASS.
683 001714 005003 CLR R3 ;R3 WILL HOLD SAMPLE.
684 001716 017700 MOV #8,R3 ;SET TO TAKE 8 SAMPLES
685 001722 016777 176060 MOV #63,VECTOR ;SET VECTOR FOR INTERRUPT
686 001730 016701 176332 MOV CCH,R1 ;GET CH. NUMBER
687 001734 000301 SWAB R1 ;FIX RIGHT POSITION IN CSR.
688 001736 052701 BLS #101,R1 ;ADD INTR. ENABLE AND GO.
689 001742 010177 MOV R1,ADSR ;START A/D.
690 001746 104400 000000 6S: EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
691 001752 *****
692 *****
693 001752 000004 000000 001760 *****
694 *****
695 *****
696 001760 067703 176250 7S: ADD #ADR,R,R3 ;ADD THIS SAMPLE TO TOTAL
697 001764 005300 DEC R0 ;ALL DONE ALL SAMPLES?
698 001766 001365 BNE ZS ;NO - DO NEXT ONE.
699 *****
700 001770 006203 ASR R3 ;YES NOW WE
701 001772 006203 ASR R3 ;MUST DIVIDE BY
702 001774 006203 ASR R3 ;8 TO GET THE
703 001776 005503 ADC R3 ;SAMPLE AVERAGE.
704 002000 016700 MOV CCH,RO ;NOW GET CH. NUMBER
705 002004 006300 ASL RO ;FORM AN OFFSET
706 002006 010301 MOV R3,R1 ;SAVE NEW SAMPLE VALUE.
707 002010 166003 SUB RECSAM(0),R3 ;GET THE DIFFERENCE BETWEEN
708 ***** ;AND THE NEW ONE WE JUST GOT
709 002014 100001 BPL R3 ;IF POSITIVE WE'RE OK
710 002016 005403 NEG R3 ;OTHERWISE MAKE IT POSITIVE.
711 002020 020367 CMP R3,OFFALL ;IS IT WITHIN TOLERANCE?
712 002024 003426 BLE ZS ;YES DO NEXT CH
713 *****
714 *****
715 *****
716 *****
717 *****
718 002026 104420 000000 000266 *****
719 002034 003502 *****
720 *****
    
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721 002036* 016067 003262* 176226      MOV      R1,RECSAM(0),TEMP1 ;GET OLD VALUP
722 ;*****
723 ;CONVERT TEMP1 TO ASCII AND
724 ;STORE AT OLDS
725 002044* 104420 000000* 000272*      OTOAS,BEGIN,TEMP1,OLDS
726 002052* 003462*
727 ;*****
728 002054* 010167 176212      MOV      R1,TEMP1 ;GET NEW VALUP
729 ;*****
730 ;CONVERT TEMP1 TO ASCII AND
731 ;STORE AT NEWS
732 002060* 104420 000000* 000272*      OTOAS,BEGIN,TEMP1,NEWS
733 002066* 003472*
734 ;*****
735 002070* 010160 003262*      MOV      R1,RECSAM(0) ;PUT NEW INTO OLD.
736 002074* 104403 000000* 003024*      MSGNS,BFGIN,MSG12 ;ASCII MESSAGE CALL WITH COMMON HEADER
737 002102* 005267 176160      10S: INC CCH ;LOOK AT NEXT CHAN.
738 002106* 000167 17572 ;JMP 45 ;GO TEST IT
739
740 002112* EENDP: ENDP: ;SIGNAL END OF ITERATION.
741 002112* 104413 000000* ;MONITOR SHALL TEST END OF PASS
742
743 ;USING SUCCESSIVE APPROXIMATION AND VERNIER DAC DEFINED IN R1.
744
745 SAR: MOV #200,R2 ;R2 = MSB OF DAC
746 CLR (R1) ;GET RID OF "ONES"
747 CLR R2 ;START WITH ZERO DAC
748 CLR R3 ;TRY THIS BIT
749 BIT: ADD R2,R2 ;LOAD DAC
750 MOV R3,R1 ;INIT HIGH COUNT
751 CLR R3 ;R4 = # OF SAMPLES IN A BURST
752 MOV #512,,R4
753
754 002146* 032767 000001 175642      BIT #R1TO,SR1 ;IS CLOCK SELECTED?
755 002154* 001004 ;BNE IS ;YES GOTO HANDLER.
756
757 002156* 052777 000001 176046      BIS #R1TO,@ADSR ;NO - SET GO BIT IN A/D.
758 002164* 000420 ;BR 2S ;GOTO 2S
759
760 002166* 004767 000364      1S: JSR PC,RANDY ;GET A RANDOM NUMBER.
761 002172* 005077 176040 ;CLR @ASR ;MAKE SURE THE CLOCK'S CSR IS CLEAR.
762 002176* 052767 177770 000434 ;BIS #177770,RNA ;WAKE SURE OF HIGH NUMBER.
763 002204* 016777 000430 176026 ;MOV RNA,@ASR ;SET CLOCK PRESET REG.
764 002210* 052777 000040 176012 ;BIS #R1TO,@ADSR ;SET OVPFLOW ENARLE.
765 002220* 012777 000011 176010 ;MOV #11,@ASR ;START CLOCK
766
767 002226* 104400      2S: EXITS ;RETURN TO MONITOR.
768
769 ;
770 ;
771 002230* 000004 000000* 002236*      PIRQS,BEGIN,1S ;QUEUE UP TO CONTINUE AT 1S AND RTI
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777 002236* 027767 175772 176004      1S: CMP @ADR,EDGE ;> OR = EDGE?
778 002244* 003401 ;BLO 2S ;BRANCH IF <EDGE
779 002246* 005203 ;INC R3 ;COUNT IF > OR =EDGE
780 002250* 005304 ;DPC R4 ;COUNT THROUGH BURST
781 002254* 006300 ;BNE CONY ;BRANCH IF NOT DONE
782 002256* 003402 ;CMP R3,R0 ;HIGH COUNT > 3 OF 512 CONVERSIONS?
783 002258* 003402 ;BLE 3S ;BRANCH TO LAVE BIT IN
784 002260* 160267 175762 ;SUB R2,R2,R3 ;TAKE BIT OUT
785 002264* 006200 ;ASR R2 ;NEXT BIT
786 002266* 001320 ;BNE BIT ;AND GO RESOLVE IT IF NOT DONE
787 002270* 005767 175752 ;TST R2 ;CHECK FOR ALL "ZPROES"
788 002274* 001405 ;BEQ WRPERR ;BRANCH IF INVALID RESULT
789 002276* 006777 175744 000377 ;CMP R2,#377 ;CHECK FOR ALL "ONES"
790 002278* 001401 ;BEQ WRPERR ;BRANCH IF INVALID RESULT
791 002306* 000207 ;RTS ;RETURN TO ADPMS1 OR ADPK1.
792
793 ;VOLTAGE NEEDED TO PRODUCE # OF HIGH COUNTS SPECIFIED IS OUT OF THE
794 ;RANGE OF THE WRAPAROUND DAC.
795 ;CLEAR INTERRUPTS, PRINT MESSAGE AND DROP MODULE.
796
797 002310* 005077 175716      WRPERR: CLR @ADSR ;STOP A/D
798 002314* 004767 000326 ;JSR PC,ERCOM ;GET ERROR PARAMETERS
799 ;ERROR PARAMETERS LOADED BY "ERCOM"
800 ;A/D NOISE LIMIT EXCEEDED
801 002326* 104405 000000* 000000 ;*****
802 ;*****
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833 002454 052777 000040 175550 BIS #BITS,@ADSR ;SET OVERFLOW ENABLF.
834 002460 012777 000011 175546 MOV #11,@ASR ;START CLOCK
835 002470 104400 000000 2S: EXITS,@BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
836 002474 000004 000000 002502 ERINT: ;-----
837 002474 000004 000000 002502 ;IRQS,@BEGIN,IS ;QUEUE UP TO CONTINUE AT IS AND RTI
838 002502 067700 175526 1S: ADD @ADBR,R0 ;R0 = RUNNING SUM OF DIFFERENCES
839 002506 160300 175520 SUB R3,R0 ;OFFSET BY EXPECTED VALUE
840 002510 077701 175520 CMP @ADBR,R1 ;HIGHER THAN PREVIOUS HIGH?
841 002514 003407 BLE #5 ;BRANCH IF NOT A NEW HIGH
842 002516 017701 175512 MOV @ADBR,R1 ;NEW HIGH RESULT
843 002522 027702 175506 2S: CMP @ADBR,R2 ;LOWER THAN PREVIOUS LOW?
844 002526 002002 BCC #5 ;BRANCH IF NO
845 002530 017702 175500 3S: MOV @ADBR,R2 ;NEW LOW RESULT
846 002534 005304 DEC R4 ;COUNT THROUGH BURST
847 002536 001324 BNE #0 ;DO ANOTHER SAMPLE IF NOT DONE
848 002540 063300 SWAB R0 ;DIVIDE SUM OF DIFFERENCES BY 256
849 002542 110000 MOV# R0,R0 ;SIGN EXTEND
850 002544 006200 ASR R0 ;DIVIDE BY 2 MORE
851 002546 005500 ADC R0 ;ROUND UP FOR > 1/2
852 002548 063300 ADD R0,R0 ;OFFSETO AVERAGE BY EXPECTED RESULT
853 002550 160201 SUB R2,R1 ;R1 = COUNT SPREAD
854 002552 000207 RTS PC
855 002554 000207 ;GENERATE A RANDOM NUMBER
856 002556 066767 000060 000054 RANDY: ADD RNB,RNA ;THESE FOLLOW SEQUENCE OF
857 002560 066767 000054 000046 ADD RNC,RNA ;INSTRUCTIONS GENERATE
858 002564 066767 000036 000036 ADC RNB ;A RANDOM NUMBER
859 002568 066767 000036 000030 ADD RNC,RNB ;TO BE USED
860 002572 066767 000034 000030 ADD RNC,RNB ;TO BE LOADED
861 002576 005567 000016 000020 ADC RNB ;INTO THE CLOCK'S PRESET
862 002580 066767 000016 000012 ADD RNC,RNC ;OFFSETO AVERAGE BY EXPECTED RESULT
863 002584 066767 000012 000012 ADD RNC,RNC ;ONLY RANA WILL BE USED.
864 002588 005567 000006 000006 ADC RNC
865 002592 000207 RTS PC ;RETURN TO "CONV"
866 002596 004241 RNB: 04241 ;RANDOM TO NUMBER A.
867 002600 142315 RNC: 142315 ;RANDOM NUMBER B.
868 002604 127623 RNC: 127623 ;RANDOM NUMBER C.
869 002608 ;CONVERT NOISE RESULT TO DECIMAL
870 002612 ERCOM: MOV @ADSR,CSRA ;LOAD HEADER FOR ERROR CALL
871 002616 MOV @ADSR,ACSR
872 002620 MOV @STAT,STAT
873 002624 MOV @TEMP1,TEMP1 ;STACK BINARY NOISE VALUE
874 002628 ;*****
875 002632 ;CONVERT TEMPI TO ASCII AND
876 002636 ;STORE AT DECIM
877 002640 BTODS,@BEGIN,TEMP1,DECIM
878 002644 ;*****
879 002648 MOV# DECIM+2,VALUE ;MOVE CONVERTED VALUES TO ASCII BUFFER
880 002652 MOV# DECIM+3,VALUE+2 ;FOR TYPEOUT OF ERROR
881 002656
882 002660
883 002664
884 002670 104421 000000 000272 002702
885 002704 116767 000032 000323 002712 116767 000025 000317

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889 002720 116767 000020 000312 MOV# DECIM+4,VALUE+3 ;ON RETURN
890 002724 ;*****
891 002728 ;CONVERT NCCH TO ASCII AND
892 002732 ;STORE AT CHANN
893 002736 104420 000000 000264 OTOAS,@BEGIN,NCCH,CHANN
894 002740 003502 ;*****
895 002744 RTS PC ;EXIT TO CALLER.
896 002748 000003 DECTM: .RLKW 3
897 002752 ;ASCII MESSAGES AND POINTERS
898 002756 MSG1: P2 ;ON CHAN
899 002760 CHANN+4 ;(CHAN NUMBER 2 DIGITS)
900 002764 P1 ;% A/D
901 002768 P4 ;RMS
902 002772 P6 ;NOISE =
903 002776 VALUE ;(CONVERTED BELOW) X.XX LSR (LIMIT =
904 002780 -1 ;0.50 LSB) "THIS VALUE WILL CHANGE IF OPERATOR CHANGES
905 002784 ;MESSAGE TERMINATOR.
906 002788 MSG5: P2 ;ON CHAN
907 002792 CHANN+4 ;(CHAN NUMBER 2 DIGITS)
908 002796 P1 ;% A/D
909 002800 P5 ;PEAK
910 002804 P3 ;WRAPAROUND
911 002808 P5 ;ERROR
912 002812 CHANN+4 ;ON CHAN
913 002816 ;(CHAN NUMBER 2 DIGITS)
914 002820 -1 ;MESSAGE TERMINATOR
915 002824 MSG11: P1 ;% A/D
916 002828 P5 ;PEAK
917 002832 P3 ;WRAPAROUND
918 002836 P5 ;ERROR
919 002840 CHANN+4 ;ON CHAN
920 002844 ;(CHAN NUMBER 2 DIGITS)
921 002848 -1 ;MESSAGE TERMINATOR
922 002852 MSG12: P1 ;% A/D
923 002856 P5 ;ERROR
924 002860 P2 ;ON CHAN
925 002864 CHANN+4 ;(CHAN NUMBER 2 DIGITS)
926 002868 -1 ;MESSAGE TERMINATOR
927 002872 MSG13: P1 ;% A/D
928 002876 P4 ;RMS
929 002880 P3 ;WRAPAROUND
930 002884 P5 ;ERROR
931 002888 P2 ;ON CHAN
932 002892 CHANN+4 ;(CHAN NUMBER 2 DIGITS)
933 002896 -1 ;MESSAGE TERMINATOR.
934 002900
935 002904
936 002908
937 002912
938 002916
939 002920
940 002924
941 002928
942 002932
943 002936
944 002940

```


TMP	000252R	358#	563*	564*	565*	566	610*	611*	612*	617	638*	639*	640*	642
TRPDFD=	000622	332#												
VALUE	003235R	887*	888*	889*	907	916	973#							
VECTOP	000010R	286#	429*	505*	530*	559*	603	685*						
WAIPT	002230R	60#	773#											
WASADR	000108R	320#												
WDFR	000116R	327#	372*											
WDT0	000114R	326#	371*											
WHO	000242R	354#	615*	641*	803									
WRPERR	002310R	788	790	796#										
XPLAG	000005R	284#												
.	= 003512R	898#	979#	980#	982#	984#	986#							

. ABS. 000000 000
003512 001

ERRORS DETECTED: 0
DEFAULT GLOBALS GENERATED: 0

XADCB0, XADCB0/SOL/CRF:SYM=DDKCOM, XADCB0
RUN-TIME: 2 2 .3 SECONDS
RUN-TIME RATIO: 29/5=5.5
CORE USED: 7K (13 PAGES)