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IDENTIFICATION

PRODUCT CODE: AC-E935B-MC
PRODUCT NAME: CXLKABO LK11 MODULE
PRODUCT DATE: SEPTEMBER 1978
MAINTAINER: DEC/X11 SUPPORT GROUP

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1. ABSTRACT

"LKA" IS AN "IOMOD" THAT EXERCISES ONE LK11 PUSH BUTTON INTERFACE. INCLUDED IN THIS MODULE IS A LOGIC TEST OF THE INTERFACE. AFTER THE LOGIC TEST HAS BEEN PERFORMED, THE MODULE WILL LOAD A SEQUENCE OF DIFFERENT PATTERNS INTO THE LAMP DATA REGISTER. THESE DIFFERENT PATTERNS WILL AID IN DETECTING INOPERATIVE LAMPS IN THE BUTTON BOX. IF THE OPERATOR DEPRESSES A BUTTON AFTER THE MODULE HAS BEEN RUNNING, THE MODULE WILL ENTER A "COMPLEMENT LAST SWITCH VALUE" LOOP. THIS LOOP WILL BE EXITED IF NO BUTTON IS DEPRESSED FOR 10 SECONDS TO THE LAMP DATA TESTING.

2. REQUIREMENTS

HARDWARE: LK11 PUSH BUTTON INTERFACE AND BUTTON BOX.

STORAGE:: LKA REQUIRES:

1. DECIMAL WORDS: 1007
2. OCTAL WORDS: 01757
3. OCTAL BYTES: 3736

3. PASS DEFINITION

ONE PASS OF LKA MODULE CONSISTS OF ONE ITERATION OF THE EIGHT DIFFERENT LIGHT PATTERNS.

4. EXECUTION TIME

LKA RUNNING ALONE ON PDP-11/05 TAKES APPROXIMATELY ONE MINUTE.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

DEVADR: 160060, VECTOR: 360, BR1: 4

REQUIRED PARAMETERS:

NONE

6. DEVICE/OPTION SETUP

THE LK-11-A INTERFACE AND BUTTON BOX MUST BE CONNECTED.

7. MODULE OPERATION

THE MODULE WILL BEGIN BY TESTING THE ABILITY OF THE LDR (LAMP DATA REGISTER) PBR (PUSH BUTTON REGISTER) AND THE PBR (PUSH BUTTON CONTROL/STATUS) REGISTER TO FUNCTION PROPERLY.

THE MODULE WILL NOW START TESTING THE LAMPS IN THE PUSH BUTTON BOX BY LOADING DIFFERENT PATTERNS INTO THE LAMP DATA REGISTER.

8. OPERATION OPTIONS

NONE

9. NON STANDARD PRINTOUTS

ALL PRINTOUTS HAVE STANDARD MEANINGS AS REPRESENTED IN DEC/X11 DOCUMENTATION.

10. ENVIRONMENT

- #1 11/10 WITH 16K OF MEMORY
RK-11-D DISK CONTROLLER WITH 1 DRIVE
LK11 PUSH BUTTON OPTION
- #2 11/45 WITH 24K OF MEMORY (16K CORE + 8K MOS)
KT-11-D MEMORY MANAGEMENT
RK-11-D DISK CONTROLLER WITH 1 DRIVE
LK11 PUSH BUTTON OPTION

142 000000-
143 000000-
144
145
146
147
148 000000-
149 000000- 045514 041101 040
150 000005- 000
151 000006- 160000
152 000010- 000360
153 000011- 200
154 000013- 000
155 000014- 000601
156 000016- 000000
157 000020- 000000
158 000022- 000000
159 000024- 000000
160
161 000026- 140000
162 000030- 000276-
163 000032- 000000
164 000033- 000000
165 000036- 000117
166 000040- 000000
167 000042- 000000
168 000043- 000000
169 000046- 000000
170 000050- 000000
171 000052- 000000
172 000053- 000000
173 000055- 000000
174 000056- 000000
175 000060- 000000
176 000062- 000000
177 000064- 000000
178 000066- 000000
179 000070- 000000
180 000072- 000000
181 000074- 000000
182 000076- 000000
183 000100- 000000
184 000102- 000000
185 000104- 000000
186 000104- 000000
187 000104- 000000
188 000106- 000000
189 000106- 000000
190 000110- 000000
191 000112- 000320-
192 000114- 000000
193 000116- 000000
194 000120- 000000
195 000122- 000101
196 000040
197

IOMOD <LKAB> 160060 360 4 79 101
MODULE 140000 LKAB 160060 360 4 79 101
TITLE LKAB DEC/X11 SYSTEM EXERCISER MODULE
DDXCOM VERSION 6 23-NOV-78
LIST

BEGIN:
MODNAM: .ASCII /LKAB / ;MODULE NAME.
XFLAG: .BYTE OPEN ;USED TO KEEP TRACK OF WBUFF USAGE
ADDR: 160060+0 ;1ST DEVICE ADDR.
VECTOR: 360+0 ;1ST DEVICE VECTOR.
BR1: .BYTE PRTY4+0 ;1ST BR LEVEL-
BR2: .BYTE PRTY+0 ;2ND BR LEVEL-
DVID1: .I ;DEVICE INDICATOR 1.
SR1: OPEN ;SWITCH REGISTER 1
SR2: OPEN ;SWITCH REGISTER 2
SR3: OPEN ;SWITCH REGISTER 3
SR4: OPEN ;SWITCH REGISTER 4

STAT: 140000 ;STATUS WORD.
INIT: START ;MODULE START ADDR.
SPCNT: MODSP ;MODULE STACK POINTER.
PASS: 0 ;PASS COUNTER.
ICOUNT: 79. ;# OF ITERATIONS PER PASS=79.
SOFCNT: 0 ;LOC TO COUNT ITERATIONS
SDHPCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
SDHPCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
SDHPCNT: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
SDHPCNT: 0 ;LOC TO SAVE HARD ERRORS PER PASS
SYSERR: 0 ;# OF SYS ERRORS ACCUMULATED
RANUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
CONFIG: 0 ;RESERVED FOR MONITOR USE
RES1: 0 ;RESERVED FOR MONITOR USE
RES2: 0 ;RESERVED FOR MONITOR USE
RES3: 0 ;RESERVED FOR MONITOR USE
SVR0: OPEN ;LOC TO SAVE R0.
SVR1: OPEN ;LOC TO SAVE R1.
SVR2: OPEN ;LOC TO SAVE R2.
SVR3: OPEN ;LOC TO SAVE R3.
SVR4: OPEN ;LOC TO SAVE R4.
SVR5: OPEN ;LOC TO SAVE R5.
SVR6: OPEN ;LOC TO SAVE R6.
CSRA: OPEN ;ADDR OF CURRENT CSR.
SBRDR: OPEN ;ADDR OF GOOD DATA, OR
ACSR: OPEN ;CONTENTS OF CSR.
WASADR: OPEN ;ADDR OF BAD DATA, OR
ASAT: OPEN ;STATUS REG CONTENTS.
ERRTYP: 0 ;TYPE OF ERROR
ASB: OPEN ;EXPECTED DATA.
AWAS: OPEN ;ACTUAL DATA.
RSTRT: RESTR ;RESTART ADDRESS AFTER END OF PASS
WTD: OPEN ;WORDS TO MEMORY PER ITERATION
WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
INTR: OPEN ;# OF INTERRUPTS PER ITERATION
IDNUM: 101 ;MODULE IDENTIFICATION NUMBER=101
-REPT SPSIZ ;MODULE STACK STARTS HERE.
-LIST

198
199
200
201 000224-
202

-WORD 0
-LIST
-ENDR
MODSP:

```
203 ;ADDRESSES AND VECTORS
204
205
206 000224 000006 PBR: ADDR ;PUSH BUTTON STATUS REGISTER
207 000222 000010 PBR: ADDR+2 ;PUSH BUTTON DATA REGISTER
208 000230 000012 LDR: ADDR+4 ;LAMP DATA REGISTER
209
210 000232 000010 VECT: VECTOR ;LK-11-A INTERRUPT VECTOR
211 000234 000012 VECTR1: VECTOR+2
212
213 ;TEMP LOCATIONS
214
215 000236 000000 BITSAV: 0
216 000240 000000 TEMP1: 0
217 000242 000000 TEMP2: 0
218 000244 000000 TEMP3: 0
219 000246 000000 TEMP4: 0
220 000250 000000 UNEXPT: 0
221
222 000250 000004 000000 000256 ;IRQS,BEGIN,1$ ; QUEUE UP TO CONTINUE AT 1$ AND RTI
223
224 000256 012767 000011 177622 1$: MOV #11,ERRTYP ;ILLEGAL INTERRUPT
225 ;*****
226 000264 104405 000000 000000 ;RDERS,BEGIN,NULL ;UNEXPECTED INTERRUPT FROM LK-11-A
227 ;*****
228 000272 104410 000000 000000 ENDS,BEGIN ;
229
230
231 000276 012767 000001 177614 START: MOV #1,INTR ;1 INTERRUPT/ITERATION
232 000304 012767 000031 177604 MOV #25,WDPR ;25 WORDS FROM MEM/ITERATION
233 000312 012767 000031 177574 MOV #25,WDTO ;25 WORDS TO MEM/ITERATION
234
235 RESTR: MOV #0,NIP
236 000320 012767 177460 177674 MOV ADDR,PBR ;LOAD THE LK-11 ADDRESSES
237 000330 012767 177452 177672 MOV ADDR,LDR
238 000336 012767 177444 177662 MOV ADDR,PBR
239 000344 062767 000002 177654 ADD #2,PBR
240 000352 062767 000004 177650 ADD #4,LDR
241
242 000360 012767 177424 177644 MOV VECTOR,VECT ;LOAD THE LK-11 VECTOR
243 000364 012767 177416 177640 MOV VECTOR,VECTR1
244 000368 062767 000002 177632 ADD #2,VECTR1
245 000402 012777 000250 177622 MOV UNEXPT,@VECT ;LOAD FALSE INTR. RETURN
246 000410 005077 177620 CLR @VECTR1
```

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246 ;LOAD ALL 1'S AND READ BACK LDR
247 000414 012767 177777 177462 LDI: MOV #1,ASTAT ;LOAD EXPECTED DATA
248 000422 012767 177456 177600 MOV ASTAT,QLDR ;LOAD ALL ONES INTO LDR
249 000430 012767 177448 177444 MOV QLDR,ACSR ;READ ACTUAL DATA
250 000438 012767 177442 177436 CMP ASTAT,ACSR ;COMPARE RESULTS
251 000444 001406 BEQ LD25 ;BRANCH IF EQUAL
252 000446 012767 000025 177432 MOV #25,ERRTYP ;BIT STUCK
253 ;*****
254 000454 104405 000000 000000 ;RDERS,BEGIN,NULL ;FAILED TO LGAD ALL ONES
255 ;*****
256
257 ;LOAD AND READ "25" PATTERN INTO LDR
258 000462 012767 125252 177414 LD25: MOV #125252,ASTAT ;LOAD EXPECTED DATA
259 000470 012767 177410 177532 MOV ASTAT,QLDR ;LOAD PATTERN INTO LDR
260 000476 012767 177526 177376 MOV QLDR,ACSR ;READ ACTUAL DATA
261 000504 026767 177374 177370 CMP ASTAT,ACSR ;COMPARE RESULTS
262 000512 001406 BEQ LD52 ;BRANCH IF EQUAL
263 000514 012767 000025 177364 MOV #25,ERRTYP ;BIT STUCK
264 ;*****
265 000522 104405 000000 000000 ;RDERS,BEGIN,NULL ;FAILED TO LOAD PATTERN
266 ;*****
267
268 ;LOAD AND READ "52" PATTERN INTO LDR
269 000530 012767 052525 177346 LD52: MOV #252525,ASTAT ;LOAD EXPECTED DATA
270 000536 012767 177342 177464 MOV ASTAT,QLDR ;LOAD PATTERN INTO LDR
271 000544 012767 177460 177330 MOV QLDR,ACSR ;READ ACTUAL DATA
272 000552 026767 177326 177322 CMP ASTAT,ACSR ;COMPARE RESULTS
273 000560 001406 BEQ FLT1 ;BRANCH IF EQUAL
274 000562 012767 000025 177316 MOV #25,ERRTYP ;BIT STUCK
275 ;*****
276 000570 104405 000000 000000 ;RDERS,BEGIN,NULL ;FAILED TO LOAD PATTERN
277 ;*****
278
279 ;FLOAT A ONE THROUGH LDR
280 000576 012767 000001 177300 FLT1: MOV #BIT0,ASTAT ;LOAD EXPECTED DATA
281 000584 012767 177274 177316 MOV ASTAT,QLDR ;LOAD LDR BIT
282 000592 012767 177270 177262 MOV QLDR,ACSR ;READ ACTUAL DATA
283 000600 026767 177260 177254 CMP ASTAT,ACSR ;COMPARE RESULTS
284 000608 001406 BEQ 1$ ;BRANCH IF EQUAL
285 000616 012767 000025 177250 MOV #25,ERRTYP ;BIT STUCK
286 ;*****
287 000636 104405 000000 000000 ;RDERS,BEGIN,NULL ;FAILED TO SET LDR BIT
288 ;*****
289 000644 005167 177234 1$: ROL A,ASTAT ;TEST NEXT BIT
290 000650 103355 BCC 2$ ;CONTINUE THROUGH LDR
291
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322 000652 012767 177776 177224 ;FLOAT ZERO THROUGH LDR
323 000660 012767 177776 177224 ;LTO: MOV #177776,ASTAT ;LOAD EXPECTED DATA
324 000660 012767 177776 177224 ;25: MOV #1,CLR ;LOAD LDR BIT
325 000674 026767 177204 177200 MOV @LDR,ACSR ;READ ACTUAL DATA
326 000702 001406 ;CMP ASTAT,ACSR ;COMPARE RESULTS
327 000704 012767 000025 177174 BEQ #25,ERRTYP ;BRANCH IF EQUAL
328 ;*****
329 ;***** ;BIT STUCK
330 000712 104405 000000 000000 ;*****
331 ;***** ;FAILED TO CLEAR LDR BIT
332 ;*****
333 000720 006167 177160 ;15: ROL ASTAT ;TEST NEXT BIT
334 000724 103355 ;BCC #25 ;CONTINUE THROUGH LDR
335 ;*****
336 ;***** ;LOAD AND READ PBR USING BIT 0
337 ;*****
338 ;***** ;LOAD EXPECTED DATA
339 000726 012767 177777 177150 MOV #1,ASTAT ;LOAD EXPECTED DATA
340 000734 012767 000001 177264 MOV #10,PPBR ;SET BIT 0
341 000734 012767 177260 177132 MOV @PPBR,ACSR ;READ ACTUAL DATA
342 000750 026767 177130 177124 CMP ASTAT,ACSR ;COMPARE RESULTS
343 000756 001406 ;BEQ CLRBIT0 ;BRANCH IF EQUAL
344 000760 012767 000025 177120 ;MOV #25,ERRTYP ;BIT STUCK
345 ;*****
346 ;***** ;FAILED TO SET PBR
347 ;*****
348 ;***** ;CLEAR PBR USING BIT 0
349 ;*****
350 ;***** ;EXPECTED DATA
351 000774 005067 177104 CLR ASTAT ;CLEAR BIT 0
352 001000 012777 177776 177220 MOV #177776,PPBR ;LOAD EXPECTED DATA
353 001006 012767 177214 177066 MOV @PPBR,ACSR ;READ ACTUAL DATA
354 001014 026767 177064 177060 CMP ASTAT,ACSR ;COMPARE RESULTS
355 001024 012767 000025 177054 BEQ LDPB ;BRANCH IF EQUAL
356 ;MOV #25,ERRTYP ;BIT STUCK
357 ;*****
358 ;***** ;FAILED TO CLEAR PBR
359 ;*****
360 ;***** ;LOAD AND READ BACK PB FLAG
361 ;*****
362 ;***** ;CLEAR STATUS REGISTER
363 001040 005077 177160 LDPB: CLR @PBBSR ;CLEAR STATUS REGISTER
364 001044 012767 000000 177032 MOV #15,ASTAT ;LOAD EXPECTED DATA
365 001044 012767 177026 177132 MOV #15,PPBSR ;LOAD STATUS REG
366 001060 012767 177140 177014 MOV @PBBSR,ACSR ;READ ACTUAL DATA
367 001066 026767 177012 177006 CMP ASTAT,ACSR ;COMPARE RESULTS
368 001074 001406 ;BEQ PBCLR ;BRANCH IF EQUAL
369 001076 012767 000025 177002 ;MOV #25,ERRTYP ;BIT STUCK
370 ;*****
371 ;***** ;FAILED TO SET BIT 15
372 ;*****
373 001104 104405 000000 000000 ;*****
374 ;*****
```

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339 001112 005077 177106 ;TEST PB FLAG CLEARED BY CLR PBBSR
340 001116 005067 176762 ;PBCLR: CLR @PBBSR ;CLEAR STATUS REGISTER
341 001116 012767 176762 177074 CLR ASTAT ;LOAD EXPECTED DATA
342 001142 052777 100000 ;BIS #15,@PBBSR ;SET PB FLAG
343 001144 012767 177070 ;CLR @PBBSR ;CLEAR STATUS REGISTER
344 001144 012767 177064 ;MOV @PBBSR,ACSR ;READ ACTUAL DATA
345 001142 026767 176736 176732 ;CMP ASTAT,ACSR ;COMPARE RESULTS
346 001150 001406 ;BEQ PBCLRH ;BRANCH IF EQUAL
347 001152 012767 000025 176726 ;MOV #25,ERRTYP ;BIT STUCK
348 ;*****
349 ;***** ;FAILED TO CLEAR PB FLAG
350 ;*****
351 ;***** ;TEST PB FLAG CLEARED WHEN READ PBR H
352 ;*****
353 ;***** ;CLEAR STATUS REGISTER
354 001166 005077 177032 ;PBCLR: CLR @PBBSR ;CLEAR STATUS REGISTER
355 001172 005067 176706 ;CLR ASTAT ;LOAD EXPECTED DATA
356 001176 012767 177026 177020 ;MOV #15,@PBBSR ;SET PB FLAG
357 001176 012767 177026 177026 ;MOV @PBBSR,TEMP ;READ PB REGISTER
358 001212 012767 177006 176662 ;MOV @PBBSR,ACSR ;READ ACTUAL DATA
359 001220 026767 176660 176654 ;CMP ASTAT,ACSR ;COMPARE RESULTS
360 001226 001406 ;BEQ PBCLR ;BRANCH IF EQUAL
361 001230 012767 000025 176650 ;MOV #25,ERRTYP ;BIT STUCK
362 ;*****
363 ;***** ;FAILED TO CLEAR PB FLAG
364 ;*****
365 ;***** ;TEST PB FLAG IS CLEARED BY READ STATUS WHEN INTR ENABLE IS CLEARED
366 ;*****
367 ;***** ;CLEAR STATUS REG
368 001244 005077 176754 ;PBCLR: CLR @PBBSR ;CLEAR STATUS REG
369 001244 005067 176630 ;CLR ASTAT ;LOAD EXPECTED DATA
370 001244 012767 176630 176742 ;MOV #15,@PBBSR ;SET PB FLAG
371 001252 012767 176736 176750 ;MOV @PBBSR,TEMP ;READ SR
372 001270 012767 176730 176604 ;MOV @PBBSR,ACSR ;READ ACTUAL DATA
373 001276 026767 176602 176576 ;CMP ASTAT,ACSR ;COMPARE RESULTS
374 001304 001406 ;BEQ LDIE ;BRANCH IF EQUAL
375 001306 012767 000025 176572 ;MOV #25,ERRTYP ;BIT STUCK
376 ;*****
377 ;***** ;READ SR FAILED TO CLEAR PB FLAG
378 ;*****
379 ;***** ;LOAD AND READ INTERRUPT ENABLE BIT, ALSO UNEXPECTED INTRPT TEST
380 ;*****
381 ;***** ;CLEAR PB STATUS REGISTER
382 001322 005077 176676 ;LDIE: CLR @PBBSR ;CLEAR PB STATUS REGISTER
383 001326 005077 040000 ;CLR @PBR ;CLEAR BUTTON REGISTER
384 001326 012767 040000 176544 ;MOV #11,ASTAT ;LOAD EXPECTED DATA
385 001340 016777 176540 176536 ;MOV ASTAT,@PBBSR ;LOAD STATUS REGISTER
386 001346 012767 176524 176526 ;MOV @PBBSR,ACSR ;READ ACTUAL DATA
387 001354 026767 176524 176520 ;CMP ASTAT,ACSR ;COMPARE RESULTS
388 001364 001406 ;BEQ TSBIC ;BRANCH IF EQUAL
389 001372 012767 000025 176514 ;MOV #25,ERRTYP ;BIT STUCK
390 ;*****
391 ;***** ;FAILED TO LOAD INTERRUPT ENABL
392 ;*****
```

```
393 001400 005077 176624 ;TEST BITC INSTRUCTION CLEARS LDR
395 001404 012767 000001 176624 ;TSBITC: CLR QLDR ;CLEAR LAMP DATA REGISTER
396 001412 012777 177777 176610 2S: MOV #BIT0,BITSAV ;INITIALIZE BITSAV
397 001420 012787 177777 176425 MOV #1,QLDR ;SET REGISTER TO ALL 1'S
398 001428 046787 176404 176425 MOV #ASTAT,ASTAT ;EXPECTED DATA
399 001434 046777 176576 176566 BIC BITSAV,QLDR ;DATA
400 001442 012767 176562 176432 MOV QLDR,ACSR ;CLEAR PCSR BIT
401 001450 025787 176430 176424 CMP ASTAT,ACSR ;READ ACTUAL DATA
402 001456 001406 ;COMPARE RESULTS
403 001460 012767 000025 176420 BEQ #25,ERRTYP ;BRANCH IF EQUAL
404 ;*****ERRTYP***** ;BIT STUCK
405 001466 104405 000000 000000 ;*****ERRTYP***** ;FAILED TO CLEAR LDR BIT
406 ;*****ERRTYP***** ;FAILED TO CLEAR LDR BIT
407 ;*****ERRTYP***** ;FAILED TO CLEAR LDR BIT
408 001474 006167 176536 1S: ROL BITSAV ;SHIFT TO TEST NEXT BIT
409 BCC #25 ;BRANCH IF NOT DONE ALL BITS
410
411 ;TEST BITS INSTRUCTION SETS LDR
412 ;TSBITS: MOV #BIT0,BITSAV ;INITIALIZE BITSAV
413 001502 012767 000001 176526 2S: CLR QLDR ;CLEAR LAMP DATA REGISTER
414 001510 005077 176514 176362 MOV BITSAV,ASTAT ;DATA
415 001518 012787 176510 176500 BIC BITSAV,QLDR ;SET LDR BIT
416 001526 052787 176494 176344 MOV QLDR,ACSR ;READ ACTUAL DATA
417 001534 026767 176342 176336 CMP ASTAT,ACSR ;COMPARE RESULTS
418 001542 001406 ;BRANCH IF EQUAL
419 001546 012767 000025 176332 BEQ #25,ERRTYP ;BIT STUCK
420 ;*****ERRTYP***** ;BIT STUCK
421 ;*****ERRTYP***** ;BIT STUCK
422 001554 104405 000000 000000 ;*****ERRTYP***** ;FAILED TO SET LDR BIT
423 ;*****ERRTYP***** ;FAILED TO SET LDR BIT
424 ;*****ERRTYP***** ;FAILED TO SET LDR BIT
425 001562 006167 176450 1S: ROL BITSAV ;SHIFT TO TEST NEXT BIT
426 BCC #25 ;BRANCH BACK IF NOT DONE ALL BITS
427
428 ;TEST BISS INSTRUCTION SETS LDR
429 ;TSBISB: CLR QLDR ;CLEAR LAMP DATA REGISTER
430 001570 005077 176434 176426 MOV #2525,QLDR ;LOAD ALTERNATING 1'S IN LDR
431 001578 012777 052525 176274 MOV #ASTAT,ASTAT ;EXPECTED DATA
432 001586 152777 177777 176412 BISB #1,QLDR ;SET LOWER BYTE TO ALL 1'S
433 001594 012767 176406 176256 MOV QLDR,ACSR ;READ ACTUAL DATA
434 001602 025787 176254 176250 CMP ASTAT,ACSR ;COMPARE RESULTS
435 001610 001406 ;BRANCH IF EQUAL
436 001614 012767 000025 176244 BEQ #25,ERRTYP ;BIT STUCK
437 ;*****ERRTYP***** ;BIT STUCK
438 001622 104405 000000 000000 ;*****ERRTYP***** ;FAILED TO SET LOWER BYTE & LEAVE UPPER BYTE THE SAME
439 ;*****ERRTYP***** ;FAILED TO SET LOWER BYTE & LEAVE UPPER BYTE THE SAME
440 001630 012777 052525 176352 1S: MOV #2525,QLDR ;LOAD ALTERNATING 1'S IN LDR
441 001638 016767 176346 176362 MOV LDR,TEMPL ;GET ADDRESS OF LDR
442 001646 005267 176356 ;GET ADDRESS OF UPPER BYTE OF LDR
443 001654 052787 177777 176206 MOV #177525,ASTAT ;EXPECTED DATA
444 001662 152777 177777 176342 BISSB #1,TEMPL ;SET UPPER BYTE TO ALL 1'S
445 001704 012767 176320 176170 MOV QLDR,ACSR ;READ ACTUAL DATA
446 001712 025787 176166 176162 CMP ASTAT,ACSR ;COMPARE RESULTS
447 001720 001406 ;BRANCH IF EQUAL
448 001722 012767 000025 176156 BEQ #25,ERRTYP ;BIT STUCK
449 ;*****ERRTYP***** ;BIT STUCK
450 ;*****ERRTYP***** ;BIT STUCK
451 001730 104405 000000 000000 ;*****ERRTYP***** ;FAILED TO SET UPPER BYTE & LEAVE LOWER BYTE THE SAME
452 ;*****ERRTYP***** ;FAILED TO SET UPPER BYTE & LEAVE LOWER BYTE THE SAME
```

```
450 )TEST BICB INSTRUCTION CLEARS LDR
451 TSBICB: CLR @LDR ;CLEAR LAMP DATA REGISTER
452 MOV @S25,@LDR ;LOAD ALTERNATING 1'S IN LSR
453 BICB @S1,@ASTAT ;EXPECTED DATA
454 MOV @S25,@LDR ;CLEAR LOWER BYTE OF LDR
455 BICB @S1,@ASTAT ;READ ACTUAL DATA
456 MOV @LDR,@ACSR ;COMPARE RESULTS
457 CMP @ASTAT,@ACSR ;BRANCH IF EQUAL
458 BEQ @S ;BIT STUCK
459 MOV @S,@ERRTYP ;BIT STUCK
460 )*****
461 002010 104405 000000 000000
462 HRDRS,BEGIN,NULL ;FAILED TO CLEAR LOWER BYTE & LEAVE UPPER BYTE THE SAME
463 )*****
464 1S: MOV @S25,@LDR ;LOAD ALTERNATING 1'S IN LDR
465 MOV @LDR,@EAPL ;GET ADDRESS OF LDR
466 INC @EAPL ;GET ADDRESS OF UPPER BYTE OF LDR
467 MOV @S1,@ASTAT ;EXPECTED DATA
468 BICB @S1,@EAPL ;READ UPPER BYTE OF LDR
469 MOV @LDR,@ACSR ;COMPARE RESULTS
470 CMP @ASTAT,@ACSR ;BRANCH IF EQUAL
471 BEQ @S ;BIT STUCK
472 MOV @S,@ERRTYP ;BIT STUCK
473 )*****
474 002076 104405 000000 000000
475 HRDRS,BEGIN,NULL ;FAILED TO CLEAR UPPER BYTE & LEAVE LOWER BYTE THE SAME.
476 )*****
477 )TEST THAT PBR AND LDR DON'T CHANGE
478 NOCHN1: CLR @PBR ;CLEAR STATUS REGISTER
479 CLR @ASTAT ;LOAD EXPECTED DATA
480 MOV @S25,@LDR ;LOAD PATTERN INTO LDR
481 MOV @LDR,@ACSR ;READ LDR DATA
482 CMP @ASTAT,@ACSR ;COMPARE RESULTS
483 BEQ @S ;GO TO NEXT SECTION
484 MOV @S,@ERRTYP ;BIT STUCK
485 )*****
486 002146 104405 000000 000000
487 HRDRS,BEGIN,NULL ;PBR CHANGED IN ERROR
488 )*****
489 1S: CLR @LDR ;CLEAR LDR
490 MOV @S15,@PBR ;SET BIT 15 IN PBR
491 MOV @LDR,@ACSR ;READ LDR DATA
492 CMP @ASTAT,@ACSR ;COMPARE RESULTS
493 BEQ @S ;GO TO NEXT TEST
494 MOV @S,@ERRTYP ;BIT STUCK
495 )*****
496 002212 104405 000000 000000
497 HRDRS,BEGIN,NULL ;LDR CHANGED IN ERROR
498 )*****
```

```
497 )TEST THAT PBR AND LDR DON'T CHANGE
498 NOCHN2: CLR @PBR ;CLEAR PBR
499 MOV @S15,@PBR ;SET ALL 1'S IN PBR
500 MOV @S1777,@ASTAT ;LOAD EXPECTED DATA
501 MOV @S25,@LDR ;LOAD PATTERN INTO LDR
502 MOV @LDR,@ACSR ;READ PBR
503 CMP @ASTAT,@ACSR ;COMPARE RESULTS
504 BEQ @S ;GO TO NEXT SECTION
505 MOV @S,@ERRTYP ;BIT STUCK
506 )*****
507 002272 104405 000000 000000
508 HRDRS,BEGIN,NULL ;PBR CHANGED IN ERROR
509 )*****
510 1S: CLR @LDR ;CLEAR LDR
511 CLR @PBR ;CLEAR PBR
512 CLR @ASTAT ;LOAD EXPECTED DATA
513 MOV @BIT0,@PBR ;SET PBR TO ALL ONES
514 MOV @LDR,@ACSR ;READ LDR
515 MOV @LDR,@ACSR ;READ LDR
516 CMP @ASTAT,@ACSR ;COMPARE RESULTS
517 BEQ @S ;GO TO NEXT TEST
518 MOV @S,@ERRTYP ;BIT STUCK
519 )*****
520 002354 104405 000000 000000
521 HRDRS,BEGIN,NULL ;LDR CHANGED IN ERROR
522 )*****
523 )TEST THAT PBR AND PBR DON'T CHANGE
524 NOCHN3: CLR @PBR ;CLEAR PBR
525 CLR @ASTAT ;LOAD EXPECTED DATA
526 MOV @S15,@PBR ;LOAD PATTERN INTO PBR
527 MOV @PBR,@ACSR ;READ PBR
528 CMP @ASTAT,@ACSR ;COMPARE RESULTS
529 BEQ @S ;GO TO NEXT SECTION
530 MOV @S,@ERRTYP ;BIT STUCK
531 )*****
532 002424 104405 000000 000000
533 HRDRS,BEGIN,NULL ;PBR CHANGED IN ERROR
534 )*****
535 1S: CLR @PBR ;CLEAR PBR
536 MOV @BIT0,@PBR ;SET PBR TO ALL ONES
537 MOV @PBR,@ACSR ;READ PBR
538 BIC @S16,@ACSR ;MASK "ENCODER" BITS
539 CMP @ASTAT,@ACSR ;COMPARE RESULTS
540 BEQ @S ;BR IF NO ERROR
541 MOV @S,@ERRTYP ;BIT STUCK
542 )*****
543 002476 104405 000000 000000
544 HRDRS,BEGIN,NULL ;PBR CHANGED IN ERROR
545 )*****
546 2S: CLR @PBR ;CLEAR BUTTON REGISTER
547 CLR @LDR ;CLEAR LAMPS
548 CLR @PBR ;CLEAR STATUS
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002520 017767 002656 000172
002521 017767 000116 000116
002524 104413 000000
002540 012767 001000 000106
002541 016777 000100 175454
002544 104407 000000
002545 104407 000000
002546 005367 000064
002572 062767 000002 000050
002600 005777 175420
002604 100320 175414 175424
002605 016777 175420 175406
002612 012767 001000 000024
002630 104407 000000
002631 104407 000000
002640 005367 000010
002644 100371
002646 000754

```
;LOAD PATTERNS INTO THE LIGHTS FROM A TABLE  
LIGHT: MOV #TABLE,10S  
13: MOV #10S,11S  
      DEC 12S  
      ENDS,BEGIN  
      ;LOAD POINTER  
      ;GET A VALUE  
      ;SIGNAL END OF ITERATION.  
      ;MONITOR SHALL TEST END OF PASS  
      ;LOAD A COUNTER  
      ;LOAD THE LIGHT REGISTER  
2S: MOV #BIT9,12S  
      MOV #11S,8LDR  
      BREAKS,BEGIN  
      BREAKS,BEGIN  
      DEC 12S  
      BPL 13S  
      ADD #2,10S  
      TST #PRSR  
      BPL 15S  
      MOV #PRR,TEMP  
      MOV TEMP,8LDR  
      MOV #BIT9,12S  
      BREAKS,BEGIN  
      BREAKS,BEGIN  
      DEC 12S  
      BPL 4S  
      BR 3S  
10S: 0  
11S: 0  
12S: 0
```

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002656 000000
002657 000000
002658 000000
002659 000000
002660 000000
002661 000000
002662 000000
002663 000000
002664 000000
002665 000000
002666 000000
002667 000000
002668 000000
002669 000000
002670 000000
002671 000000
002672 000100
002673 000200
002674 000200
002675 000400
002676 000400
002677 000800
002678 002000
002679 004000
002680 010000
002681 020000
002682 040000
002683 100000
002716 174637
002717 174637
002718 174637
002722 003140
002723 003140
002724 003140
002725 122525
002726 122525
002727 52525
002728 52525
002729 52525
002730 7417
002731 7417
002732 170360
002733 170360
002734 170360
002735 170360
002742 170360
002744 170360
002746 000001
002750 000022
002752 000444
002754 011110
002756 022200
002760 044400
002762 100000
002764 000010
002765 000204
002766 004102
002772 102041
002774 041020
002776 020400
003000 010000

```
;TABLE OF VALUES TO BE LOADED INTO THE "LAMP DATA REGISTER"  
TABLE:  
      BIT0  
      BIT1  
      BIT2  
      BIT3  
      BIT4  
      BIT5  
      BIT6  
      BIT7  
      BIT8  
      BIT9  
      BIT10  
      BIT11  
      BIT12  
      BIT13  
      BIT14  
      BIT15  
      174637  
      174637  
      174637  
      3140  
      3140  
      3140  
      122525  
      122525  
      52525  
      52525  
      52525  
      7417  
      7417  
      170360  
      170360  
      BIT0  
      BIT4 BIT1  
      BIT8 BIT5 BIT2  
      BIT12 BIT9 BIT6 BIT3  
      BIT13 BIT10 BIT7  
      BIT14 BIT11  
      BIT15  
      BIT3  
      BIT7 BIT2  
      BIT11 BIT6 BIT1  
      BIT15 BIT10 BIT5 BIT0  
      BIT14 BIT9 BIT4  
      BIT13 BIT8  
      BIT12
```

630	003002	100000	BIT15
631	003003	044000	BIT14
632	003004	022000	BIT13
633	003005	011110	BIT12
634	003010	000444	BIT11
635	003011	000022	BIT10
636	003012	000001	BIT9
637	003016	000000	BIT8
638	003020	010000	BIT7
639	003021	020400	BIT6
640	003022	041020	BIT5
641	003023	102041	BIT4
642	003026	004102	BIT3
643	003030	000204	BIT2
644	003033	000010	BIT1
645	003034	000001	BIT0
646	003036	000001	BIT0
647	003040	000002	BIT1
648	003041	000004	BIT2
649	003042	000010	BIT3
650	003044	000020	BIT4
651	003046	000040	BIT5
652	003050	004000	BIT6
653	003051	010000	BIT7
654	003054	040000	BIT8
655	003056	020000	BIT9
656	003060	010000	BIT10
657	003061	000000	BIT11
658	003064	000020	BIT12
659	003066	000040	BIT13
660	003070	000100	BIT14
661	003071	000200	BIT15
662	003074	001000	BIT16
663	003076	177777	BIT17
664	003078	177777	BIT18
665	003102	177777	BIT19
666	003104	000000	BIT20
667	003106	177777	BIT21
668	003110	000000	BIT22
670			BIT23
671	003112	123456	BIT24
672	003114	000310	BIT25
673	003134	123456	BIT26
674		000001	BIT27

```

-1
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-1
-1
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-1
0
123456 ;TERM
-BLKN 200
-MURD 123456
-END

```

CROSS REFERENCE TABLE -- USER SYMBOLS														
ACSR	000102R	185#	249*	250	260*	261	271*	272	282*	283	295*	296*	308*	309
		319*	320	331*	332	344*	345	358*	359	372*	373	385*	386*	400*
		401*	481*	417	418*	432*	443*	444	456*	457	468*	469	481*	482
		490*	491	504*	505	516*	517	529*	529*	530	538*	539*	540	540*
ADDR	000006R	151	206	207	208	235	236	237						
ADDR22=	001000	203												
ASB	000106R	187*	247*	248	250	258*	259	261	269*	270	272	280*	281	283
ASTAT	000104R	289	293*	294	296	302*	309	317*	320	320	329*	330	332	341*
		345*	355*	359	360*	373	383*	384	386	397*	398*	401	414*	417
		378*	384	441*	442	454*	457	466*	469	479*	482	491	502*	505
		513*	516	527*	527*	530	540							
AWAS	000110R	190												
BEGIN	000000R	148	222	226	228	254	265	276	287	300	313	324	336	349
		161*	377	390	405	421	436	448	461	473	486	495	509	521
		162*	344	350	359	360	370	384	397	412*	414	423*	423*	423*
BITSAV	000236R	215*	395*	398	399	407*	412*	414	415	423*	423*	423*	423*	423*
BIT0	000001	203	280	307	395	501	501	514	537	584	614	625	637	642
BIT1	000002	203	585	615	624	636	643	648						
BIT10	002000	203	594	618	625	633	642	661						
BIT11	004000	203	595	619	624	632	643	652						
BIT12	010000	203	596	618	625	634	639	656						
BIT13	020000	203	597	618	625	633	640	655						
BIT14	040000	203	598	619	626	632	641	655						
BIT15	100000	203	329	342	356	370	489	599						
BIT2	000004	203	586	616	623	635	644	649						
BIT3	000010	203	587	617	623	634	645	650						
BIT4	000020	203	588	615	626	636	641	658						
BIT5	000040	203	589	616	625	635	642	659						
BIT6	000100	203	590	617	624	634	643	660						
BIT7	000200	203	591	618	623	633	644	661						
BIT8	000400	203	592	616	627	635	640	661						
BIT9	001000	203	556	568	593	617	626	634	641	662				
BREAK	104407R	153	559	560	570	571								
BR1	000012R	154												
BR2	000013R	154												
BTODS	104421	203												
CDATA	104412	203												
CLBIT0	000774R	173	317#											
CONFIG	000056R	183												
CSRA	000100R	183												
DAIACK	104411	203												
DATER	104404	203												
DVID1	000014R	155												
ENDITS	104413	203	554											
ENDS	104410	203	428											
ERRYP	000106R	188	403*	252*	263*	274*	285*	298*	311*	322*	334*	347*	361*	375*
		188		419*	434*	446*	459*	471*	484*	493*	507*	519*	532*	542*
EXITS	104400	203												
FLTD	000652R	293												
FLT	000576R	273	280#											
CRTPAS	104418	203												
CWBUF	104414	203												
HRDCNT	000044R	168												
HRDRS	104405	203	226	254	265	276	287	300	313	324	336	349	363	377

HRDPAS	000050R	390#	405	421	436	448	461	473	486	495	509	521	534	544
ICDNT	000036R	169#												
ICOUNT	000040R	166#												
IDNUM	000122R	195#												
INT	000030R	194#												
INTR	000120R	194#												
LDIE	001322R	374#												
LDPB	001040R	322#												
LDR	000230R	251#												
LD1	000414R	243#												
LD2	000462R	253#												
LD5	000530R	262#												
LIGHT	002520R	553#												
HAP22 =	104416	203#												
HODMAN	000000R	149#												
HODSP	000224R	163#												
HSGNS =	104403	403#												
HSGS =	104402	403#												
HSGS =	104401	403#												
NOCHN1	002104R	470#												
NOCHN2	002220R	498#												
NOCHN3	002220R	498#												
HULL	000006R	208#												
OPEN	000000	390#												
OTOAS =	104420	203#												
PASCNT	000034R	164#												
PBCLR	001142R	333#												
PBCLRD	001142R	333#												
PBCLRH	001166R	346#												
PBR	000226R	207#												
PBSR	000224R	207#												
PIRQS =	000004	564#												
POPSP =	022626	203#												
PRIV	000000	154#												
PRIV0	000000	203#												
PRIV1	000040	203#												
PRIV2	000100	203#												
PRIV3	000140	203#												
PRIV4	000200	153#												
PRIV5	000240	203#												
PRIV6	000300	203#												
PRIV7	000340	203#												
PS	177776	203#												
PSM	177776	203#												
PUSH	005746	203#												
PUSH2	024646	203#												

RANDS =	104417	203#												
RANNDM	000054R	172#												
RESTR	000320R	191#												
RES1	000656R	174#												
RES2	000060R	175#												
RSTR	000112R	191#												
SBADR	000102R	184#												
SDFCNT	000442R	164#												
SDFPAC	104402	203#												
SDFPAS	000046R	169#												
SPOINT	000032R	163#												
SPSIZ =	000040	159#												
SR1	000016R	158#												
SR2	000020R	157#												
SR3	000022R	158#												
SR4	000024R	159#												
START	000274R	163#												
STAT	000026R	164#												
SVRO	000062R	176#												
SVR1	000064R	177#												
SVR2	000066R	178#												
SVR3	000070R	179#												
SVR4	000072R	180#												
SVR5	000074R	181#												
SVR6	000076R	182#												
SYSCNT	000052R	177#												
TABLE	002556R	552#												
TEMP	000246R	418#												
TEMP1	000246R	417#												
TEMP2	000244R	418#												
TRPDF =	000022	203#												
TSBTCB	001436R	445#												
TSBTCB	001570R	427#												
TSBTCB	001400R	387#												
TSBTCB	001502R	412#												
UNEXPT	000250R	226#												
VECT	000232R	210#												
VECTOR	000010R	152#												
VECTR1	000234R	211#												
WASADR	000104R	181#												
WDFR	000116R	193#												
WDT0	000114R	192#												
XFLAG	000009R	159#												
.	003736R	672#												

ABS. 000000 000
 003736 001

ERRORS DETECTED: 0
 DEFAULT GLOBALS GENERATED: 0
 XLKABO,XLKABO/SQL/CRF:SYM=DDXCOM,XLKABO
 RUN-TIME: 1 2 .3 SECONDS

LKAB DEC/X11 SYSTEM EXERCISER MODULE
XLKAB0.P11 12-OCT-78 12:07
RUN-TIME RATIO: 24/4=5.4
CORE USED: 7K (13 PAGES)

MACY11 30A(1052) 12-OCT-78 16:48 PAGE 22
CROSS REFERENCE TABLE -- USER SYMBOLS

SEQ 0020