

DataGeneral

**TECHNICAL
STATEMENT**

TEXT LISTING

068-000101-09

PROGRAM

MEMORY CHECKERBOARD IV

TEXT TAPE

097-000101-09

ABSTRACT

CHECKERBOARD IV IS A MAINTENANCE PROGRAM DESIGNED TO PRODUCE WORST NOISE CONDITIONS ON THE SENSE/INHIBIT WIRES. THE PROGRAM SHOULD BE RUN TO INSURE PROPER OPERATION OF THE SENSE AMPS, INHIBIT DRIVERS, AND MEMORY CURRENTS.

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0001 *MAIN          MACHD REV 06.30          09:07:25 06/15/78
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? NAME: CHECKIV.TX          PART NUMBER: 097-000101
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? DESCRIPTION: CHECKERBOARD IV
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? REVISION HISTORY:
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? REV.          DATE
? 00           05/21/73
? 01           01/25/74
? 02           02/22/74
? 03           06/07/74
? 04           04/18/75
? 05           06/11/76
? 06           12/31/76
? 07           03/18/77
? 08           09/02/77
? 09           08/18/78
?
? COPYRIGHT (C) DATA GENERAL CORPORATION, 1973, 1974, 1975, 1976,
? 1977, 1978.
? ALL RIGHTS RESERVED.
? LICENSED MATERIAL-PROPERTY OF DATA GENERAL CORPORATION.
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? 0.0 REVISION HISTORY
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? REV. 09 WAS CREATED TO
? IMPLEMENT THE STANDARDS PROVIDED
? BY DLLB.
? THIS HAS NOT CHANGED THE PHILOSOPHY
? OR TEST PROCEDURES IN THIS PROGRAM.
? ALL UNNECESSARY "IORS" HAVE BEEN
? DELETED FROM THIS FILE.
?
? CHECKERBOARD IV
?
? 1. ABSTRACT
? CHECKERBOARD IV IS A MAINTENANCE PROGRAM DESIGNED
? TO PRODUCE WORST CASE NOISE CONDITIONS ON THE
? SENSE/INHIBIT WIRES. THE PROGRAM SHOULD BE RUN
? TO INSURE PROPER OPERATION OF SENSE AMPS, INHIBIT
? DRIVERS, AND MEMORY CURRENTS.
?
? 2. MACHINE REQUIREMENTS
? 2.1 NOVA/SUPERNOVA/ECLIPSE PROCESSOR
? 2.2 4K READ/WRITE MEMORY
?
? 3. SWITCH SETTINGS
?
? LOCATION "SWREG" IS USED TO SELECT THE PROGRAM OPTIONS
? (NOT SYSTEM CONFIGURATION), WHILE RUNNING UNDER DTOS,
? THIS LOCATION WILL BE LOADED BY THE MONITOR.
? HOWEVER UNDER STAND ALONE AND PROGRAM LOAD MODES THIS
? LOCATION WILL BE SET ACCORDING TO THE ANSWERS SUPPLIED
? BY THE OPERATOR. IN ANY CASE THE OPTIONS CAN BE CHANGED
? OR VERIFIED BY USING ONE OF THE COMMANDS GIVEN IN SEC.
? 3.2
?
? 3.1 SWITCH OPTIONS
? DIFFERENT BITS AND THEIR INTERPRETATION AT LOCATION
? "SWREG" IS AS FOLLOWS:
?
? BIT      OCTAL  BINARY  INERPRETATION
? VALUE    VALUE
? 1         40000  1     LOOP ON ERROR
?          1     SKIP LOOPING ON ERROR
? 2         20000  1     PRINT TO CONSOLE
?          1     ABORT PRINT OUT TO CONSOLE
? 3         10000  1     DO NOT PRINT % FAILURE
?          1     PRINT % FAILURE
? 4         04000  1     ALLOW END OF PASS PRINT OUT
?          1     SUPPRESS END OF PASS PRINT OUT
? 5         0     DO NOT PRINT ON THE LINE PRINTER

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10005 .MAIN

0006 .MAIN
**00000 TOTAL ERRORS, 00000 PASS 1 ERRORS

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PROGRAM OUTPUT/ERROR DESCRIPTION
15. WHEN AN ERROR OCCURS, THE PROGRAM WILL EXIT TO
15.1 THE OOT ROUTINE (OCTAL DEBUG TOOL). EXAMINATION
: OF CELL 4A WILL PROVIDE THE CONTENTS OF THE PC
: AT THE TIME THE ERROR OCCURRED. CHECK LISTING
: FOR CAUSE OF ERROR.
15.2 SYNC PULSES.
: A "sp" PULSE (A74) IN STORE CYCLE.

PROGRAM DESCRIPTION
16. STORE THE CHECKERBOARD PATTERN
16.1 IF BIT C (12=1) DISTURB THE CONTENTS OF MEMORY BY
16.2 REFERENCING LOCATIONS 0101,0202,0303,ETC. 512
: TIMES. THIS PRODUCES 1024 READ/WRITE DISTURBS.
16.3 CHECK THE PATTERN WORD
16.4 COMPLEMENT AND CHECK THE WORD
16.5 RESTORE THE WORD
16.6 WHEN THE END OF THE PATTERN IS REACHED THE
: PROGRAM COMPLEMENTS THE PATTERN WORD AND RE-
: TURNS TO STEP 6.1 .

PROGRAM DESCRIPTION
17. THE PROGRAM SEQUENTIALLY USES:
: 1) A SLIDING FLOATING ONE BIT
: 2) A SLIDING FLOATING ZERO BIT

18. LIMITATIONS
: NONE

.EOT

0007 .MAIN

S:MPD 000000 MC 2/27