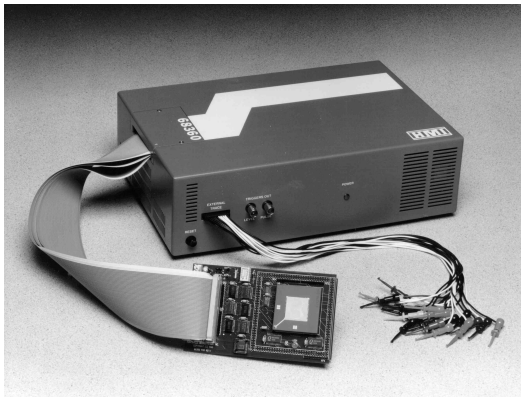
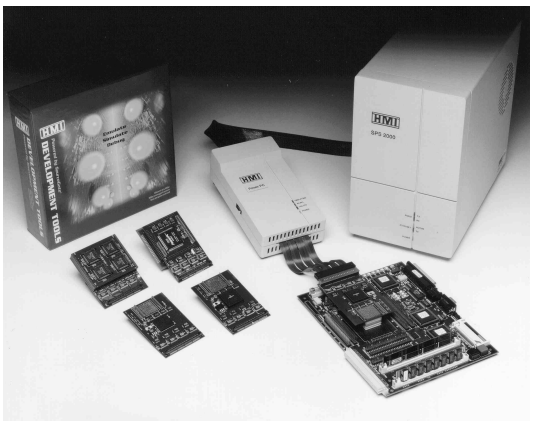
Product Overview

Huntsville Microsystems, Inc. (HMI) offers a broad range of products to the embedded system developer. Current product offerings include HMI-200 Series in-circuit emulators, Background Mode Debuggers (BMDs), CPU Simulators, and the recently introduced, state-of-the-art, SPS-2000 Series in-circuit emulators. All HMI products are driven by SourceGate II, a native GUI source-level debugger that provides a common user interface for Windows 3.1x/95/NT, Sun/Sparc, and HP workstations. This common user interface approach eliminates the need for the user to relearn a new interface when changing processors or host platforms.

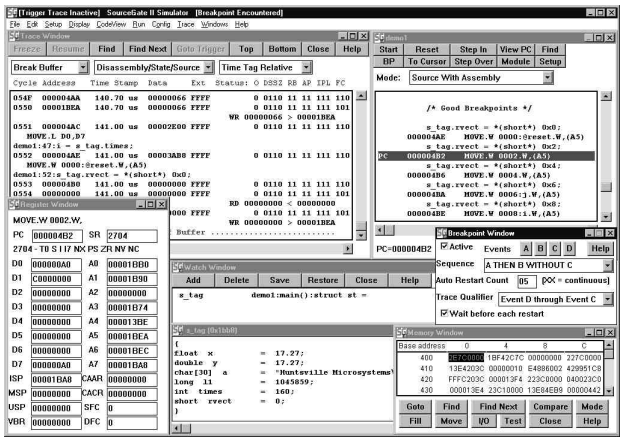


HMI-200 Series in-circuit emulators are proven tools that include such features as dual trace buffers that can be viewed during emulation, hardware-based software performance and code coverage analysis, complex breakpoint and triggering logic, overlay memory, 16 external trace leads for logic analyzer functionality, pulse and level trigger outputs, and advanced source-level debugging support. Available for the following devices: 64180/Z180, 6809, 68HC11, 68HC16, 68000, 68020, 68030, 68040, 68302, 68306, 68307, 68330, 68331, 68332, 68333, 68340, 68349, 68356, 68360, 8051, 8085, 8096, and Z80.

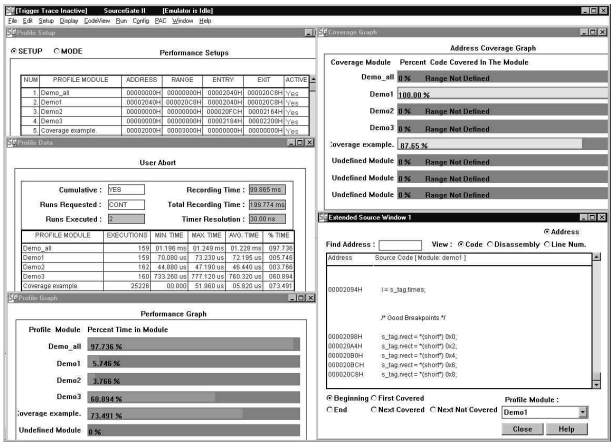
SPS-2000 Series in-circuit emulators are HMI's next generation product and provide all of the features of the HMI-200 Series with the addition of a multilevel sequencer for processing ultra-complex breakpoint and triggering conditions. Up to 8 levels of sequence logic can be defined consisting of address and/or data values, processor status bits and external signals. These conditions can then be used to perform specific tasks such as breaking emulation, capturing specific information in a trace buffer, or triggering other test equipment. A 128K trace buffer can be configured to display up to 8192 separate buffers of interest. Custom back-plane based design results in a rugged, reliable, and easy to service system. Available for the following devices: 68060, MPC505, MPC8xx, and IBM40x.



Background Mode Debuggers (BMDs) provide a powerful, extremely low-cost ***($199 Windows/$299 Unix including SourceGate)*** development solution that takes advantage of the debug port capability of select devices. BMDs include the ability to set up to 128 software breakpoints, single-step at the source or assembly level, and the capability of defining and viewing watch variables. All members of the BMD family provide support for programming of Flash devices. Available for the following devices: 68330, 68331, 68332, 68333, 68340, 68349, 68360, MCF520x, MPC505, MPC8xx, and IBM40x.



CPU Simulators come with a versatile scripting language that allows memory and I/O devices to be accurately simulated. A debug console can also be defined allowing the user to see system output messages and enter input to the system being simulated. Processor exceptions and interrupts can be simulated as well. Simulated trace and performance analysis are also provided. Available for the following devices: 68030, 68331, 68332, 68340, 68349, and 68360.



SourceGate II, HMI's acclaimed source-level debugger, is a common user interface for all HMI products. SourceGate II provides the advanced features expected by today's demanding embedded developer. Features like CodeView windows that control system operation from within the actual software module of interest. Multiple CodeView windows can be open at any time allowing single-step and breakpoint operations to be performed at the assembly language level, source level, or a combination of the two. Watch windows are provided to allow variable and data structure information to be displayed. In addition, changes to variable values can be made directly in these windows. SourceGate II also provides the user interface to the HMI hardware being used showing detailed trace buffer information in several display modes on those systems that supply a trace buffer. Color-enhanced histogram, code coverage information, and timing analysis data is provided for those systems that contain HMI's hardware-based Performance Analysis system (PAC).

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