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IBM Operating System/360

Assembler Language

This publication contains preliminary specifications for the IBM Operating System/360 Assembler Language.

The assembler language is a symbolic programming language used to write programs for the IBM System/360. The language provides a convenient means for representing the machine instructions and related data necessary to program the IBM System/360. The IBM Operating System/360 Assembler Program processes the language and provides auxiliary functions useful in the preparation and documentation of a program, and includes facilities for processing the assembler macro language.

Part I of this publication describes the assembler language.

Part II of this publication describes an extension of the assembler language -- the macro language -- used to define macro-instructions.



PREFACE

This publication is a reference manual for the programmer using the assembler language and its features.

Part I of this publication presents information common to all parts of the language followed by specific information concerning the symbolic machine instruction codes and the assembler program functions provided for the programmer's use. Part II contains a description of the macro language and procedures for its use.

Appendixes A through J follow Part II. Appendixes A through F are associated with Part I and present such items as a summary chart for constants (Appendix F), instruction listings, character set representations, and other aids to programming. Appendix G contains macro-language summary charts, and Appendix H discusses table capacities for various elements of the language. Appendix I is a sample program, and Appendix J is a features comparison chart of System/360 assemblers.

Knowledge of IBM System/360 machine operations, particularly storage addressing, data formats, and machine instruction formats and functions, is prerequisite to using this publication, as is experience with programming concepts and techniques or completion of basic courses of instruction in these areas. IBM System/360 machine operations are discussed in the publication IBM System/360: Principles of Operation, Form A22-6821.

The following publications are referred to in this publication.

- IBM System/360: Introduction
- IBM System/360: Linkage Editor
- IBM System/360: Control Program Services
- IBM System/360: Concepts and Facilities
- IBM System/360: Data Management

The publication form numbers not shown may be obtained in the publication IBM System/360: Introduction, Form C28-6534.

MINOR REVISION (April 1965)

This publication is a minor revision of the previous edition, Form C28-6514-2. It supersedes the previous edition but does not make it obsolete.

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PART I -- THE ASSEMBLER LANGUAGE

SECTION 1: INTRODUCTION

SECTION 2: GENERAL INFORMATION

SECTION 3: ADDRESSING AND PROGRAM SECTIONING AND LINKING

SECTION 4: MACHINE INSTRUCTIONS

SECTION 5: ASSEMBLER INSTRUCTIONS



Computer programs may be expressed in machine language, i.e., language directly interpreted by the computer, or in a symbolic language, which is much more meaningful to the programmer. The symbolic language, however, must be translated into machine language before the computer can execute the program. This function is accomplished by an associated processing program.

Of the various symbolic programming languages, assembler languages are closest to machine language in form and content.

The assembler language discussed in this manual is a symbolic programming language for the IBM System/360. It enables the programmer to use all IBM System/360 machine functions, as if he were coding in System/360 machine language.

The assembler program that processes the language translates symbolic instructions into machine-language instructions, assigns storage locations, and performs auxiliary functions necessary to produce an executable machine-language program.

Compatibility

Operating System/360 assemblers assemble source programs written in the Basic Programming Support/360: Basic Assembler Language and the IBM 7090/7094 Support Package for IBM System/360 assembler language. Operating System/360 assemblers also assemble other System/360 assembler languages, with the following exceptions:

1. The XFR assembler instruction is considered an invalid mnemonic operation code by Operating System/360 assemblers.
2. The assignment, size, and ordering of literal pools may differ among the assemblers.

Differences in the macro language for System/360 assemblers are described in Section 10 of this publication.

THE ASSEMBLER LANGUAGE

The basis of the assembler language is a collection of mnemonic symbols which represent:

1. System/360 machine-language operation codes.
2. Operations (auxiliary functions) to be performed by the assembler program.

The language is augmented by other symbols, supplied by the programmer, and used to represent storage addresses or data. Symbols are easier to remember and code than their machine-language equivalents. Use of symbols greatly reduces programming effort and error.

The programmer may also create a type of instruction called a macro-instruction. A mnemonic symbol, supplied by the programmer, serves as the operation code of the instruction.

Machine Operation Codes

The assembler language provides mnemonic machine-instruction operation codes for all machine instructions in the IBM System/360 Universal Instruction Set, and extended mnemonic operation codes for the conditional branch instruction.

Assembler Operation Codes

The assembler language also contains mnemonic assembler-instruction operation codes, used to specify auxiliary functions to be performed by the assembler program. These are instructions to the assembler program itself and, with a few exceptions, do not result in the generation of any machine-language code by the assembler program.

Macro-Instructions

The assembler language enables the programmer to define and use macro-instructions, if so desired.

Macro-instructions are represented by an operation code which, in turn, actually stands for a sequence of machine and/or assembler instructions that accomplish the desired function.

Macro-instructions used in preparing an assembler language source program fall into two categories: system macro-instructions, provided by IBM, which relate the object program to components of the operating system, and macro-instructions created by the programmer specifically for use in the program at hand, or for incorporation in a library, available for future use.

Programmer-created macro-instructions are used to simplify the writing of a program and/or to ensure that a standard sequence of instructions is used to accomplish a desired function.

For instance, the logic of a program may require the same instruction sequence to be executed again and again. Rather than code this entire sequence each time it is needed, the programmer creates a macro-instruction to represent the sequence, and then each time the sequence is needed, the programmer simply codes the macro-instruction statement. During assembly, the sequence of instructions represented by the macro-instruction is inserted in the object program.

Part II of this publication discusses the language and procedures for defining and using macro-instructions.

THE ASSEMBLER PROGRAM

The assembler program, also referred to as the "assembler," processes the source statements written in the assembler language.

Basic Functions

Processing involves the translation of source statements into machine language, the assignment of storage locations to instructions and other elements of the program, and the performance of the auxiliary assembler program functions designated by the programmer. The output of the assembler program is the object program, a machine-language equivalent of the source program. The program furnishes a printed listing of the source statements and object program statements and additional information useful to the programmer in analyzing his program, such as error indications. The object program is in the format required by the linkage editor component of Operating System/360. (See the linkage editor publication.)

The amount of main and secondary storage allocated to the assembler program for use during processing determines the maximum number of certain language elements that may be present in the source program. For a discussion of these dependencies, see Appendix H.

PROGRAMMER AIDS

The assembler program provides auxiliary functions that assist the programmer in checking and documenting programs, in controlling address assignment, in segmenting a program, in data and symbol definition, in generating macro-instructions, and in controlling the assembly program itself. Mnemonic codes, specifying these functions, are provided in the language.

Variety in Data Representation: Decimal, binary, hexadecimal, or character representation of machine-language binary values may be employed by the programmer in writing source statements. The programmer selects the representation best suited to his purpose.

Base Register Address Calculation: As discussed in the IBM System/360 Principles of Operation manual, the System/360 addressing scheme requires the designation of a base register (containing a base address value) and a displacement value in specifying a storage location. The assembler assumes the clerical burden of calculating storage addresses in these terms for the symbolic addresses used by the programmer. The programmer retains control of base register usage and the values entered therein.

Relocatability: The object programs produced by the assembler are in a format enabling relocation from the originally assigned storage area to any other suitable area.

Sectioning and Linking: The assembler language and program provide facilities for partitioning an assembly into one or more parts called control sections. Control sections may be added or deleted when loading the object program. Because control sections do not have to be loaded contiguously in storage, a sectioned program may be loaded and executed even though a continuous block of storage large enough to accommodate the entire program may not be available.

The linking facilities of the assembler language and program allow symbols to be defined in one assembly and referred to in another, thus effecting a link between separately assembled programs. This per-

mits reference to data and/or transfer of control between programs. A discussion of sectioning and linking is contained in Section 3 under the heading, "Program Sectioning and Linking."

Program Listings: A listing of the source program statements and the resulting object program statements is produced by the assembler for each source program it assembles. The programmer can partly control the form and content of the listing.

Error Indications: As a source program is assembled, it is analyzed for actual or potential errors in the use of the assembler language. Detected errors are indicated in the program listing.

OPERATING SYSTEM RELATIONSHIPS

The assembler program is a component of the IBM Operating System/360 and, as such,

functions under control of the operating system. The operating system provides the assembler with input/output, library, and other services needed in assembling a source program. In a like manner, the object program produced by the assembler will normally operate under control of the operating system and depend on it for input/output and other services. In writing the source program, the programmer must include statements requesting the desired functions from the operating system. These statements are discussed in the control program services publication.

During assembly, the assembler will create the proper linkage between the object program and the specified service components of the operating system. The introduction and the concepts and facilities publications provide further information on operating system relationships.

Input/output considerations are discussed in the data management publication.

Continuation Lines

When it is necessary to continue a statement on another line the following rules apply.

1. Enter a character (not blank, and not part of the statement coding) in column 72 of the statement line.
2. Continue the statement on the next line, starting in column 16. All columns to the left of column 16 must be blank.
3. When more than one line is needed, each line to be continued must have a character (not blank, and not part of the statement coding) entered in column 72.

Statement Boundaries

Source statements are normally contained in columns 1 -- 71 of statement lines and columns 16 -- 71 of any continuation lines. Therefore, columns 1, 71, and 16 are referred to as the "begin," "end," and "continue" columns, respectively. This convention may be altered by use of the Input Format Control (ICTL) assembler instruction discussed later in this publication. The continuation character, if used, always immediately follows the "end" column.

Statement Format

Statements may consist of one to four entries in the statement field. They are, from left to right: a name entry, an operation entry, an operand entry, and a comments entry. These entries must be separated by one or more blanks, and must be written in the order stated.

The coding form (Figure 2-1) is ruled to provide an eight-character name field, a five-character operation field, and a 56-character operand and/or comments field.

If desired, the programmer may disregard these boundaries and write the name, operation, operand, and comment entries in other positions, subject to the following rules:

1. The entries must not extend beyond statement boundaries (either the conventional boundaries, or as designated by the programmer via the ICTL instruction).
2. The entries must be in proper

sequence, as stated above.

3. The entries must be separated by one or more blanks.
4. If used, a name entry must be written starting in the begin column.
5. The name and operation entries must be completed in the first line of the statement, including at least one blank following the operation entry.

A description of the name, operation, operand, and comments entries follows:

Name Entries: The name entry is a symbol created by the programmer to identify a statement. A name entry is usually optional. The symbol must consist of eight characters or less, and be entered with the first character appearing in the begin column. If the begin column is blank, the assembler program assumes no name has been entered. No blanks may appear in the symbol.

Operation Entries: The operation entry is the mnemonic operation code specifying the machine operation or assembler functions desired. An operation entry is mandatory and must appear in the first statement line, starting at least one position to the right of the begin column. Valid mnemonic operation codes for machine and assembler operations are contained in Appendixes B and C of this publication. Valid operation codes consist of five characters or less for machine or assembler operation codes, and eight characters or less for macro-instruction operation codes. No blanks may appear within the operation entry.

Operand Entries: Operand entries are the coding that identifies and describes data to be acted upon by the instruction, by indicating such things as storage locations, masks, storage-area lengths, or types of data.

Depending on the needs of the instruction, one or more operands may be written. Operands are required for all machine instructions.

Operands must be separated by commas and no blanks may intervene between operands and the commas that separate them.

The operands may not contain embedded blanks except as follows:

If character representation is used to specify a constant, a literal, or immediate data in an operand, the character string may contain blanks, e.g., C'AB D'.

Comments Entries: Comments are descriptive items of information about the program that are to be inserted in the program listing.

All valid characters (see "Character Set" in this section), including blanks may be used in writing a comment. The entry cannot extend beyond the end column (normally column 71), and a blank must separate it from the operand.

An entire line may be used for a comment by placing an asterisk in the begin column. Extensive comments entries may be written by using a series of lines with an asterisk in the begin column of each line or by using the aforementioned continuation lines.

In statements where an optional operand entry is omitted but a comments entry is desired, the absence of the operand entry must be indicated by a comma preceded and followed by one or more blanks, as follows:

Name	Operation	Operand
	END	, COMMENT

Statement Example: The following example illustrates the use of name, operation, operand, and comments entries. A compare instruction has been named by the symbol COMP; the operation entry (CR) is the mnemonic operation code for a register-to-register compare operation, and the two operands (5,6) designate the two general registers whose contents are to be compared. The comments entry reminds the programmer that he is comparing "new sum" to "old" with this instruction.

Name	Operation	Operand
COMP	CR	5,6 NEW SUM TO OLD

Identification-Sequence Field

The identification-sequence field of the coding form (columns 73 -- 80) is used to enter program identification and/or statement sequence characters. The entry is optional. If the field, or a portion of it, is used for program identification, the identification is punched in the statement cards, and reproduced in the printed listing of the source program.

To aid in keeping source statements in order, the programmer may code an ascending sequence of characters in this field or a portion of it. These characters are

punched into their respective cards, and, during assembly, the programmer may request the assembler to verify this sequence by use of the Input Sequence Checking (ISEQ) assembler instruction. This instruction is discussed in Section 5 under "Program Control Instructions."

Summary of Statement Format

The entries in a statement must always be separated by at least one blank and must be in the following order: name, operation, operand(s), comment.

Every statement requires an operation entry. Name and comment entries are optional. Operand entries are required for all machine instructions and most assembler instructions.

The name and operation entries must be completed in the first statement line, including at least one blank following the operation entry.

The name and operation entries must not contain blanks. Operand entries must not have blanks preceding or following the commas that separate them.

A name entry must always start in the "begin" column.

If the column after the end column is blank, the next line must start a new statement. If the column after the end column is not blank, the following line will be treated as a continuation line.

All entries must be contained within the designated begin, end, and continue column boundaries.

Character Set

Source statements are written using the following characters:

Letters A through Z, and \$, #, @

Digits 0 through 9

Special Characters + - , = . * () ' / & blank

These characters are represented by the card punch combinations and internal bit configurations listed in Appendix A. In addition, any of the remainder of the 256 punch combinations may be designated in a character self-defining term or character constant.

ASSEMBLER LANGUAGE STRUCTURE

The basic structure of the language can be stated as follows.

A source statement is composed of:

- A name entry (optional).
- An operation entry (mandatory).
- An operand entry (usually required).

A name entry is:

- A symbol.

An operation entry is:

- A mnemonic operation code representing a machine-, assembler, or macro instruction.

An operand entry is:

- One or more operands composed of one or more expressions, which, in turn, are composed of a term or an arithmetic combination of terms.

Operands of machine instructions generally represent such things as storage locations, general registers, immediate data, or constant values. Operands of assembler instructions provide the information needed by the assembler program in order to perform the designated operation.

Figure 2-2 depicts this structure. Terms shown in Figure 2-2 are classed as absolute or relocatable. Terms are absolute or relocatable due to the effect of program relocation upon them. Program relocation is the loading of the object program into storage locations other than those originally assigned by the assembler program. A term is absolute if its value does not change upon relocation. A term is relocatable if its value changes upon relocation.

The following subsection "Terms and Expressions" discusses these items as outlined in Figure 2-2.

TERMS AND EXPRESSIONS

TERMS

Every term represents a value. This value may be assigned by the assembler program (symbols, symbol length attribute, location counter reference) or may be

inherent in the term itself (self-defining term, literal).

An arithmetic combination of terms is reduced to a single value by the assembler program.

The following material discusses each type of term and the rules for its use.

Symbols

A symbol is a character or combination of characters used to represent addresses or arbitrary values.

Symbols, through their use as names and in operands, provide the programmer with an efficient way to name and reference a program element. A symbol, created by the programmer for use as a name entry and in an operand, must conform to these rules:

1. The symbol must not consist of more than eight characters. The first character must be a letter. The other characters may be letters, digits, or a combination of the two.
2. No special characters may be included in a symbol.
3. No blanks are allowed in a symbol.

The following are valid symbols:

READER	LOOP2	@B4
A23456	N	\$A1
X4F2	S4	#56

The following symbols are invalid, for the reasons noted:

256B	(first character is not alphabetic)
RECORDAREA2	(more than eight characters)
BCD*34	(contains a special character - *)
IN AREA	(contains a blank)

DEFINING SYMBOLS: The assembler program assigns a value to each symbol appearing as a name entry in a source statement. The values assigned to symbols naming storage areas, instructions, constants, and control sections represent the addresses of the leftmost bytes of the storage fields containing the named items. Since the addresses of these items may change upon program relocation, the symbols naming them are considered relocatable terms.

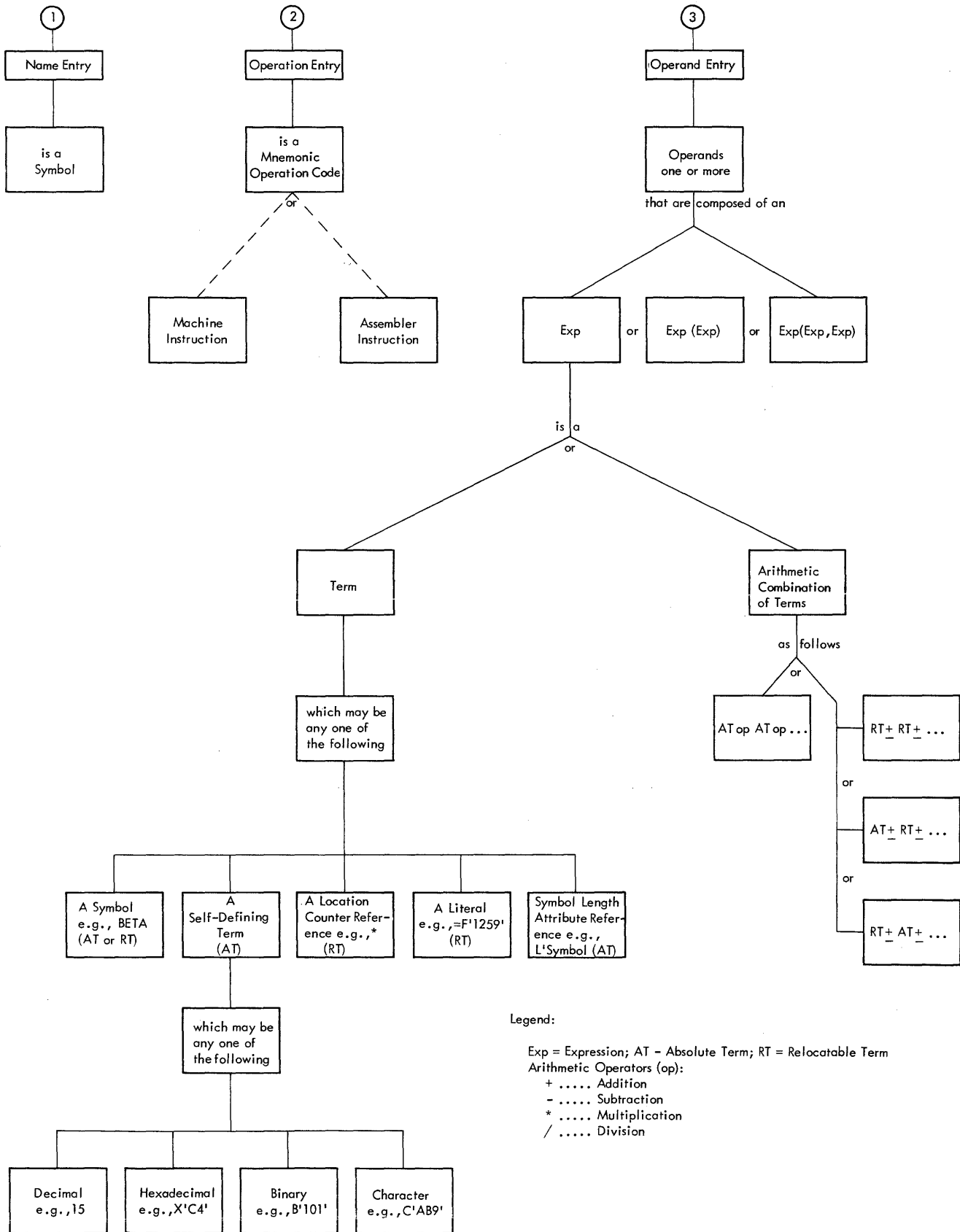


Figure 2-2. Assembler Language Structure -- Machine and Assembler Instructions

A symbol used as a name entry in the Equate Symbol (EQU) assembler instruction is assigned the value designated in the operand entry of the instruction. Since the operand entry may represent a relocatable value or an absolute (i.e., non-changing) value, the symbol is considered a relocatable term or an absolute term depending upon the value it is equated to.

The value of a symbol may not be negative and may not exceed $2^{24}-1$.

A symbol is said to be defined when it appears as the name of a source statement. (A special case of symbol definition is discussed in Section 3, in the subsection "Program Sectioning and Linking.")

Symbol definition also involves the assignment of a length attribute to the symbol. (The assembler program maintains an internal table -- the symbol table -- in which the values and attributes of symbols are kept. When the assembler program encounters a symbol in an operand, it refers to the table for the values associated with the symbol.) The length attribute of a symbol is the size, in bytes, of the storage field whose address is represented by the symbol. For example, a symbol naming an instruction that occupies four bytes of storage has a length attribute of four.

PREVIOUSLY DEFINED SYMBOLS: Some instructions require that a symbol appearing in the operand entry be previously defined. This simply means that the symbol, before its use in an operand, must have appeared as a name entry in a prior statement.

GENERAL RESTRICTIONS ON SYMBOLS: A symbol may be defined only once in an assembly. That is, each symbol used as the name of a statement must be unique to that assembly. However, a symbol may be used in the name field more than once as a control section name (i.e., defined in the START, CSECT, or DSECT assembler statements described in Section 3) because the coding of a control section may be suspended and then resumed at any subsequent point. The CSECT or DSECT statement that resumes the section must be named by the same symbol that initially named the section; thus, the symbol that names the section must be repeated. Such usage is not considered to be duplication of a symbol definition.

Self-Defining Terms

A self-defining term is one whose value is inherent in the term. It is not assigned a value by the assembler program.

For example, the decimal self-defining term -- 15 -- represents a value of fifteen.

There are four types of self-defining terms: decimal, hexadecimal, binary, and character. Use of these terms is spoken of as decimal, hexadecimal, binary, or character representation of the machine language binary value or bit configuration they represent.

Self-defining terms are classed as absolute terms since the values they represent do not change upon program relocation.

USING SELF-DEFINING TERMS: Self-defining terms are the means of specifying machine values or bit configurations without equating the values to symbol and using the symbols.

Self-defining terms may be used to specify such program elements as immediate data, masks, registers, addresses, and address increments. The type of term selected (decimal, hexadecimal, binary, or character) will depend on what is being specified.

The use of a self-defining term is quite distinct from the use of data constants or literals. When a self-defining term is used in a machine-instruction statement, its value is assembled into the instruction. When a data constant or literal is specified in the operand of an instruction, its address is assembled into the instruction.

Decimal Self-Defining Term: A decimal term is simply an unsigned decimal number written as a sequence of decimal digits. High-order zeros may be used (e.g., 007). Limitations on the value of the term depend on its use. For example, a decimal term that designates a general register should have a value between 0 and 15 inclusively; one that represents an address should not exceed the size of storage. In any case, a decimal term may not consist of more than eight digits, or exceed $16,777,215 (2^{24}-1)$. A decimal term is assembled as its binary equivalent. Some examples of decimal self-defining terms are: 8, 147, 4092, 00021.

Hexadecimal Self-defining Term: A hexadecimal self-defining term is an unsigned hexadecimal number written as a sequence of hexadecimal digits. The digits must be enclosed in single quotation marks and preceded by the letter X: X'C49'.

Each hexadecimal digit is assembled as its four-bit binary equivalent. Thus, a hexadecimal term used to represent an eight-bit mask would consist of two hexadecimal digits. The maximum value of a hexadecimal term is X'FFFFFF'.

The hexadecimal digits and their bit patterns are as follows:

0- 0000	4- 0100	8- 1000	C- 1100
1- 0001	5- 0101	9- 1001	D- 1101
2- 0010	6- 0110	A- 1010	E- 1110
3- 0011	7- 0111	B- 1011	F- 1111

A table for converting from hexadecimal representation to decimal representation is provided in Appendix E.

Binary Self-Defining Term: A binary self-defining term is written as an unsigned sequence of 1s and 0s enclosed in single quotation marks and preceded by the letter B, as follows: B'10001101'. This term would appear in storage as shown, occupying one byte. A binary term may have up to 24 bits represented.

Binary representation is used primarily in designating bit patterns of masks or in logical operations.

The following example illustrates a binary term used as a mask in a Test Under Mask (TM) instruction. The contents of GAMMA are to be tested, bit by bit, against the pattern of bits represented by the binary term.

Name	Operation	Operand
ALPHA	TM	GAMMA, B'10101101'

Character Self-Defining Term: A character self-defining term consists of one to three characters enclosed by single quotation marks. It must be preceded by the letter C. All letters, decimal digits, and special characters may be used in a character term. In addition, any of the remainder of the 256 punch combinations may be designated in a character self-defining term. Examples of character self-defining terms are as follows:

C '/'	C ' ' (blank)
C 'ABC'	C '13'

Because of the use of quotes in the assembler language and ampersands in the macro language as syntactic characters, the following rule must be observed when using these characters in a character term.

For each single quotation mark or ampersand desired in a character term, two single quotation marks or ampersands must be written. For example, the character value A'# would be written as 'A''#, while a single quotation mark followed by a blank

and another single quotation mark would be written as '' ''.

Each character in the character sequence is assembled as its eight-bit code equivalent (see Appendix A). The two quotation marks or ampersands that must be used to represent a single quotation mark or ampersand within the character sequence are assembled as a single quotation mark or ampersand.

Location Counter Reference

The programmer may refer to the current value of the Location Counter at any place in a program, by using an asterisk in an operand. The asterisk represents the location of the first byte of currently available storage (i.e., after any required boundary adjustment). Using an asterisk in a machine-instruction statement is the same as placing a symbol in the name field of the statement and then using that symbol as an operand of the statement. Because a Location Counter is maintained for each control section, a Location Counter reference designates the Location Counter for the section in which the reference appears.

A reference to the Location Counter may be made in a literal address constant (i.e., the asterisk may be used in an address constant specified in literal form). The address of the instruction containing the literal is used for the value of the Location Counter. A Location Counter reference may not be used in a statement which requires the use of a predefined symbol, with the exception of the EQU and ORG assembler instructions.

The Location Counter: A Location Counter is used to assign storage addresses to program statements. It is the assembler program's equivalent of the instruction counter in the computer. As each machine instruction or data area is assembled, the Location Counter is first adjusted to the proper boundary for the item, if adjustment is necessary, and then incremented by the length of the assembled item. Thus, it always points to the next available location. If the statement is named by a symbol, the value attribute of the symbol is the value of the Location Counter after boundary adjustment, but before addition of the length.

The assembler maintains a Location Counter for each control section of the program and manipulates each Location Counter as previously described. Source statements for each section are assigned addresses from the Location Counter for

that section. The Location Counter for each successively declared control section assigns locations in consecutively higher areas of storage. Thus, if a program has multiple control sections, all statements identified as belonging to the first control section will be assigned from the Location Counter for section 1, the statements for the second control section will be assigned from the Location Counter for section 2, etc. This procedure is followed whether the statements from different control sections are interspersed or written in control section sequence.

The Location Counter setting can be controlled by using the START and ORG assembler instructions, which are described in Sections 3 and 5 respectively. The counter affected by either of these assembler instructions is the counter for the control section in which they appear. The maximum value for the Location Counter is 2²⁴-1.

Literals

A literal term is one of three basic ways to introduce data into a program. It is simply a constant preceded by an equal sign (=).

A literal represents data rather than a reference to data. The appearance of a literal in a source statement directs the assembler program to assemble the value specified by the literal, store this value in a "literal pool", and place the address of the storage field containing the value in the operand field of the assembled source statement.

Literals provide a means of entering constants (such as numbers for calculation, addresses, indexing factors, or words or phrases for printing out a message) into a program by specifying the constant in the operand of the instruction in which it is used. This is in contrast to using the DC assembler instruction to enter the data into the program, and then using the name of the DC instruction in the operand. Only one literal is allowed in a machine-instruction statement.

A literal term may not be combined with any other terms.

A literal may not be used as the receiving field of an instruction that modifies storage.

A literal may not be specified in an address constant (see Section 5, DC--Define Constant).

The instruction coded below shows one use of a literal.

Name	Operation	Operand
GAMMA	L	10,=F'274'

The statement GAMMA is a load instruction using a literal as the second operand. When assembled, the second operand of the instruction will be the address at which the binary value represented by F'274' is stored.

In general, literals may be used wherever a storage address is permitted as an operand. They may not, however, be used in any assembler instruction that requires the use of a previously defined symbol. Literals are considered relocatable, because the address of the literal, rather than the literal itself, will be assembled in the statement that employs a literal. The assembler generates the literals, collects them, and places them in a specific area of storage, as explained in the subsection "The Literal Pool." A literal is not to be confused with the immediate data in an SI instruction. Immediate data is assembled into the instruction.

Literal Format: The assembler requires a description of the type of literal being specified as well as the literal itself. This descriptive information assists the assembler in assembling the literal correctly. The descriptive portion of the literal must indicate the format in which the constant is to be assembled. It may also specify the length the constant is to occupy.

The method of describing and specifying a constant as a literal is nearly identical to the method of specifying it in the operand of a DC assembler instruction. The major difference is that the literal must start with an equal sign (=), which indicates to the assembler that a literal follows. The reader is referred to the discussion of the DC assembler instruction operand format (Section 5) for the means of specifying a literal. The type of literal designated in an instruction is not checked for correspondence with the operation code of the instruction.

Some examples of literals are:

- =A (BETA) -- address constant literal.
- =F'1234' -- a fixed-point number with a length of four bytes.
- =C'ABC' -- a character literal.

The Literal Pool: The literals processed by the assembler are collected and placed in a special area called the literal pool, and the location of the literal, rather than the literal itself, is assembled in the statement employing a literal. The positioning of the literal pool may be controlled by the programmer, if he so desires. Unless otherwise specified, the literal pool is placed at the end of the first control section.

The programmer may also specify that multiple literal pools be created. However, the sequence in which literals are ordered within the pool is controlled by the assembler. Further information on positioning the literal pool(s) is in Section 5 under "LTORG--Begin Literal Pool."

Symbol Length Attribute Reference

The length attribute of a symbol may be used as a term. Reference to the attribute is made by coding L' followed by the symbol, as in:

L'BETA

The length attribute of BETA will be substituted for the term. The following example illustrates the use of L'symbol in moving a character constant into either the high-order or low-order end of a storage field.

For ease in following the example, the length attributes of A1 and B2 are mentioned. However, keep in mind that the L'symbol term makes coding such as this possible in situations where lengths are unknown.

Name	Operation	Operand
A1	DS	CL8
B2	DC	CL2'AB'
HIORD	MVC	A1(L'B2),B2
LOORD	MVC	A1+L'A1-L'B2(L'B2),B2

A1 names a storage field eight bytes in length and is assigned a length attribute of eight. B2 names a character constant two bytes in length and is assigned a length attribute of two. The statement named HIORD moves the contents of B2 into the leftmost two bytes of A1. The term L'B2 in parentheses provides the length specification required by the instruction. When the instruction is assembled, the length is placed in the proper field of the machine instruction.

The statement named LOORD moves the contents of B2 into the rightmost two bytes of A1. The combination of terms A1+L'A1-L'B2 results in the addition of the length of A1 to the beginning address of A1, and the subtraction of the length of B2 from this value. The result is the address of the seventh byte in field A1. The constant represented by B2 is moved into A1 starting at this address. L'B2 in parentheses provides length specification as in HIORD.

Terms in Parentheses

Terms in parentheses are reduced to a single value; thus, the terms in parentheses, in effect, become a single term.

Arithmetically combined terms, enclosed in parentheses, may be used in combination with terms outside the parentheses, as follows:

14+BETA-(GAMMA-LAMBDA)

When the assembler program encounters terms in parentheses in combination with other terms, it first reduces the combination of terms inside the parentheses to a single value which may be absolute or relocatable, depending on the combination of terms. This value then is used in reducing the rest of the combination to another single value.

Terms in parentheses may be included within a set of terms in parentheses:

A+B-(C+D-(E+F)+10)

The innermost set of terms in parentheses is evaluated first. Five levels of parentheses are allowed. An arithmetic combination of terms is evaluated as described in the next section "Expressions."

EXPRESSIONS

This subsection discusses the expressions used in coding operand entries for source statements. Two types of expressions, absolute and relocatable, are presented along with the rules for determining these attributes of an expression.

As shown in Figure 2-2, an expression is composed of a single term or an arithmetic combination of terms. The following are examples of valid expressions:

*	BETA*10
AREA1+X'2D'	B'101'
#+32	C'ABC'
N-25	29
FIELD+332	L'FIELD
FIELD	LAMBDA+GAMMA
(EXIT-ENTRY+1)+GO	TEN/TWO
=F'1234'	
ALPHA-BETA/(10+AREA*L'FIELD)-100	

The rules for coding expressions are:

1. An expression may not start with an arithmetic operator (+-/*). Therefore, the expression -A+BETA is invalid. However, the expression 0-A+BETA is valid.
2. An expression may not contain two terms or two operators in succession.
3. An expression may not consist of more than 16 terms.
4. An expression may not have more than five levels of parentheses.
5. A multi-term expression may not contain a literal.

Evaluation of Expressions

A single term expression, e.g., 29, BETA, *, L'SYMBOL, takes on the value of the term involved.

A multi-term expression, e.g., BETA+10, ENTRY-EXIT, 25*10+A/B, is reduced to a single value, as follows:

1. Each term is given its value.
2. Arithmetic operations are performed left to right. Multiplication and division are done before addition and subtraction, e.g., A+B*C is evaluated as A+(B*C), not (A+B)*C. The computed result is the value of the expression.
3. Division always yields an integer result; any fractional portion of the result is dropped. E.g., 1/2*10 yields a zero result, whereas 10*1/2 yields 5.
4. Division by zero is valid and yields a zero result.

Parenthesized multi-term expressions used in an expression are processed before the rest of the terms in the expression, e.g., in the expression A+BETA*(CON-10), the term CON-10 is evaluated first and the resulting value used in computing the final value of the expression.

Final values of expressions representing storage addresses are never greater than $2^{24}-1$; however, intermediate results may have a maximum value of $2^{31}-1$.

Absolute and Relocatable Expressions

An expression is called absolute if its value is unaffected by program relocation.

An expression is called relocatable if its value changes upon program relocation.

The two types of expressions, absolute and relocatable, take on these characteristics from the term or terms composing them. The following material discusses this relationship.

ABSOLUTE EXPRESSION: An absolute expression may be an absolute term or any arithmetic combination of absolute terms. An absolute term may be an absolute symbol, any of the self-defining terms, or the length attribute reference. As indicated in Figure 2-2, all arithmetic operations are permitted between absolute terms.

An absolute expression may contain relocatable terms (RT) -- alone or in combination with absolute terms (AT) -- under the following conditions:

1. There must be an even number of relocatable terms in the expression.
2. The relocatable terms must be paired. Each pair of terms must have the same relocatability attribute, i.e., they appear in the same control section in this assembly (see "Program Sectioning and Linking," Section 3). Each pair must consist of terms with opposite signs. The paired terms do not have to be contiguous, e.g., RT+AT-RT.
3. No relocatable term may enter into a multiply or divide operation. Thus, RT-RT*10 is invalid. However, (RT-RT)*10 is valid.

The pairing of relocatable terms (with opposite signs and the same relocatability attribute) cancels the effect of relocation. Therefore the value represented by the paired terms remains constant, regardless of program relocation. For example, in the absolute expression A-Y+X, A is an absolute term, and X and Y are relocatable terms with the same relocatability attribute. If A equals 50, Y equals 25, and X equals 10, the value of the expression would be 35. If X and Y are relocated by a factor of 100 their values would then be 125 and 110. However, the expression would still evaluate as 35 (50-125+110=35).

An absolute expression reduces to a single absolute value.

The following examples illustrate absolute expressions. A is an absolute term; X and Y are relocatable terms with the same relocatability attribute.

A-Y+X
 A
 A*A
 X-Y+A
 *-Y (a reference to the Location Counter must be paired with another relocatable term from the same control section, i.e., with the same relocatability attribute)

RELOCATABLE EXPRESSIONS: A relocatable expression is one whose value would change by n if the program in which it appears is relocated n bytes away from its originally assigned area of storage. All relocatable expressions must have a positive value.

A relocatable expression may be a relocatable term. A relocatable expression may contain relocatable terms -- alone or in combination with absolute terms -- under the following conditions:

1. There must be an odd number of relocatable terms.
2. All the relocatable terms but one must be paired. Pairing is described in "Absolute Expression."
3. The unpaired term must not be directly preceded by a minus sign.
4. No relocatable term may enter into a

5. A relocatable expression must have a positive value.

A relocatable expression reduces to a single relocatable value. This value is the value of the odd relocatable term, adjusted by the values represented by the absolute terms and/or paired relocatable terms associated with it.

For example, in the expression W-X+W-10, W and X are relocatable terms with the same relocatability attribute. If initially W equals 10 and X equals 5, the value of the expression is 5. However, upon relocation this value will change. If a relocation factor of 100 is applied, the value of the expression is 105. Note that the value of the paired terms, W-X, remains constant at 5 regardless of relocation. Thus, the new value of the expression, 105, is the result of the value of the odd term (W) adjusted by the values of W-X and 10.

The following examples illustrate relocatable expressions. A is an absolute term, W and X are relocatable terms with the same relocatability attribute, Y is a relocatable term with a different relocatability attribute.

Y-32*A	W-X**	=F'1234' (literal)
W-X+Y		A*A+W-W+Y
* (reference to Location Counter)	W-X+W	Y

ADDRESSING

The System/360 addressing technique requires the use of a base register, which contains the base address, and a displacement, which is added to the contents of the base register. The programmer may specify a symbolic address and request the assembler to determine its storage address in terms of a base register and a displacement. The programmer may rely on the assembler to perform this service for him by indicating which general registers are available for assignment and what values the assembler may assume each contains. The programmer may use as many or as few registers for this purpose as he desires. The only requirement is that, at the point of reference, a register containing an address from the same control section is available, and that this address is less than or equal to the address of the item to which the reference is being made. The difference between the two addresses may not exceed 4095 bytes.

ADDRESSES -- EXPLICIT AND IMPLIED

An address is composed of a displacement plus the contents of a base register. (In the case of RX instructions, the contents of an index register are also used to derive the address.)

The programmer writes an explicit address by specifying the displacement and the base register number. In designating explicit addresses a base register may not be combined with a relocatable symbol.

He writes an implied address by specifying an absolute or relocatable address. The assembler has the facility to select a base register and compute a displacement, thereby generating an explicit address from an implied address, provided that it has been informed (1) what base registers are available to it and (2) what each contains. The programmer conveys this information to the assembler through the USING and DROP assembler instructions.

BASE REGISTER INSTRUCTIONS

The USING and DROP assembler instructions enable programmers to use expressions representing implied addresses as operands of machine-instruction statements, leaving the assignment of base registers and the calculation of displacements to the assembler.

In order to use symbols in the operand field of machine-instruction statements, the programmer must (1) indicate to the assembler, by means of a USING statement, that one or more general registers are available for use as base registers, (2) specify, by means of the USING statement, what value each base register contains, and (3) load each base register with the value he has specified for it.

A program must have at least one USING statement for each control section to be addressed.

Having the assembler determine base registers and displacements relieves the programmer of separating each address into a displacement value and a base address value. This feature of the assembler will eliminate a likely source of programming errors, thus reducing the time required to check out programs. To take advantage of this feature, the programmer uses the USING and DROP instructions described in this subsection. The principal discussion of this feature follows the description of both instructions.

USING -- Use Base Address Register

The USING instruction indicates that one or more general registers are available for use as base registers. This instruction also states the base address values that the assembler may assume will be in the registers at object time. Note that a USING instruction does not load the registers specified. It is the programmer's responsibility to see that the specified base address values are placed into the registers. Suggested loading methods are described in the subsection "Programming with the USING Instruction." The format of the USING instruction statement is:

Name	Operation	Operand
Blank	USING	From 2-17 expressions of the form v,r1,r2,r3,...,r16

Operand v must be an absolute or relocatable expression. No literals are permitted. Operand v specifies a value that the assembler can use as a base address. The other operands must be absolute expressions. The operand r1 specifies the general register that can be assumed to contain the base address represented by operand v. Operands r2, r3, r4, . . . specify registers that can be assumed to contain v+4096, v+8192, v+12288, . . . , respectively. The values of the operands r1, r2, r3, . . . , r16 must be between 0 and 15. For example, the statement:

Name	Operation	Operand
	USING	*,12,13

tells the assembler it may assume that the current value of the Location Counter will be in general register 12 at object time, and that the current value of the Location Counter, incremented by 4096, will be in general register 13 at object time.

If the programmer changes the value in a base register currently being used, and wishes the assembler to compute displacement from this value, the assembler must be told the new value by means of another USING statement. In the following sequence the assembler first assumes that the value of ALPHA is in register 9. The second statement then causes the assembler to assume that ALPHA+1000 is the value in register 9.

Name	Operation	Operand
	USING	ALPHA,9
	USING	ALPHA+1000,9

A USING statement may specify general register 0 as a base register only if operand v is a relocatable expression in the first control section of the program. If general register 0 is specified, it must be operand r1. In this case, the assembler

assumes that register 0 contains the value zero. Subsequent registers specified in the same statement are assumed to have the values 4096, 8192, etc. The assembler therefore places all subsequent effective addresses less than 4096 in the displacement field and uses zero for the base register field.

Note: If register 0 is made available by a USING instruction, the program is not relocatable, despite the fact that the value specified by operand v must be relocatable. However, the programmer is able to make the program relocatable at some future time by:

1. Replacing register 0 in the USING statement.
2. Loading the register with a relocatable value.
3. Reassembling the program.

DROP -- Drop Base Register

The DROP instruction specifies a previously available register that may no longer be used as a base register. The format of the DROP instruction statement is as follows:

Name	Operation	Operand
Blank	DROP	Up to 16 absolute expressions of the form r1,r2,r3,...,r16

The expressions indicate general registers previously named in a USING statement that are now unavailable for base addressing. The following statement, for example, prevents the assembler from using registers 7 and 11:

Name	Operation	Operand
	DROP	7,11

It is not necessary to use a DROP statement when the base address in a register is changed by a USING statement; nor are DROP statements needed at the end of the source program.

A register made unavailable by a DROP instruction can be made available again by a subsequent USING instruction.

PROGRAMMING WITH THE USING INSTRUCTION

The USING (and DROP) instructions may be used anywhere in a program, as often as needed, to indicate the general registers that are available for use as base registers and the base address values the assembler may assume each contains at execution time. Whenever an address is specified in a machine-instruction statement, the assembler determines whether there is an available register containing a suitable base address. A register is considered available for a relocatable address if it was loaded with a relocatable value that is in the same control section as the address. A register with an absolute value is available only for absolute addresses. In either case, the base address is considered suitable only if it is less than or equal to the address of the item to which the reference is made. The difference between the two addresses may not exceed 4095 bytes.

Name	Operation	Operand
BEGIN	BALR	2,0
	USING	*,2
FIRST	.	
	.	
LAST	.	
	END	BEGIN

In the preceding sequence, the BALR instruction loads register 2 with the address of the first storage location immediately following. In this case, it is the address of the instruction named FIRST. The USING instruction indicates to the assembler that register 2 contains this location. When employing this method, the USING instruction must immediately follow the BALR instruction. No other USING or load instructions are required if the loca-

tion named LAST is within 4095 bytes of FIRST.

In Figure 3-1, the BALR and LM instructions load registers 2-5. The USING instruction indicates to the assembler that these registers are available as base registers for addressing a maximum of 16,384 consecutive bytes of storage, beginning with the location named HERE. The number of addressable bytes may be increased or decreased by altering the number of registers designated by the USING and LM instructions and the number of address constants specified in the DC instruction.

RELATIVE ADDRESSING

Relative addressing is the technique of addressing instructions and data areas by designating their location in relation to the Location Counter or to some symbolic location. This type of addressing is always in bytes, never in bits, words, or instructions. Thus, the expression `**+4` specifies an address that is four bytes greater than the current value of the Location Counter. In the sequence of instructions shown in the following example, the location of the CR machine instruction can be expressed in two ways, `ALPHA+2` or `BETA-4`, because all of the mnemonics in the example are for 2-byte instructions in the RR format.

Name	Operation	Operand
ALPHA	LR	3,4
	CR	4,6
	BCR	1,14
BETA	AR	2,3

Name	Operation	Operand
BEGIN	BALR	2,0
	USING	HERE,2,3,4,5
HERE	LM	3,5,BASEADDR
	B	FIRST
BASEADDR	DC	A (HERE+4096,HERE+8192,HERE+12288)
FIRST	.	
	.	
LAST	.	
	END	BEGIN

Figure 3-1. Multiple Base Register Assignment

PROGRAM SECTIONING AND LINKING

It is often convenient, or necessary, to write a large program in sections. The sections may be assembled separately, then combined subsequently into one object program. The assembler provides facilities for creating multisectioned programs and symbolically linking separately assembled programs or program sections. The combined number of control sections and dummy sections plus the number of unique symbols in EXTRN statements and V-type address constants may not exceed 255. (EXTRN statements are discussed in this section; V-type constants in Section 5 under the DC -- Define Constant assembler instruction.) If the same symbol appears in a V-type address constant and in the name field of a CSECT or DSECT statement, it is counted as two symbols.

Sectioning a program is optional, and many programs can best be written without sectioning them. The programmer writing an unsectioned program need not concern himself with the subsequent discussion of program sections, which are called control sections. He need not employ the CSECT instruction, which is used to identify the control sections of a multisection program. Similarly, he need not concern himself with the discussion of symbolic linkages if his program neither requires a linkage to nor receives a linkage from another program. He may, however, wish to identify the program and/or specify a tentative starting location for it, both of which may be done by using the START instruction. He may also want to employ the dummy section feature obtained by using the DSECT instruction.

Note: Program sectioning and linking is closely related to the specification of base registers for each control section. Sectioning and linking examples are provided under the heading "Addressing External Control Sections."

CONTROL SECTIONS

The concept of program sectioning is a consideration at coding time, assembly time, and load time. To the programmer, a program is a logical unit. He may want to divide it into sections called control sections; if so, he writes it in such a way that control passes properly from one section to another regardless of the relative physical position of the sections in storage. A control section is a block of coding that can be relocated, independently of other coding, at load time without

altering or impairing the operating logic of the program. It is normally identified by the CSECT instruction. However, if it is desired to specify a tentative starting location, the START instruction may be used to identify the first control section.

To the assembler, there is no such thing as a program; instead, there is an assembly, which consists of one or more control sections. (However, the terms assembly and program are often used interchangeably.) An unsectioned program is treated as a single control section. To the linkage editor, there are no programs, only control sections that must be fashioned into an object program.

The output of the assembler consists of the assembled control sections and a control dictionary. The control dictionary contains information the linkage editor needs in order to complete cross-referencing between control sections, as it combines them into an object program. The linkage editor can take control sections from various assemblies and combine them properly with the help of the corresponding control dictionaries. Successful combination of separately assembled control sections depends on the techniques used to provide symbolic linkages between the control sections.

Whether the programmer writes an unsectioned program, a multisection program, or part of a multisection program, he still knows what eventually will be entered into storage, because he has described storage symbolically. He may not know where each section appears in storage, but he does know what storage contains. There is no constant relationship between control sections. Thus, knowing the location of one control section does not make another control section addressable by relative addressing techniques.

Control Section Location Assignment

Control section contents can be intermixed because the assembler provides a Location Counter for each control section. Locations are assigned to control sections in such a way that the sections are placed in storage consecutively, in the same order as they first occur in the program. Each control section subsequent to the first begins at the next available double-word boundary.

FIRST CONTROL SECTION

The first control section of a program has the following special properties.

1. Its tentative loading location may be specified as an absolute value.
2. It normally contains the literals requested in the program, although their positioning can be altered. This is further explained under the discussion of the LTOrg assembler instruction.

START -- Start Assembly

The START instruction may be used to give a name to the first (or only) control section of a program. There may be only one START instruction in an assembly. It may also be used to specify a tentative starting location for the program. The format of the START instruction statement is as follows:

Name	Operation	Operand
A symbol or blank	START	A self-defining term or blank

If a symbol names the START instruction, the symbol is established as the name of the control section. If not, the control section is considered to be unnamed. All subsequent statements are assembled as part of that control section. This continues until a CSECT instruction identifying a different control section or a DSECT instruction is encountered. A CSECT instruction named by the same symbol that names a START instruction is considered to identify the continuation of the control section first identified by the START. Similarly, an unnamed CSECT that occurs in a program initiated by an unnamed START is considered to identify the continuation of the unnamed control section.

The symbol in the name field is a valid relocatable symbol whose value represents the address of the first byte of the control section. It has a length attribute of one.

The assembler uses the self-defining value specified by the operand as the tentative starting location of the program. This value must be divisible by eight. For example, either of the following statements:

Name	Operation	Operand
PROG2	START	2040
PROG2	START	X'7F8'

could be used to assign the name PROG2 to the first control section and to indicate an initial assembly location of 2040. If the operand is omitted, the assembler sets the tentative starting location of the program at zero. The Location Counter is set at the next double-word boundary when the value of the START operand is not divisible by eight.

Note: The START instruction may not be preceded by any type of assembler language statement that may either affect or depend upon the setting of the Location Counter.

CSECT -- Identify Control Section

The CSECT instruction identifies the beginning or the continuation of a control section. The format of the CSECT instruction statement is as follows:

Name	Operation	Operand
A symbol or blank	CSECT	Not used; should be blank

If a symbol names the CSECT instruction, the symbol is established as the name of the control section; otherwise the section is considered to be unnamed. All statements following the CSECT are assembled as part of that control section until a statement identifying a different control section is encountered (i.e., another CSECT or a DSECT instruction).

The symbol in the name field is a valid relocatable symbol whose value represents the address of the first byte of the control section. It has a length attribute of one.

Several CSECT statements with the same name may appear within a program. The first is considered to identify the beginning of the control section; the rest identify the resumption of the section. Thus, statements from different control sections may be interspersed. They are properly assembled (assigned contiguous storage locations) as long as the statements from the various control sections are

identified by the appropriate CSECT instructions.

Unnamed Control Section

If neither a named CSECT instruction nor START instruction appears at the beginning of the program, the assembler determines that it is to assemble an unnamed control section as the first (or only) control section. There may be only one unnamed control section in a program. If one is initiated and is then followed by a named control section, any subsequent unnamed CSECT statements are considered to resume the unnamed control section. If it is desired to write a small program that is unsectioned, the program does not need to contain a CSECT instruction.

DSECT -- Identify Dummy Section

A dummy section represents a control section that is assembled but is not part of the object program. A dummy section is a convenient means of describing the layout of an area of storage without actually reserving the storage. (It is assumed that the storage is reserved either by some other part of this assembly or else by another assembly.) The DSECT instruction identifies the beginning or resumption of a dummy section. More than one dummy section may be defined per assembly, but each must be named. The format of the DSECT instruction statement is as follows:

Name	Operation	Operand
A symbol	DSECT	Not used; should be blank

The symbol in the name field is a valid relocatable symbol whose value represents the first byte of the section. It has a length attribute of one.

Program statements belonging to dummy sections may be interspersed throughout the program or may be written as a unit. In either case, the appropriate DSECT instruction should precede each set of statements. When multiple DSECT instructions with the same name are encountered, the first is considered to initiate the dummy section and the rest to continue it.

Symbols that name statements in a dummy section may be used in USING instructions. Therefore, they may be used in program elements (e.g., machine-instructions and data definitions) that specify storage addresses. An example illustrating the use of a dummy section appears subsequently under "Addressing Dummy Sections."

Note: A symbol that names a statement in a dummy section may be used in an A-type address constant only if it is paired with another symbol (with the opposite sign) from the same dummy section.

DUMMY SECTION LOCATION ASSIGNMENT: A Location Counter is used to determine the relative locations of named program elements in a dummy section. The Location Counter is always set to zero at the beginning of the dummy section, and the location values assigned to symbols that name statements in the dummy section are relative to the initial statement in the section.

ADDRESSING DUMMY SECTIONS: The programmer may wish to describe the format of an area whose storage location will not be determined until the program is executed. He can describe the format of the area in a dummy section, and he can use symbols defined in the dummy section as the operands of machine instructions. To effect references to the storage area, he does the following:

1. Provides a USING statement specifying both a general register that the assembler can assign to the machine-instructions as a base register and a value from the dummy section that the assembler may assume the register contains.
2. Ensures that the same register is loaded with the actual address of the storage area.

The values assigned to symbols defined in a dummy section are relative to the initial statement of the section. Thus, all machine-instructions which refer to names defined in the dummy section will, at execution time, refer to storage locations relative to the address loaded into the register.

An example is shown in the following coding. Assume that two independent assemblies (assembly 1 and assembly 2) have been loaded and are to be executed as a single overall program. Assembly 1 is an input routine that places a record in a specified area of storage, places the address of the input area containing the record in general register 3, and branches to assembly 2. Assembly 2 processes the record. The cod-

ing shown in the example is from assembly 2.

The input area is described in assembly 2 by the DSECT control section named INAREA. Portions of the input area (i.e., record) that the programmer wishes to work with are named in the DSECT control section as shown. The assembler instruction USING INAREA,3 designates general register 3 as the base register to be used in addressing the DSECT control section, and that general register 3 is assumed to contain the address of INAREA.

Assembly 1, during execution, loads the actual beginning address of the input area in general register 3. Because the symbols used in the DSECT section are defined relative to the initial statement in the section, the address values they represent, will, at the time of program execution, be the actual storage locations of the input area.

Name	Operation	Operand
ASMBLY2	CSECT	
BEGIN	BALR	2,0
	USING	*,2
	.	
	.	
	USING	INAREA,3
	CLI	INCODE,C'A'
	BE	ATYPE
	.	
	.	
ATYPE	MVC	WORKA,INPUTA
	MVC	WORKB,INPUTB
	.	
	.	
WORKA	DS	CL20
WORKB	DS	CL18
	.	
	.	
INAREA	DSECT	
INCODE	DS	CL1
INPUTA	DS	CL20
INPUTB	DS	CL18
	.	
	END	

COM -- DEFINE BLANK COMMON CONTROL SECTION

The COM assembler instruction identifies and reserves a common area of storage that may be referred to by independent assemblies that have been linked and loaded for execution as one overall program.

Only one blank common control section can be designated in an assembly.

When several assemblies are loaded, each designating a common control section, the amount of storage reserved is equal to the longest common control section. The format is:

Name	Operation	Operand
Blank	COM	Blank

The common area may be broken up into subfields through use of the DS and DC assembler instructions. Names of subfields are defined relative to the beginning of the common section, as in the DSECT control section.

No instructions or constants appearing in a common control section are assembled. Data can only be placed in a common control section through execution of the program.

If the assignment of common storage is done in the same manner by each independent assembly, reference to a location in common by any assembly results in the same location being referenced. When assembled, blank common location assignment starts on the next double word boundary after the highest tentative location assigned to the assembly.

SYMBOLIC LINKAGES

Symbols may be defined in one program and referred to in another, thus effecting symbolic linkages between independently assembled programs. The linkages can be effected only if the assembler is able to provide information about the linkage symbols to the linkage editor, which resolves these linkage references at load time. The assembler places the necessary information in the control dictionary on the basis of the linkage symbols identified by the ENTRY and EXTRN instructions. Note that these symbolic linkages are described as linkages between independent assemblies; more specifically, they are linkages between independently assembled control sections.

In the program where the linkage symbol is defined (i.e., used as a name), it must also be identified to the assembler by means of the ENTRY assembler instruction. It is identified as a symbol that names an entry point, which means that another program will use that symbol in order to effect a branch operation or a data ref-

erence. The assembler places this information in the control dictionary.

Similarly, the program that uses a symbol defined in some other program must identify it by the EXTRN assembler instruction. It is identified as an externally defined symbol (i.e., defined in another program) that is used to effect linkage to the point of definition. The assembler places this information in the control dictionary.

Another way to obtain symbolic linkages, is by using the V-type address constant. The subsection "Data Definition Instructions" in Section 5 contains the details pertinent to writing a V-type address constant. It is sufficient here to note that this constant may be considered an indirect linkage point. It is created from an externally defined symbol, but that symbol does not have to be identified by an EXTRN statement. The V-type address constant may be used for external branch references (i.e., for effecting branches to other programs). It may not be used for external data references (i.e., for referring to data in other programs).

ENTRY -- IDENTIFY ENTRY-POINT SYMBOL

The ENTRY instruction identifies linkage symbols that are defined in this program but may be used by some other program. The format of the ENTRY instruction statement is as follows:

Name	Operation	Operand
Blank	ENTRY	One or more relocatable symbols, separated by commas, that also appear as statement names

A program may contain a maximum of 100 entry symbols.

The symbols in the ENTRY operand field may be used as operands by other programs. An ENTRY statement operand may not contain a symbol defined in an unnamed control section, a dummy section or blank common. The following example identifies the statements named SINE and COSINE as entry points to the program.

Name	Operation	Operand
	ENTRY	SINE,COSINE

Note: The name of a control section does not have to be identified by an ENTRY instruction when another program uses it as an entry point. The assembler automatically places information on control section names in the control dictionary.

EXTRN -- IDENTIFY EXTERNAL SYMBOL

The EXTRN instruction identifies linkage symbols that are used by this program but defined in some other program. Each external symbol must be identified; this includes symbols that name control sections. The format of the EXTRN instruction statement is as follows:

Name	Operation	Operand
Blank	EXTRN	One or more relocatable symbols, separated by commas

The symbols in the operand field may not appear as names of statements in this program. The following example identifies three external symbols that have been used as operands in this program but are defined in some other program.

Name	Operation	Operand
	EXTRN	RATEBL,PAYCALC
	EXTRN	WITHCALC

An example that employs the EXTRN instruction appears subsequently under "Addressing External Control Sections."

Note: A V-type address constant does not have to be defined by an EXTRN statement.

Note: When external symbols are used in an expression they may not be paired. Each external symbol must be considered as having a unique relocatability attribute.

Addressing External Control Sections

A common way for a program to link to an external control section is to:

1. Create a V-type address constant with the name of the external symbol.
2. Load the constant into a general register and branch to the control section via the register.

Name	Operation	Operand
MAINPROG	CSECT	
BEGIN	BALR	2,0
	USING	*,2
	.	
	.	
	L	3,VCON
	BALR	1,3
	.	
	.	
VCON	DC	V (SINE)
	END	BEGIN

For example, to link to the control section named SINE, the preceding coding might be used.

An external symbol naming data may be referred to as follows:

1. Identify the external symbol with the EXTRN instruction, and create an address constant from the symbol.
2. Load the constant into a general register, and use the register for base addressing.

For example, to use an area named RATETBL, which is in another control section, the following coding might be used:

Name	Operation	Operand
MAINPROG	CSECT	
BEGIN	BALR	2,0
	USING	*,2
	.	
	.	
	EXTRN	RATETBL
	.	
	.	
	L	4,RATEADDR
	USING	RATETBL,4
	A	3,RATETBL
	.	
	.	
RATEADDR	DC	A (RATETBL)
	END	BEGIN

SECTION 4: MACHINE-INSTRUCTIONS

This section discusses the coding of the machine-instructions represented in the assembler language. The reader is reminded that the functions of each machine-instruction are discussed in the principles of operation manual (see Preface).

MACHINE-INSTRUCTION STATEMENTS

Machine-instructions may be represented symbolically as assembler language statements. The symbolic format of each varies according to the actual machine-instruction format, of which there are five: RR, RX, RS, SI, and SS. Within each basic format, further variations are possible.

The symbolic format of a machine-instruction is similar to, but does not duplicate, its actual format. Appendix D illustrates machine format for the five classes of instructions. A mnemonic operation code is written in the operation field, and one or more operands are written in the operand field. Comments may be appended to a machine-instruction statement as previously explained in Section 1.

Any machine-instruction statement may be named by a symbol, which other assembler statements can use as an operand. The value attribute of the symbol is the address of the leftmost byte assigned to the assembled instruction. The length attribute of the symbol depends on the basic instruction format, as follows:

<u>Basic Format</u>	<u>Length Attribute</u>
RR	2
RX	4
RS	4
SI	4
SS	6

Instruction Alignment and Checking

All machine-instructions are aligned automatically by the assembler on half-word boundaries. If any statement that causes information to be assembled requires alignment, the bytes skipped are filled with hexadecimal zeros. All expressions that specify storage addresses are checked to insure that they refer to appropriate boundaries for the instructions in which

they are used. Register numbers are also checked to make sure that they specify the proper registers, as follows:

1. Floating-point instructions must specify floating-point registers 0, 2, 4, or 6.
2. Double-shift, full-word multiply, and divide instructions must specify an even-numbered general register in the first operand.

OPERAND FIELDS AND SUBFIELDS

Some symbolic operands are written as a single field and other operands are written as a field followed by one or two subfields. For example, addresses consist of the contents of a base register and a displacement. An operand that specifies a base and displacement is written as a displacement field followed by a base register subfield, as follows: 40(5). In the RX format, both an index register subfield and a base register subfield are written as follows: 40(3,5). In the SS format, both a length subfield and a base register subfield are written as follows: 40(21,5).

Appendix D shows two types of addressing formats for RX, RS, SI, and SS instructions. In each case, the first type shows the method of specifying an address explicitly, as a base register and displacement. The second type indicates how to specify an implied address as an expression.

For example, a load multiple instruction (RS format) may have either of the following symbolic operands:

R1,R3,D2(B2) - - explicit address
R1,R3,S2 - - implied address

Whereas D2 and B2 must be represented by absolute expressions, S2 may be represented either by a relocatable or an absolute expression.

In order to use implied addresses, the following rules must be observed:

1. The base register assembler instructions (USING and DROP) must be used.
2. An explicit base register designation must not accompany the implied address.

For example, assume that FIELD is a relocatable symbol, which has been assigned a value of 7400. Assume also that the assembler has been notified (by a USING instruction) that general register 12 currently contains a relocatable value of 4096 and is available as a base register. The following example shows a machine-instruction statement as it would be written in assembler language and as it would be assembled. Note that the value of D2 is the difference between 7400 and 4096 and that X2 is assembled as zero, since it was omitted. The assembled instruction is presented in decimal:

Assembler statement:

```
ST      4,FIELD
```

Assembled instruction:

```
Op.Code  R1  X2  B2  D2
50       4   0  12  3304
```

An address may be specified explicitly as a base register and displacement (and index register for RX instructions) by the formats shown in the first column of Table 4-1. The address may be specified as an implied address by the formats shown in the second column. Observe that the two storage addresses required by the SS instructions are presented separately; an implied address may be used for one while an explicit address is used for the other.

Table 4-1. Details of Address Specification

Type	Explicit Address	Implied Address
RX	D2 (X2,B2) D2 (0,B2) *	S2 (X2) S2
RS	D2 (B2)	S2
SI	D1 (B1)	S1
SS	D1 (L1,B1) D1 (L,B1) D2 (L2,B2)	S1 (L1) S1 (L) S2 (L2)

*A zero must be supplied when it is desired to omit an index register specification in an RX explicit address.

A comma must be written to separate operands. Parentheses must be written to enclose a subfield or subfields, and a comma must be written to separate two subfields within parentheses. When parentheses are used to enclose one subfield, and the subfield is omitted, the parentheses must be omitted. In the case of two subfields that are separated by a comma and

enclosed by parentheses, the following rules apply:

1. If both subfields are omitted, the separating comma and the parentheses must also be omitted.

```
L  2,48(4,5)
L  2,FIELD      (implied address)
```

2. If the first subfield in the sequence is omitted, the comma that separates it from the second subfield is written. The parentheses must also be written.

```
MVC 32(16,5),FIELD2
MVC BETA(,5),FIELD2 (implied length)
```

3. In the RX class of instructions if the index register subfield is not used, but the base register is specified, the first subfield (index register) must be specified as zero. It may not be omitted.

```
L  2,48(4,5)
L  2,48(0,5)
```

4. If the second subfield in the sequence is omitted, the comma that separates it from the first subfield must be omitted. The parentheses must be written.

```
MVC 32(16,5),FIELD2
MVC FIELD1(16),FIELD2      (implied address)
```

Fields and subfields in a symbolic operand may be represented either by absolute or by relocatable expressions, depending on what the field requires. (An expression has been defined as consisting of one term or a series of arithmetically combined terms.) Refer to Appendix D for a detailed description of field requirements.

Note: Blanks may not appear in an operand unless provided by a character self-defining term or a character literal. Thus, blanks may not intervene between fields and the comma separators, between parentheses and fields, etc.

LENGTHS -- EXPLICIT AND IMPLIED

The length field in SS instructions can be explicit or implied. To imply a length, the programmer omits a length field from the operand. The omission indicates that the length field is either of the following:

1. The length attribute of the expression

specifying the displacement, if an explicit base and displacement have been written.

2. The length attribute of the expression specifying the effective address, if the base and displacement have been implied.

In either case, the length attribute for an expression is the length of the leftmost term in the expression.

By contrast, an explicit length is written by the programmer in the operand as an absolute expression. The explicit length overrides any implied length.

Whether the length is explicit or implied, it is always an effective length. The value inserted into the length field of the assembled instruction is one less than the effective length in the machine-instruction statement.

Note: If a length field of zero is desired, the length may be stated as zero or one.

To summarize, the length required in an SS instruction may be specified explicitly by the formats shown in the first column of Table 4-2 or may be implied by the formats shown in the second column. Observe that the two lengths required in one of the SS instruction formats are presented separately. An implied length may be used for one while an explicit length is used for the other.

Table 4-2. Details of Length Specification in SS Instructions

Explicit Length	Implied Length
D1 (L1,B1)	D1 (,B1)
S1 (L1)	S1
D1 (L,B1)	D1 (,B1)
S1 (L)	S1
D2 (L2,B2)	D2 (,B2)
S2 (L2)	S2

MACHINE-INSTRUCTION MNEMONIC CODES

The mnemonic operation codes (shown in Appendix B) are designed to be easily remembered codes that indicate the functions of the instructions. The normal format of the code is shown below; the items in brackets are not necessarily present in all codes:

Verb [Modifier] [Data Type] [Machine Format]

The verb, which is usually one or two characters, specifies the function. For example, A represents Add , and MV represents Move. The function may be further defined by a modifier. For example, the modifier L indicates a logical function, as in AL for Add Logical.

Mnemonic codes for functions involving data usually indicate the data types, by letters that correspond to those for the data types in the DC assembler instruction (see Section 5). Furthermore, letters U and W have been added to indicate short and long, unnormalized floating-point operations, respectively. For example, AE indicates Add Normalized Short, whereas AU indicates Add Unnormalized Short. Where applicable, full-word fixed-point data is implied if the data type is omitted.

The letters R and I are added to the codes to indicate, respectively, RR and SI machine instruction formats. Thus, AER indicates Add Normalized Short in the RR format. Functions involving character and decimal data types imply the SS format.

MACHINE-INSTRUCTION EXAMPLES

The examples that follow are grouped according to machine-instruction format. They illustrate the various symbolic operand formats. All symbols employed in the examples must be assumed to be defined elsewhere in the same assembly. All symbols that specify register numbers and lengths must be assumed to be equated elsewhere to absolute values.

Implied addressing, control section addressing, and the function of the USING assembler instruction are not considered here. For discussion of these considerations and for examples of coding sequences that illustrate them, the reader is referred to Section 3, "Program Sectioning and Linking" and "Base Register Instructions."

RR Format

Name	Operation	Operand
ALPHA1	LR	1,2
ALPHA2	LR	REG1,REG2
BETA	SPM	15
GAMMA1	SVC	250
GAMMA2	SVC	TEN

The operands of ALPHA1, BETA, and GAMMA1 are decimal self-defining values, which are categorized as absolute expressions. The operands of ALPHA2 and GAMMA2 are symbols that are equated elsewhere to absolute values.

RX Format

Name	Operation	Operand
ALPHA1	L	1, 39 (4, 10)
ALPHA2	L	REG1, 39 (4, TEN)
BETA1	L	2, ZETA (4)
BETA2	L	REG2, ZETA (REG4)
GAMMA1	L	2, ZETA
GAMMA2	L	REG2, ZETA
GAMMA3	L	2, =F'1000'
LAMBDA1	L	3, 20 (0, 5)

Both ALPHA instructions specify explicit addresses; REG1 and TEN are absolute symbols. Both BETA instructions specify implied addresses, and both use index registers. Indexing is omitted from the GAMMA instructions. GAMMA1 and GAMMA2 specify implied addresses. The second operand of GAMMA3 is a literal. LAMBDA1 specifies no indexing.

RS Format

Name	Operation	Operand
ALPHA1	BXH	1, 2, 20 (14)
ALPHA2	BXH	REG1, REG2, 20 (REGD)
ALPHA3	BXH	REG1, REG2, ZETA
ALPHA4	SLL	REG2, 15
ALPHA5	SLL	REG2, 0 (15)

Whereas ALPHA1 and ALPHA2 specify explicit addresses, ALPHA3 specifies an implied address. ALPHA4 is a shift instruction shifting the contents of REG2 left 15 bit positions. ALPHA5 is a shift instruction shifting the contents of REG2 left by the value contained in general register 15.

SI Format

Name	Operation	Operand
ALPHA1	CLI	40 (9), X'40'
ALPHA2	CLI	40 (REG9), TEN
BETA1	CLI	ZETA, TEN
BETA2	CLI	ZETA, C'A'
GAMMA1	SIO	40 (9)
GAMMA2	SIO	0 (9)
GAMMA3	SIO	40 (0)
GAMMA4	SIO	ZETA

The ALPHA instructions and GAMMA1-GAMMA3 specify explicit addresses, whereas the BETA instructions and GAMMA4 specify implied addresses. GAMMA2 specifies a displacement of zero. GAMMA3 does not specify a base register.

SS Format

Name	Operation	Operand
ALPHA1	AP	40 (9, 8), 30 (6, 7)
ALPHA2	AP	40 (NINE, REG8), 30 (L6, 7)
ALPHA3	AP	FIELD2, FIELD1
ALPHA4	AP	FIELD2 (9), FIELD1 (6)
BETA	AP	FIELD2 (9), FIELD1
GAMMA1	MVC	40 (9, 8), 30 (7)
GAMMA2	MVC	40 (NINE, REG8), DEC (7)
GAMMA3	MVC	FIELD2, FIELD1
GAMMA4	MVC	FIELD2 (9), FIELD1

ALPHA1, ALPHA2, GAMMA1, and GAMMA2 specify explicit lengths and addresses. ALPHA3 and GAMMA3 specify both implied length and implied addresses. ALPHA4 and GAMMA4 specify explicit length and implied addresses. BETA specifies an explicit length for FIELD2 and an implied length for FIELD1; both addresses are implied.

EXTENDED MNEMONIC CODES

For the convenience of the programmer, the assembler provides extended mnemonic codes, which allow conditional branches to be specified mnemonically as well as through the use of the BC machine-instruction. These extended mnemonic codes specify both the machine branch instruction and the condition on which the branch is to occur. The codes are not part of the universal set of machine-instructions, but

Extended Code	Meaning	Machine-Instruction
B D2 (X2,B2)	Branch Unconditional	BC 15,D2 (X2,B2)
BR R2	Branch Unconditional (RR format)	BCR 15,R2
NOP D2 (X2,B2)	No Operation	BC 0,D2 (X2,B2)
NOPR R2	No Operation (RR format)	BCR 0,R2
Used After Compare Instructions		
BH D2 (X2,B2)	Branch on High	BC 2,D2 (X2,B2)
BL D2 (X2,B2)	Branch on Low	BC 4,D2 (X2,B2)
BE D2 (X2,B2)	Branch on Equal	BC 8,D2 (X2,B2)
BNH D2 (X2,B2)	Branch on Not High	BC 13,D2 (X2,B2)
BNL D2 (X2,B2)	Branch on Not Low	BC 11,D2 (X2,B2)
BNE D2 (X2,B2)	Branch on Not Equal	BC 7,D2 (X2,B2)
Used After Arithmetic Instructions		
BO D2 (X2,B2)	Branch on Overflow	BC 1,D2 (X2,B2)
BP D2 (X2,B2)	Branch on Plus	BC 2,D2 (X2,B2)
BM D2 (X2,B2)	Branch on Minus	BC 4,D2 (X2,B2)
BZ D2 (X2,B2)	Branch on Zero	BC 8,D2 (X2,B2)
Used After Test Under Mask Instructions		
BO D2 (X2,B2)	Branch if Ones	BC 1,D2 (X2,B2)
BM D2 (X2,B2)	Branch if Mixed	BC 4,D2 (X2,B2)
BZ D2 (X2,B2)	Branch if Zeros	BC 8,D2 (X2,B2)

Figure 4-1. Extended Mnemonic Codes

are translated by the assembler into the corresponding operation and condition combinations.

The allowable extended mnemonic codes and their operand formats are shown in Figure 4-1, together with their machine-instruction equivalents. Unless otherwise noted, all extended mnemonics shown are for instructions in the RX format. Note that the only difference between the operand fields of the extended mnemonics and those of their machine-instruction equivalents is the absence of the R1 field and the comma that separates it from the rest of the operand field. The extended mnemonic list, like the machine-instruction list, shows explicit address formats only. Each address can also be specified as an implied address. Examples illustrating instructions using extended mnemonic codes follow the list of extended mnemonics.

In the following examples, which illustrate the use of extended mnemonics, it is to be assumed that the symbol GO is defined elsewhere in the program.

Name	Operation	Operand
	B	40 (3,6)
	B	40 (0,6)
	BL	GO (3)
	BL	GO
	BR	4

The first two instructions specify an unconditional branch to an explicit address. The address in the first case is the sum of the contents of base register 6, the contents of index register 3, and the displacement 40; the address in the second instruction is not indexed. The third instruction specifies a branch on low to the address implied by GO as indexed by the contents of index register 3; the fourth instruction does not specify an index register. The last instruction is an unconditional branch to the address contained in register 4.

SECTION 5: ASSEMBLER INSTRUCTION STATEMENTS

Just as machine instructions are used to request the computer to perform a sequence of operations during program execution time, so assembler instructions are requests to the assembler to perform certain operations during the assembly. Assembler-instruction statements, in contrast to machine-instruction statements, do not always cause machine-instructions to be included in the assembled program. Some, such as DS and DC, generate no instructions but do cause storage areas to be set aside for constants and other data. Others, such as EQU and SPACE, are effective only at assembly time; they generate nothing in the assembled program and have no effect on the Location Counter.

The following is a list of all the assembler instructions.

Symbol Definition Instruction EQU - Equate Symbol

Data Definition Instructions

DC - Define Constant
DS - Define Storage
CCW - Define Channel Command Word

* Program Sectioning and Linking Instructions

START - Start Assembly
CSECT - Identify Control Section
DSECT - Identify Dummy Section
ENTRY - Identify Entry-Point Symbol
EXTRN - Identify External Symbol
COM - Identify Blank Common Control Section

* Base Register Instructions

USING - Use Base Address Register
DROP - Drop Base Address Register

Listing Control Instructions

TITLE - Identify Assembly Output
EJECT - Start New Page
SPACE - Space Listing
PRINT - Print Optional Data

Program Control Instructions

ICTL - Input Format Control
ISEQ - Input Sequence Checking
ORG - Set Location Counter
LTORG - Begin Literal Pool
CNOP - Conditional No Operation
COPY - Copy Predefined Source Coding
END - End Assembly
PUNCH - Punch a Card
REPRO - Reproduce Following Card

* Discussed in Section 3.

SYMBOL DEFINITION INSTRUCTION

EQU -- EQUATE SYMBOL

The EQU instruction is used to define a symbol by assigning to it the attributes of an expression in the operand field. The format of the EQU instruction statement is as follows:

Name	Operation	Operand
A symbol	EQU	An expression

The expression in the operand field may be absolute or relocatable. Any symbols appearing in the expression must be previously defined.

The symbol in the name field is given the same attributes as the expression in the operand field. The length attribute of the symbol is that of the leftmost (or only) term of the expression. The value attribute of the symbol is the value of the expression.

The EQU instruction is the means of equating symbols to register numbers, immediate data, and other arbitrary values. The following examples illustrate how this might be done:

Name	Operation	Operand
REG2	EQU	2 (general register)
TEST	EQU	X'3F' (immediate data)

To reduce programming time, the programmer can equate symbols to frequently used expressions and then use the symbols as operands in place of the expressions. Thus, in the statement

Name	Operation	Operand
FIELD	EQU	ALPHA-BETA+GAMMA

FIELD is defined as ALPHA-BETA+GAMMA and may be used in place of it. Note, however, that ALPHA, BETA, and GAMMA must all be previously defined.

DATA DEFINITION INSTRUCTIONS

There are three data definition instruction statements: Define Constant (DC), Define Storage (DS), and Define Channel Command Word (CCW).

These statements are used to enter data constants into storage, to define and reserve areas of storage, and to specify the contents of channel command words. The statements may be named by symbols so that other program statements can refer to the fields generated from them. The discussion of the DC instruction is far more extensive than that of the DS instruction, because the DS instruction is written in the same format as the DC instruction and may specify some or all of the information that the DC instruction provides. Only the function and treatment of the statements vary. For this reason, the DC instruction is presented first and discussed in more detail than the DS instruction.

DC -- DEFINE CONSTANT

The DC instruction is used to provide constant data in storage. It may specify one constant or a series of constants, thereby relieving the programmer of the necessity to write a separate data definition statement for each constant desired. Furthermore, a variety of constants may be specified: fixed-point, floating-point, decimal, hexadecimal, character, and storage addresses. (Data constants are generally called constants unless they are created from storage addresses, in which case they are called address constants.) The format of the DC instruction statement is as follows:

Name	Operation	Operand
A symbol or blank	DC	One or more operands in the format described below, each separated by a comma

Each operand consists of four subfields; the first three describe the constant (some or all may be omitted, depending on the constant), and the fourth subfield provides the constant or constants. Note that more than one constant may be specified in the fourth subfield for most types of constants. Each constant so specified must be of the same type; the descriptive subfields that precede the constants apply to all of them. No blanks may occur within any of the subfields (unless provided as characters in a character constant), nor may they occur between the subfields of an operand. Similarly, blanks may not occur between operands and the commas that separate them when multiple operands are being specified.

The subfields of each DC operand are written in the following sequence:

1	2	3	4
Duplication Factor	Type	Modifiers	Constant (s)

Although the constants specified in one operand must have the same characteristics, each operand may specify different types of constants. For example, in a DC instruction with three operands, the first operand might specify four decimal constants, the second a floating-point constant, and the third a character constant.

The symbol that names the DC instruction is the name of the constant (or first constant if the instruction specifies more than one). Relative addressing (e.g., SYMBOL+2) may be used to address the various constants if more than one has been specified, because the number of bytes allocated to each constant can be determined.

The value attribute of the symbol naming the DC instruction is the address of the leftmost byte (after alignment) of the first, or only, constant. The length attribute depends on two things: the type of constant being defined and the presence of a length specification. Implied lengths are assumed for the various constant types in the absence of a length specification. If more than one constant is defined, the length attribute is the length in bytes (specified or implied) of the first constant.

Boundary alignment also varies according to the type of constant being specified and the presence of a length specification. Some constant types are only aligned to a byte boundary, but the DS instruction can be used to force any type of word boundary alignment for them. This is explained under "DS -- Define Storage." Other con-

stants are aligned at various word boundaries (half, full, or double) in the absence of a length specification. If length is specified, no boundary alignment occurs for such constants.

Bytes that must be skipped in order to align the field at the proper boundary are not considered to be part of the constant. In other words, the Location Counter is incremented to reflect the proper boundary (if any incrementing is necessary) before the address value is established. Thus, the symbol naming the constant will not receive a value attribute that is the location of a skipped byte.

Any bytes skipped in aligning statements that do not cause information to be assembled are not zeroed. Thus bytes skipped to align a statement such as DC F'123' are zeroed, and bytes skipped to align a statement such as DS F'123' are not zeroed.

Appendix F summarizes, in chart form, the information concerning constants that is presented in this section.

LITERAL DEFINITIONS: The reader is reminded that the discussion of literals as machine-instruction operands (in Section 2) referred him to the description of the DC operand for the method of writing a literal operand. All subsequent operand specifications are applicable to writing literals, the only differences being:

1. The literal is preceded by an = sign.
2. Multiple operands may not be specified.
3. Unsigned decimal values may be used to express the duplication factor and all modifier values.
4. The duplication factor may not be zero.
5. S-type address constants may not be specified.

Examples of literals appear throughout the balance of the DC instruction discussion.

Operand Subfield 1: Duplication Factor

The duplication factor may be omitted. If specified, it causes the constant(s) to be generated the number of times indicated by the factor. The factor may be specified either by an unsigned decimal value or by a positive absolute expression that is enclosed by parentheses. The duplication factor is applied after the constant is

assembled. All symbols in the expression must be previously defined.

Note that a duplication factor of zero is permitted except in a literal and achieves the same result as it would in a DS instruction. See "Forcing Alignment" under "DS -- Define Storage."

Note: If duplication is specified for an address constant containing a Location Counter reference, the value of the Location Counter used in each duplication is incremented by the length of the constant.

Operand Subfield 2: Type

The type subfield defines the type of constant being specified. From the type specification, the assembler determines how it is to interpret the constant and translate it into the appropriate machine format. The type is specified by a single-letter code as shown in Figure 5-1.

Further information about these constants is provided in the discussion of the constants themselves under "Operand Subfield 4: Constant."

Operand Subfield 3: Modifiers

Modifiers describe the length in bytes desired for a constant (in contrast to an implied length), and the scaling and exponent for the constant. If multiple modifiers are written, they must appear in this sequence: length, scale, exponent. Each is written and used as described in the following text.

LENGTH MODIFIER: This is written as Ln, where n is either an unsigned decimal value or an absolute expression enclosed by parentheses. Any symbols in the expression must be previously defined. The value of n represents the number of bytes of storage that are assembled for the constant. The maximum value permitted for the length modifiers supplied for the various types of constants is summarized in Appendix F. This table also indicates the implied length for each type of constant; the implied length is used unless a length modifier is present. A length modifier may be specified for any type of constant. However, no boundary alignment will be provided when a length modifier is given.

<u>Code</u>	<u>Type of Constant</u>	<u>Machine Format</u>
C	Character	8-bit code for each character
X	Hexadecimal	4-bit code for each hexadecimal digit
B	Binary	binary format
F	Fixed-point	Signed, fixed-point binary format; normally a full word
H	Fixed-point	Signed, fixed-point binary format; normally a half word
E	Floating-point	Short floating-point format; normally a full word
D	Floating-point	Long floating-point format; normally a double word
P	Decimal	Packed decimal format
Z	Decimal	Zoned decimal format
A	Address	Value of address; normally a full word
Y	Address	Value of address; normally a half word
S	Address	Base register and displacement value; a half word
V	Address	Space reserved for external symbol addresses; each address normally a full word

Figure 5-1. Type Codes for Constants

Bit-Length Specification: The length of a constant, in bits, is specified by L.n, where n is specified as stated above and represents the number of bits in storage into which the constant is to be assembled. The value of n may exceed eight and is interpreted to mean an integral number of bytes plus so many bits. For example, L.20 is interpreted as a length of two bytes plus four bits.

Assembly of the first or only constant with bit-length specification starts on a byte boundary. The constant is placed in the high or low order end of the field depending on the type of constant being specified. The constant is padded or truncated to fit the field. If the assembled length does not leave the Location Counter set at a byte boundary, and another bit length constant does not follow, the remainder of the last byte used is filled with zeros. This leaves the location counter set at the next byte boundary. Figure 5-2 shows a fixed-point constant with a specified bit-length of 13, as coded, and as it would appear in storage. Note that the constant has been padded on the left to bring it to its designated 13-bit length.

As coded:

Name	Operation	Operand
BLCON	DC	FL.13'579'

In storage:

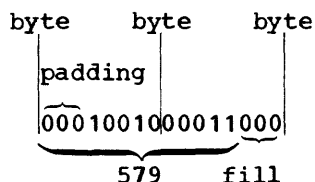


Figure 5-2. Bit-Length Specification (Single Constant)

The implied length of BLCON is two bytes. A reference to BLCON would cause the entire two bytes to be referenced.

When bit-length specification is used in association with multiple constants (see "Operand Subfield 4: Constant" following), each succeeding constant in the list is assembled starting at the next available bit. Figure 5-3 illustrates this.

As coded:

Name	Operation	Operand
BLMCON	DC	FL.10'673,21,57'

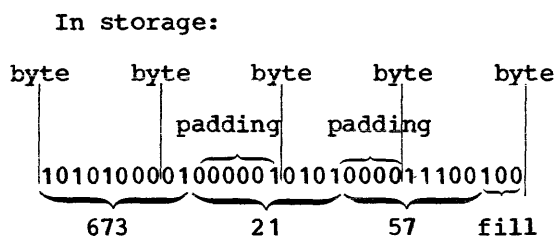


Figure 5-3. Bit-Length Specification (Multiple Constants)

The symbol used as a name entry in a DC assembler instruction takes on the length attribute of the first constant in the list, therefore the implied length of BLMCON in Figure 5-3 is two bytes.

If duplication is specified, filling occurs once at the end of the field occupied by the duplicated constant(s).

When bit-length specification is used in association with multiple operands, assembly of the constant(s) in each succeeding operand starts at the next available bit. Figure 5-4 illustrates this.

As coded:

Name	Operation	Operand
BLMOCON	DC	FL.7'9', CL.10'AB', XL.14'C4'

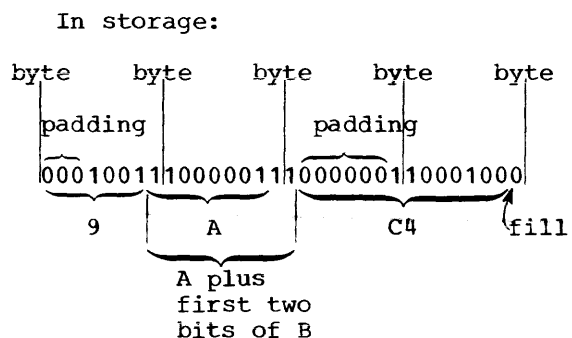


Figure 5-4. Bit-Length Specification (Multiple Operands)

In Figure 5-4, three different types of constants have been specified, one to an operand. Note that the character constant 'AB' which normally would occupy 16 bits is truncated on the right to fit the 10-bit field designated. Note that filling occurs only at the end of the field occupied by all the constants.

SCALE MODIFIER: This modifier is written as Sn, where n is either a decimal value or an absolute expression enclosed by parentheses. Any symbol in the expression must be previously defined. The decimal value or the parenthesized expression may be preceded by a sign; if none is present, a plus sign is assumed. The maximum values for scale modifiers are summarized in Appendix F.

A scale modifier may be used with fixed-point (F, H) and floating-point (E, D) constants only. It is used to specify the amount of internal scaling that is desired, as follows.

Scale Modifier for Fixed-Point Constants: the scale modifier specifies the power of two by which the constant must be multiplied after it has been converted to its binary representation. Just as multiplication of a decimal number by a power of 10 causes the decimal point to move, multiplication of a binary number by a power of two causes the binary point to move. This multiplication has the effect of moving the binary point away from its assumed position in the binary field; the assumed position being to the right of the rightmost position.

Thus, the scale modifier indicates either of the following: (1) the number of binary positions to be occupied by the fractional portion of the binary number, or (2) the number of binary positions to be deleted from the integral portion of the binary number. A positive scale of x shifts the integral portion of the number x binary positions to the left, thereby reserving the rightmost x binary positions for the fractional portion. A negative scale shifts the integral portion of the number right, thereby deleting rightmost integral positions. If a scale modifier does not accompany a fixed-point constant containing a fractional part, the fractional part is lost.

In all cases where positions are lost because of scaling (or the lack of scaling), rounding occurs in the leftmost bit of the lost portion. The rounding is reflected in the rightmost position saved.

Scale Modifier for Floating-Point Constants: Only a positive scale modifier may be used with a floating-point constant. It indicates the number of hexadecimal positions that the fraction is to be shifted to the right. Note that this shift amount is in terms of hexadecimal positions, each of which is four binary positions. (A positive scaling actually indicates that the point is to be moved to the left. However, a floating-point constant is always converted to a fraction,

which is hexadecimally normalized. The point is assumed to be at the left of the leftmost position in the field. Since the point cannot be moved left, the fraction is shifted right.)

Thus, scaling that is specified for a floating-point constant provides an assembled fraction that is unnormalized, i.e., contains hexadecimal zeros in the leftmost positions of the fraction. When the fraction is shifted, the exponent is adjusted accordingly to retain the correct magnitude. When hexadecimal positions are lost, rounding occurs in the leftmost hexadecimal position of the lost portion. The rounding is reflected in the rightmost hexadecimal position saved.

EXPONENT MODIFIER: This modifier is written as E_n , where n is either a decimal value or an absolute expression enclosed by parentheses. Any symbols in the expression must be previously defined. The decimal value or the parenthesized expression may be preceded by a sign; if none is present, a plus sign is assumed. The maximum values for exponent modifiers are summarized in Appendix F.

An exponent modifier may be used with fixed-point (F, H) and floating-point (E, D) constants only. The modifier denotes the power of 10 by which the constant is to be multiplied before its conversion to the proper internal format.

This modifier is not to be confused with the exponent of the constant itself, which is specified as part of the constant and is explained under "Operand Subfield 4: Constant." Both are denoted in the same fashion, as E_n . The exponent modifier affects each constant in the operand, whereas the exponent written as part of the constant only pertains to that constant. Thus, a constant may be specified with an exponent of +2, and an exponent modifier of +5 may precede the constant. In effect, the constant has an exponent of +7.

Note that there is a maximum value, both positive and negative, listed in Appendix F for exponents. This applies both to exponent modifier and exponents specified as part of the constant, or to their sum if both are specified.

Operand Subfield 4: Constant

This subfield supplies the constant (or constants) described by the subfields that precede it. A data constant (all types except A, Y, S, and V) is enclosed by single quotation marks. An address constant

(types A, Y, S, and V) is enclosed by parentheses. To specify two or more constants in the subfield, the constants must be separated by commas and the entire sequence of constants must be enclosed by the appropriate delimiters (i.e., single quotation marks or parentheses). Thus, the format for specifying the constant(s) is one of the following:

<u>Single Constant</u>	<u>Multiple Constants*</u>
'constant'	'constant,...,constant'
(constant)	(constant,...,constant)

* Not permitted for character, hexadecimal, and binary constants.

All constant types except character (C), hexadecimal (X), binary (B), packed decimal (P), and zoned decimal (Z), are aligned on the proper boundary, as shown in Appendix F, unless a length modifier is specified. In the presence of a length modifier, no boundary alignment is performed. If an operand specifies more than one constant, any necessary alignment applies to the first constant only. Thus, for an operand that provides five full-word constants, the first would be aligned on a full-word boundary, and the rest would automatically fall on full-word boundaries.

The total storage requirement of an operand is the product of the length times the number of constants in the operand times the duplication factor (if present) plus any bytes skipped for boundary alignment of the first constant. If more than one operand is present, the storage requirement is derived by summing the requirements for each operand.

If an address constant contains a Location Counter reference, the Location Counter value that is used is the storage address of the first byte the constant will occupy. Thus, if several address constants in the same instruction refer to the Location Counter, the value of the Location Counter varies from constant to constant. Similarly, if a single constant is specified (and it is a Location Counter reference) with a duplication factor, the constant is duplicated with a varying Location Counter value.

The subsequent text describes each of the constant types and provides examples.

Character Constant -- C: Any of the valid 256 punch combinations may be designated in a character constant. Only one character constant may be specified per operand. Since multiple constants within an operand are separated by commas, an attempt to specify two character constants would

result in interpreting the comma separating them as a character.

Special consideration must be given to representing quotation marks and ampersands as characters. Each single quotation mark or ampersand desired as a character in the constant must be represented by a pair of single quotation marks or ampersands. Only one single quotation mark or ampersand appears in storage.

The maximum length of a character constant is 256 bytes. No boundary alignment is performed. Each character is translated into one byte. Double quotation marks or double ampersands count as one character. If no length modifier is given, the size in bytes of the character constant is equal to the number of characters in the constant. If a length modifier is provided, the result varies as follows:

1. If the number of characters in the constant exceeds the specified length, as many rightmost bytes as necessary are dropped.
2. If the number of characters is less than the specified length, the excess rightmost bytes are filled with blanks.

In the following example, the length attribute of FIELD is 12:

Name	Operation	Operand
FIELD	DC	C'TOTAL IS 110'

However, in this next example, the length attribute is 15, and three blanks appear in storage to the right of the zero:

Name	Operation	Operand
FIELD	DC	CL15'TOTAL IS 110'

In the next example, the length attribute of FIELD is 12, although 13 characters appear in the operand. The two ampersands count as only one byte.

Name	Operation	Operand
FIELD	DC	C'TOTAL is &&10'

Note that in the next example, a length of four has been specified, but there are five characters in the constant.

Name	Operation	Operand
FIELD	DC	3CL4'ABCDE'

The generated constant would be:

ABCDABCDABCD

On the other hand, if the length had been specified as six instead of four, the generated constant would have been:

ABCDE ABCDE ABCDE

Note that the same constant could be specified as a literal.

Name	Operation	Operand
	MVC	AREA (12) ,=3CL4'ABCDE'

Hexadecimal Constant -- X: A hexadecimal constant consists of one or more of the hexadecimal digits, which are 0-9 and A-F. Only one hexadecimal constant may be specified per operand. The maximum length of a hexadecimal constant is 256 bytes (512 hexadecimal digits). No word boundary alignment is performed.

Constants that contain an even number of hexadecimal digits are translated as one byte per pair of digits. If an odd number of digits is specified, the leftmost byte has the leftmost four bits filled with a hexadecimal zero, while the rightmost four bits contain the odd (first) digit.

If no length modifier is given, the implied length of the constant is half the number of hexadecimal digits in the constant (assuming that a hexadecimal zero is added to an odd number of digits). If a length modifier is given, the constant is handled as follows:

1. If the number of hexadecimal digit pairs exceeds the specified length, the necessary leftmost bits (and/or bytes) are dropped.
2. If the number of hexadecimal digit pairs is less than the specified length, the necessary bits (and/or bytes) are added to the left and filled with hexadecimal zeros.

An eight-digit hexadecimal constant provides a convenient way to set the bit pattern of a full binary word. The constant in the following example would set the first and third bytes of a word to 1s:

Name	Operation	Operand
	DS	0F
TEST	DC	X'FF00FF00'

The DS instruction sets the location counter to a full word-boundary.

The next example uses a hexadecimal constant as a literal and inserts 1s into bits 24 through 31 of register 5.

Name	Operation	Operand
	IC	5,=X'FF'

In the following example, the digit A would be dropped, because five hexadecimal digits are specified for a length of two bytes:

Name	Operation	Operand
ALPHACON	DC	3XL2'A6F4E'

The resulting constant would be 6F4E, which would occupy the specified two bytes. It would then be duplicated three times, as requested by the duplication factor. If it had merely been specified as X'A6F4E', the resulting constant would have had a hexadecimal zero in the leftmost position:

0A6F4E

Binary Constant -- B: A binary constant is written using 1s and 0s enclosed in quotation marks. Only one binary constant may be specified in an operand. Duplication and length may be specified. The maximum length of a binary constant is 256 bytes.

The implied length of a binary constant is the number of bytes occupied by the constant including any padding necessary. Padding or truncation takes place on the left. The padding bit used is a 0.

The following example shows the coding used to designate a binary constant. BCON would have a length attribute of one.

Name	Operation	Operand
BCON	DC	B'11011101'
BTRUNC	DC	BL1'100100011'
BPAD	DC	BL1'101'

BTRUNC would assemble with the leftmost bit truncated, as follows:

00100011

BPAD would assemble with five zeros as padding, as follows:

00000101

Fixed-Point Constants -- F and H: A fixed-point constant is written as a decimal number, which may be followed by a decimal exponent if desired. The number may be an integer, a fraction, or a mixed number (i.e., one with integral and fractional portions). The format of the constant is as follows:

1. The number is written as a signed or unsigned decimal value. The decimal point may be placed before, within, or after the number, or it may be omitted, in which case the number is assumed to be an integer. A positive sign is assumed if an unsigned number is specified. Unless a scale modifier accompanies a mixed number or fraction, the fractional portion is lost, as explained under "Subfield 3: Modifiers."
2. The exponent is optional. If specified, it is written immediately after the number as En, where n is an optionally signed decimal value specifying the exponent of the factor 10. The exponent may be in the range -85 to +75. If an unsigned exponent is specified, a plus sign is assumed. The exponent causes the value of the constant to be adjusted by the power of 10 that it specifies before the constant is converted to its binary form.

The number is converted to its binary equivalent and is assembled as a full-word or half-word, depending on whether the type is specified as F or H. It is aligned at the proper full-word or half-word boundary if a length is not specified. An implied length of four bytes is assumed for a full-word (F) and two bytes for a half-word (H). However, any length up to and includ-

ing eight bytes may be specified for either type of constant by a length modifier, in which case no boundary alignment occurs.

Maximum and minimum values, exclusive of scaling, for fixed-point constants are:

Length	Max	Min
8	$2^{63}-1$	-2^{63}
4	$2^{31}-1$	-2^{31}
2	$2^{15}-1$	-2^{15}
1	2^7-1	-2^7

The binary number occupies the rightmost portion of the field in which it is placed. The unoccupied portion (i.e., the leftmost bits) is filled with the sign. That is, the setting of the bit designating the sign is the setting for the bits in the unused portion of the field. If the value of the number exceeds the length, the necessary leftmost bits are dropped. A negative number is carried in 2s complement form.

If the rightmost portion of the number must be dropped as a result of scale modifiers, rounding occurs. Any duplication factor that is present is applied after the constant is converted to its binary format and assembled into the proper number of bytes.

A field of three full-words is generated from the statement shown below. The location attribute of CONWRD is the address of the leftmost byte of the first word, and the length attribute is four, the implied length for a full-word fixed-point constant. The expression CONWRD+4 could be used to address the second constant (second word) in the field.

Name	Operation	Operand
CONWRD	DC	3F'658474'

The next statement causes the generation of a two-byte field containing a negative constant. Notice that scaling has been specified in order to reserve six bits for the fractional portion of the constant.

Name	Operation	Operand
HALFCON	DC	HS6'-25.93'

The next constant (3.50) is multiplied by 10 to the -2 before being converted to its binary format. The scale modifier

reserves twelve bits for the fractional portion.

Name	Operation	Operand
FULLCON	DC	HS12'3.50E-2'

The same constant could be specified as a literal:

Name	Operation	Operand
	AH	7,=HS12'3.50E-2'

The final example specifies three constants. Notice that the scale modifier requests four bits for the fractional portion of each constant. The four bits are provided whether or not the fraction exists.

Name	Operation	Operand
THREECON	DC	FS4'10,25.3,100'

Floating-Point Constants -- E and D: A floating-point constant is written as a decimal number, which may be followed by a decimal exponent, if desired. The number may be an integer, a fraction, or a mixed number (i.e., one with integral and fractional portions). The format of the constant is as follows:

1. The number is written as a signed or unsigned decimal value. The decimal point may be placed before, within, or after the number, or it may be omitted, in which case, the number is assumed to be an integer. A positive sign is assumed if an unsigned number is specified.
2. The exponent is optional. If specified, it is written immediately after the number as E_n , where n is an optionally signed decimal value specifying the exponent of the factor 10. The exponent may be in the range -85 to +75. If an unsigned exponent is specified, a plus sign is assumed.

Machine format for a floating-point number is in two parts: the portion containing the exponent, which is sometimes called the characteristic, followed by the portion containing the fraction, which is sometimes

called the mantissa. Therefore, the number specified as a floating-point constant must be converted to a fraction before it can be translated into the proper format. For example, the constant 27.35E2 represents the number 27.35 times 10 to the 2nd. Represented as a fraction, it would be .2735 times 10 to the 4th, the exponent having been modified to reflect the shifting of the decimal point. The exponent may also be affected by the presence of an exponent modifier, as explained under "Operand Subfield 3: Modifiers." Thus, the exponent is also altered before being translated into machine format. Once the constant is converted into the proper exponent and fraction, each is translated into its binary equivalent and arranged in machine floating-point format.

The translated constant is placed in a full word or a double word, depending on whether the type is specified as E or D. It is aligned at the proper word or double word boundary if a length is not specified. An implied length of four bytes is assumed for a full word (E) and eight bytes is assumed for a double word (D). However, any length up to and including eight bytes may be specified for either type of constant by a length modifier, in which case no boundary alignment occurs.

Within the portion of the floating-point field allocated to the fraction, the hexadecimal point is assumed to be to the left of the leftmost hexadecimal digit, and the fraction occupies the leftmost portion of the field. The fraction is normalized (no leading hexadecimal zeros), unless scaling is specified. If the rightmost portion of the fraction must be dropped because of length or scale modifiers, rounding will occur. Negative fractions are carried in true representation, not in the 2s complement form.

Any of the following statements could be used to specify 46.415 as a positive, full-word, floating-point constant; the last is a machine-instruction statement with a literal operand. Note that the last two constants contain an exponent modifier.

Name	Operation	Operand
	DC	E'46.415'
	DC	E'46415E-3'
	DC	E'+464.15E-1'
	DC	E'+.46415E+2'
	DC	EE2'.46415'
	AE	6,=EE2'.46415'

The following would each be generated as double-word floating-point constants.

Name	Operation	Operand
FLOAT	DC	DE+4'+46,-3.729,+473'

Decimal Constants -- P and Z: A decimal constant is written as a signed or unsigned decimal value. If the sign is omitted, a plus sign is assumed. The decimal point may be written wherever desired or may be omitted. Scaling and exponent modifiers may not be specified for decimal constants. The maximum length of a decimal constant is 16 bytes. No word boundary alignment is performed.

The placement of a decimal point in the definition does not affect the assembly of the constant in any way, because, unlike fixed-point and floating-point constants, a decimal constant is not converted to its binary equivalent. The fact that a decimal constant is an integer, a fraction, or a mixed number is not pertinent to its generation. Furthermore, the decimal point is not assembled into the constant. The programmer may determine proper decimal point alignment either by defining his data so that the point is aligned or by selecting machine-instructions that will operate on the data properly (i.e., shift it for purposes of alignment).

If zoned decimal format is specified (Z), each decimal digit is translated into one byte. The translation is done according to the character set shown in Appendix A. The rightmost byte contains the sign as well as the rightmost digit. For packed decimal format (P), each pair of decimal digits is translated into one byte. The rightmost digit and the sign are translated into the rightmost byte. The bit configuration for the digits is identical to the configurations for the hexadecimal digits 0-9 as shown in Section 3 under "Hexadecimal Self-Defining Value." For both packed and zoned decimals, a plus sign is translated into the hexadecimal digit C, and a minus sign into the digit D.

If an even number of packed decimal digits is specified, one digit will be left unpaired, because the rightmost digit is paired with the sign. Therefore, in the leftmost byte, the leftmost four bits will be set to zeros and the rightmost four bits will contain the odd (first) digit.

If no length modifier is given, the implied length for either constant is the number of bytes the constant occupies (taking into account the format, sign, and possible addition of zero bits for packed decimals). If a length modifier is given, the constant is handled as follows:

1. If the constant requires fewer bytes than the length specifies, the necessary number of bytes is added to the left. For zoned decimal format, the decimal digit zero is placed in each added byte. For packed decimals, the bits of each added byte are set to zero.
2. If the constant requires more bytes than the length specifies, the necessary number of leftmost digits or pairs of digits is dropped, depending on which format is specified.

Examples of decimal constant definitions follow.

Name	Operation	Operand
	DC	P'+1.25'
	DC	Z'-543'
	DC	Z'79.68'
	DC	PL3'79.68'

The following statement specifies both packed and zoned decimal constants. The length modifier applies to each constant in the first operand (i.e., to each packed decimal constant). Note that a literal could not specify both operands.

Name	Operation	Operand
DECIMALS	DC	PL8'+25.8,-3874, +2.3'Z'+80,-3.72'

The last example illustrates the use of a packed decimal literal.

Name	Operation	Operand
	UNPK	OUTAREA,=PL2'+25'

ADDRESS CONSTANTS: An address constant is a storage address that is translated into a constant. Address constants are normally used for initializing base registers to facilitate the addressing of storage. Furthermore, they provide the means of communicating between control sections of a multisection program. However, storage addressing and control section communication are also dependent on the use of the USING assembler instruction and the loading of registers. Coding examples that illustrate these considerations are provided in

Section 3 under "Programming with the Using Instruction."

An address constant, unlike other types of constants, is enclosed in parentheses. If two or more address constants are specified in an operand, they are separated by commas, and the entire sequence is enclosed by parentheses. There are four types of address constants: A, Y, S, and V.

Complex Relocatable Expressions: A complex relocatable expression can only be used to specify an A-type or Y-type address constant. These expressions contain two or more unpaired relocatable terms and/or a negative relocatable term in addition to any absolute or paired relocatable terms that may be present. In contrast to relocatable expressions, complex relocatable expressions may represent negative values. A complex relocatable expression might consist of external symbols (which cannot be paired) and designate an address in an independent assembly that is to be linked and loaded with the assembly containing the address constant.

A-Type Address Constant: This constant is specified as an absolute, relocatable, or complex relocatable expression. (Remember that an expression may be single term or multiterm.) The value of the expression is calculated as explained in Section 2, with one exception. The maximum value of the expression may be $2^{31}-1$. The implied length of an A-type constant is four bytes, and the value is placed in the rightmost portion. Alignment is to a full-word boundary, unless a length is specified. A length modifier may be used, in which case no alignment will occur. The length that may be specified depends on the type of expression used for the constant; a length of .1-4 bytes may be used for an absolute expression, while a length of 3-4 bytes may be used for a relocatable or complex relocatable expression.

In the following examples, the field generated from the statement named ACONST contains four constants, each of which occupies four bytes. Note that there is a Location Counter reference in one. The value of the Location Counter will be the address of the first byte allocated to the fourth constant. The second statement shows the same set of constants specified as literals (i.e., address constant literals).

Name	Operation	Operand
ACONST	DC	A (108, LOOP, END~STRT, **4096)
	LM	4, 7, =A (108, LOOP, END~STRT, **4096)

Note: When the Location Counter reference occurs in a literal, as in the LM instruction above, the value of the Location Counter is the address of the first byte of the instruction.

Y-Type Address Constant: A Y-type address constant has the characteristics and format of the A-type constant discussed above except for the following:

1. The constant is assembled as a 16-bit value and aligned to a half-word boundary.
2. The implied length is two bytes.
3. The maximum length of a Y-type address constant is two bytes. If length specification is used, a length of two bytes may be designated for a relocatable or complex expression and .1 to 2 bytes for an absolute expression.

Warning: Specification of relocatable Y-type address constants two or less bytes in length should be avoided in programs destined to be executed on machines having more than 32,767 bytes of storage capacity. In any case Y-type address constants should not be used in programs to be executed under Operating System/360 control.

S-Type Address Constant: The S-type address constant is used to store an address in base-displacement form.

The constant may be specified in two ways:

1. As an absolute or relocatable expression, e.g., S(BETA).
2. As two absolute expressions, the first of which represents the displacement value and the second, the base register, e.g., S(400(13)).

The address value represented by the expression in (1) will be broken down by the assembler into the proper base register and displacement value. An S-type constant is assembled as a half word and aligned on a half-word boundary. The leftmost four bits of the assembled constant represents the base register designation, the remaining 12 bits the displacement value.

If length specification is used, only two bytes may be specified. S-type address constants may not be specified as literals.

V-Type Address Constant: This constant is used to reserve storage for the address of an external symbol that is used for effecting branches to other programs. The constant may not be used for external data references. The constant is specified as one relocatable symbol, which need not be identified by an EXTRN statement. Whatever symbol is used is assumed to be an external

symbol by virtue of the fact that it is supplied in a V-type address constant.

Note that specifying a symbol as the operand of a V-type constant does not constitute a definition of the symbol for this assembly. The implied length of a V-type address constant is four bytes, and boundary alignment is to a full word. A length modifier may be used to specify a length of either three or four bytes, in which case no such boundary alignment occurs. In the following example, 12 bytes will be reserved, because there are three symbols. The value of each assembled constant will be zero until the program is loaded.

Name	Operation	Operand
VCONST	DC	V (SORT, MERGE, CALC)

DS -- DEFINE STORAGE

The DS instruction is used to reserve areas of storage and to assign names to those areas. The use of this instruction is the preferred way of symbolically defining storage for work areas, input/output areas, etc. The size of a storage area that can be reserved by using the DS instruction is limited only by the maximum value of the Location Counter.

Name	Operation	Operand
A symbol or blank	DS	One or more operands, separated by commas, written in the format described in the following text

The format of the DS operand is identical to that of the DC operand; exactly the same subfields are employed and are written in exactly the same sequence as they are in the DC operand. Although the formats are identical, there are two differences in the specification of subfields. They are:

1. The specification of data (subfield 4) is optional in a DS operand, but it is mandatory in a DC operand.
2. The maximum length that may be specified for character (C) and hexadecimal (X) field types is 65,535 bytes rather than 256 bytes.

If a DS operand specifies a constant in subfield 4, and no length is specified in subfield 3, the assembler determines the length of the data and reserves the appropriate amount of storage. It does not assemble the constant. The ability to specify data and have the assembler calculate the storage area that would be required for such data is a convenience to the programmer. If he knows the general format of the data that will be placed in the storage area during program execution, all he needs to do is show it as the fourth subfield in a DS operand. The assembler then determines the correct amount of storage to be reserved, thus relieving the programmer of length considerations.

If the DS instruction is named by a symbol, its value attribute is the location of the leftmost byte of the reserved area. The length attribute of the symbol is the length (implied or explicit) of the type of data specified. Should the DS have a series of operands, the length attribute for the symbol is developed from the first item in the first operand. Any positioning required for aligning the storage area to the proper type of boundary is done before the address value is determined. Bytes skipped for alignment are not set to zero.

Each field type (e.g., hexadecimal, character, floating-point) is associated with certain characteristics (these are summarized in Appendix F). The associated characteristics will determine which field-type code the programmer selects for the DS operand and what other information he adds, notably a length specification or a duplication factor. For example, the E floating-point field and the F fixed-point field both have an implied length of four bytes. The leftmost byte is aligned to a full-word boundary. Thus, either code could be specified if it were desired to reserve four bytes of storage aligned to a full-word boundary. To obtain a length of eight bytes, one could specify either the E or F field type with a length modifier of eight. However, a duplication factor would have to be used to reserve a larger area, because the maximum length specification for either type is eight bytes. Note also that specifying length would cancel any special boundary alignment.

In contrast, packed and zoned decimal (P and Z), character (C), hexadecimal (X), and binary (B) fields have an implied length of one byte. Any of these codes, if used, would have to be accompanied by a length modifier, unless just one byte is to be reserved. Although no alignment occurs, the use of C and X field types permits greater latitude in length specifications, the maximum for either type being 65,535 bytes. (Note that this differs from the

maximum for these types in a DC instruction.) Unless a field of one byte is desired, either the length must be specified for the C, X, P, Z, or B field types, or else the data must be specified (as the fourth subfield), so that the assembler can calculate the length.

To define four 10-byte fields and one 100-byte field, the respective DS statements might be as follows:

Name	Operation	Operand
FIELD	DS	4CL10
AREA	DS	CL100

Although FIELD might have been specified as one 40-byte field, the preceding definition has the advantage of providing FIELD with a length attribute of 10. This would be pertinent when using FIELD as a SS machine-instruction operand.

Additional examples of DS statements are shown below:

Name	Operation	Operand
ONE	DS	CL80 (one 80-byte field, length attribute of 80)
TWO	DS	80C (80 one-byte fields, length attribute of one)
THREE	DS	6F (six full words, length attribute of four)
FOUR	DS	D (one double word, length attribute of eight)
FIVE	DS	4H (four half-words, length attribute of two)

Note: A DS statement causes the storage area to be reserved but not set to zeros. No assumption should be made as to the contents of the reserved area.

Special Uses of the Duplication Factor

FORCING ALIGNMENT: The Location Counter can be forced to a double-word, full-word, or half-word boundary by using the appropriate field type (e.g., D, F, or H) with a duplication factor of zero. This method may be used to obtain boundary alignment that otherwise would not be provided. For example, the following statements would set the Location Counter to the

next double-word boundary and then reserve storage space for a 128-byte field (whose leftmost byte would be on a double-word boundary).

Name	Operation	Operand
	DS	0D
AREA	DS	CL128

DEFINING FIELDS OF AN AREA: A DS instruction with a duplication factor of zero can be used to assign a name to an area of storage without actually reserving the area. Additional DS and/or DC instructions may then be used to reserve the area and assign names to fields within the area (and generate constants if DC is used).

For example, assume that 80-character records are to be read into an area for processing and that each record has the following format:

Positions 5-10	Payroll Number
Positions 11-30	Employee Name
Positions 31-36	Date
Positions 47-54	Gross Wages
Positions 55-62	Withholding Tax

The following example illustrates how DS instructions might be used to assign a name to the record area, then define the fields of the area and allocate the storage for them. Note that the first statement names the entire area by defining the symbol RDAREA; the statement gives RDAREA a length attribute of 80 bytes, but does not reserve any storage. Similarly, the fifth statement names a 6-byte area by defining the symbol DATE; the three subsequent statements actually define the fields of DATE and allocate storage for them. The second, ninth, and last statements are used for spacing purposes and, therefore, are not named.

Name	Operation	Operand
RDAREA	DS	0CL80
	DS	CL4
PAYNO	DS	CL6
NAME	DS	CL20
DATE	DS	0CL6
DAY	DS	CL2
MONTH	DS	CL2
YEAR	DS	CL2
	DS	CL10
GROSS	DS	CL8
FEDTAX	DS	CL8
	DS	CL18

CCW -- DEFINE CHANNEL COMMAND WORD

The CCW instruction provides a convenient way to define and generate an eight-byte channel command word aligned at a double-word boundary. The internal machine format of a channel command word is shown in Table 5-1. The format of the CCW instruction statement is:

Name	Operation	Operand
A symbol or blank	CCW	Four operands, separated by commas, specifying the contents of the channel command word in the format described in the following text

All four operands must appear. They are written, from left to right, as follows:

1. An absolute expression that specifies the command code. This expression's value is right-justified in byte 1.
2. An absolute or relocatable expression specifying the data address. The value of this expression is right-justified in bytes 2-4.
3. An absolute expression that specifies the flags for bits 32-36 and zeros for bits 37-39. The value of this expression is right-justified in byte 5. (Byte 6 is set to zero.)
4. An absolute expression that specifies the count. The value of this expression is right-justified in bytes 7-8.

The following is an example of a CCW statement:

Name	Operation	Operand
	CCW	2,READAREA,X'48',80

Note that the form of the third operand sets bits 37-39 to zero, as required. The bit pattern of this operand is as follows:

<u>32-35</u>	<u>36-39</u>
0100	1000

If there is a symbol in the name field of the CCW instruction, it is assigned the address value of the leftmost byte of the channel command word. The length attribute of the symbol is eight.

Table 5-1. Channel Command Word

Byte	Bits	Usage
1	0-7	Command code
2-4	8-31	Data address
5	32-36	Flags
	37-39	Must be zero
6	40-47	Set to zero
7-8	48-63	Count

LISTING CONTROL INSTRUCTIONS

The listing control instructions are used to identify an assembly listing and assembly output cards, to provide blank lines in an assembly listing, and to designate how much detail is to be included in an assembly listing. In no case are instructions or constants generated in the object program.

TITLE -- IDENTIFY ASSEMBLY OUTPUT

The TITLE instruction enables the programmer to identify the assembly listing and assembly output cards. The format of the TITLE instruction statement is as follows:

Name	Operation	Operand
Name or blank	TITLE	A sequence of characters, enclosed in single quotation marks

The name field may contain a name of from one to four alphabetic or numeric characters in any combination. The contents of the name field are punched into columns 73-76 of all the output cards for the program except those produced by the PUNCH and REPRO assembler instructions. Only the first TITLE statement in a program may have a name in the name field. The name field of all subsequent TITLE statements must be blank.

The operand field may contain up to 100 characters enclosed in single quotation marks. The contents of the operand field are printed at the top of each page of the assembly listing.

A program may contain more than one TITLE statement. Each TITLE statement pro-

vides the heading for pages in the assembly listing that follow it, until another TITLE statement is encountered. Additionally, the first TITLE statement in a program provides the heading for pages of the assembly listing that precede it. Each TITLE statement encountered after the first one causes the listing to be advanced to a new page (before the heading is printed).

For example, if the following statement is the first TITLE statement to appear in a program:

Name	Operation	Operand
PGM1	TITLE	'FIRST HEADING'

then PGM1 is punched into all of the output cards (columns 73-76) and this heading appears at the top of each page: FIRST HEADING.

If the following statement occurs later in the same program:

Name	Operation	Operand
	TITLE	'A NEW HEADING'

then, PGM1 is still punched into the output cards, but each following page begins with the heading: A NEW HEADING.

Note: The sequence number of the cards in the output deck is contained in columns 77-80.

EJECT -- START NEW PAGE

The EJECT instruction causes the next line of the listing to appear at the top of a new page. This instruction provides a convenient way to separate routines in the program listing. The format of the EJECT instruction statement is as follows:

Name	Operation	Operand
Blank	EJECT	Not used; should be blank

If the next line of the listing normally appears at the top of a new page, the EJECT statement has no effect. Two EJECT statements may be used in succession to obtain a completely blank page.

DATA - Constants are printed out in full in the listing.
 NODATA - Only the first eight bytes (16 hexadecimal digits) or the first constant, whichever is shorter, of the assembled data is printed on the listing.

SPACE -- SPACE LISTING

The SPACE instruction is used to insert one or more blank lines in the listing. The format of the SPACE instruction statement is as follows:

A program may contain any number of PRINT statements. A PRINT statement controls the printing of the assembly listing until another PRINT statement is encountered.

Until the first PRINT statement (if any) is encountered, the following is assumed:

Name	Operation	Operand
Blank	SPACE	A decimal value or blank

Name	Operation	Operand
	PRINT	ON, NODATA, GEN

A decimal value is used to specify the number of blank lines to be inserted in the assembly listing. A blank operand causes one blank line to be inserted. If this value exceeds the number of lines remaining on the listing page, the statement will have the same effect as an EJECT statement.

For example, if the statement:

Name	Operation	Operand
	DC	XL256'00'

PRINT -- PRINT OPTIONAL DATA

appears in a program, 256 bytes of zeros are assembled. If the statement:

The PRINT instruction is used to control printing of the assembly listing. The format of the PRINT instruction statement is:

Name	Operation	Operand
	PRINT	DATA

Name	Operation	Operand
Blank	PRINT	One to three operands

is the last PRINT statement to appear before the DC statement, all 256 bytes of zeros are printed in the assembly listing. However, if:

One to three of the following operands are used:

Name	Operation	Operand
	PRINT	NODATA

- ON - A listing is printed.
- OFF - No listing is printed.
- GEN - All statements generated by macro-instructions are printed.
- NOGEN - Statements generated by macro-instructions are not printed. However, the macro-instruction itself will appear in the listing.

is the last PRINT statement to appear before the DC statement, only eight bytes of zeros are printed in the assembly listing.

Whenever an operand is omitted, it is assumed to be unchanged and continues according to its last specification.

PROGRAM CONTROL INSTRUCTIONS

The program control instructions are used to specify the end of an assembly, to set the Location Counter to a value or word boundary, to insert previously written coding in the program, to specify the placement of literals in storage, to check the sequence of input cards, to indicate statement format, and to punch a card. Except for the CNOP and COPY instructions, none of these assembler instructions generate instructions or constants in the object program.

ICTL -- INPUT FORMAT CONTROL

The ICTL instruction allows the programmer to alter the normal format of his source program statements. The ICTL statement must precede all other statements in the source program and may be used only once. The format of the ICTL instruction statement is as follows:

Name	Operation	Operand
Blank	ICTL	1-3 decimal values of the form b,e,c,

Operand b specifies the begin column of the source statement. It must always be specified, and must be from 1-40, inclusive. Operand e specifies the end column of the source statement. The end column, when specified, must be from 41-80, inclusive; when not specified, it is assumed to be 71. The column after the end column is used to indicate whether the next card is a continuation card. Operand c specifies the continue column of the source statement. The continue column, when specified, must be from 2-40 and must be greater than b. If the continue column is not specified, or if column 80 is specified as the end column, the assembler assumes that there are no continuation cards, and all statements must be contained on a single card.

If no ICTL statement is used in the source program, the assembler assumes that 1, 71, and 16 are the begin, end, and continue columns, respectively.

The next example designates the begin column as column 25. Since the end column is not specified, it is assumed to be column 71. No continuation cards are rec-

ognized because the continue column is not specified.

Name	Operation	Operand
	ICTL	25

ISEQ -- INPUT SEQUENCE CHECKING

The ISEQ instruction is used to check the sequence of input cards. The format of the ISEQ instruction statement is as follows:

Name	Operation	Operand
Blank	ISEQ	Two decimal values of the form l,r; or blank

The operands l and r, respectively, specify the leftmost and rightmost columns of the field in the input cards to be checked. Operand r must be equal to or greater than operand l. Columns to be checked must not be between the "begin" and "end" columns.

Sequence checking begins with the first card following the ISEQ statement. Comparison of adjacent cards makes use of the eight-bit internal collating sequence.

An ISEQ statement with a blank operand terminates the operation. Checking may be resumed with another ISEQ statement.

Sequence checking is only performed on statements contained in the source program. Statements inserted by the COPY assembler-instruction or generated by a macro-instruction are not checked for sequence.

PUNCH -- PUNCH A CARD

The PUNCH assembler-instruction causes the data in the operand to be punched into a card. One PUNCH statement produces one punched card. As many PUNCH statements may be used as are necessary. The format is:

Name	Operation	Operand
Blank	PUNCH	'PUNCH A CARD'

Name	Operation	Operand
Blank	ORG	A relocatable expression or blank

Using character representation, the operand is written as a string of up to 80 characters enclosed in single quotation marks. All characters, including blank, are valid. The position immediately to the right of the left quotation mark is regarded as column one of the card to be punched. The assembly program does not process the data in the operand of a PUNCH statement other than causing it to be punched in a card.

Any symbols in the expression must have been previously defined. The unpaired relocatable symbol must be defined in the same control section in which the ORG statement appears.

If used, PUNCH statements must precede all statements composing the first or only control section of the program. The output cards produced by PUNCH precede all other cards in the object program card deck.

The Location Counter is set to the value of the expression in the operand. If the operand is omitted, the Location Counter is set to a location that is one byte higher than the maximum location assigned for the control section up to this point.

An ORG statement must not be used to specify a location below the beginning of the control section in which it appears. For example, the statement:

REPRO -- REPRODUCE FOLLOWING CARD

Name	Operation	Operand
	ORG	*-500

The REPRO assembler-instruction causes data on the following statement line to be punched into a card. The data is not processed other than causing it to be punched in a card. One REPRO instruction produces one punched card. The REPRO instruction may appear at any point in the program. REPRO statements that occur before all statements composing the first or only control section will punch cards which precede the ESD cards of the object deck. The format is:

is invalid if it appears less than 500 bytes from the beginning of the current control section.

If it is desired to reset the Location Counter to a value that is one byte beyond the highest location yet assigned (in the control section), the following statement would be used:

Name	Operation	Operand
Blank	REPRO	Blank

Name	Operation	Operand
	ORG	

The line to be reproduced may contain any combination of up to 80 valid characters. Characters may be entered starting in column 1 and continue through column 80 of the line. Column 1 of the line corresponds to column 1 of the card to be punched.

If previous ORG statements have reduced the Location Counter for the purpose of redefining a portion of the current control section, an ORG statement with an omitted operand can then be used to terminate the effects of such statements and restore the Location Counter to its highest setting plus one.

ORG -- SET LOCATION COUNTER

LTORG -- BEGIN LITERAL POOL

The ORG instruction is used to alter the setting of the Location Counter for the current control section. The format of the ORG instruction statement is:

The LTOrg instruction causes all literals thus far encountered in the source program to be assembled at appropriate boundaries starting at the first double-

word boundary following the LTORG statement. The format of the LTORG instruction statement is:

Name	Operation	Operand
Symbol or blank	LTORG	Not used

The symbol represents the address of the first byte of the literal pool. It has a length attribute of one.

Special Addressing Consideration

Any literals used after the last LTORG statement in a program are placed at the end of the first control section. If there are no LTORG statements in a program, all literals used in the program are placed at the end of the first control section. In these circumstances the programmer must ensure that the first control section is always addressable. This means that the base address register for the first control section should not be changed through usage in subsequent control sections. If the programmer does not wish to reserve a register for this purpose he may place a LTORG statement at the end of each control section thereby ensuring that all literals appearing in that section are addressable.

Duplicate Literals

If duplicate literals occur within the range controlled by one LTORG statement, only one literal is stored. Literals are considered duplicates only if their specifications are identical. A literal will be stored, even if it appears to duplicate another literal, if it is an A-type address constant containing any reference to the Location Counter.

The following examples illustrate how the assembler stores pairs of literals, if the placement of each pair is controlled by the same LTORG statement.

X'F0'	}	Both are stored
C'0'		
XL3'0'	}	Both are stored
HL3'0'		
A(*+4)	}	Both are stored
A(+4)		
X'FFFF'	}	Identical; the first is stored
X'FFFF'		

CNOP -- CONDITIONAL NO OPERATION

The CNOP instruction allows the programmer to align an instruction at a specific word boundary. If any bytes must be skipped in order to align the instruction properly, the assembler insures an unbroken instruction flow by generating no-operation instructions. This facility is useful in creating calling sequences consisting of a linkage to a subroutine followed by parameters such as channel command words (CCW).

The CNOP instruction insures the alignment of the Location Counter setting to a half-word, word, or double-word boundary. If the Location Counter is already properly aligned, the CNOP instruction has no effect. If the specified alignment requires the Location Counter to be incremented, one to three no-operation instructions are generated, each of which uses two bytes.

The format of the CNOP instruction statement is as follows:

Name	Operation	Operand
Blank	CNOP	Two absolute expressions of the form b,w

Any symbols used in the expressions in the operand field must have been previously defined.

Operand b specifies at which byte in a word or double word the Location Counter is to be set; b can be 0, 2, 4, or 6. Operand w specifies whether byte b is in a word (w=4) or double word (w=8). The following pairs of b and w are valid:

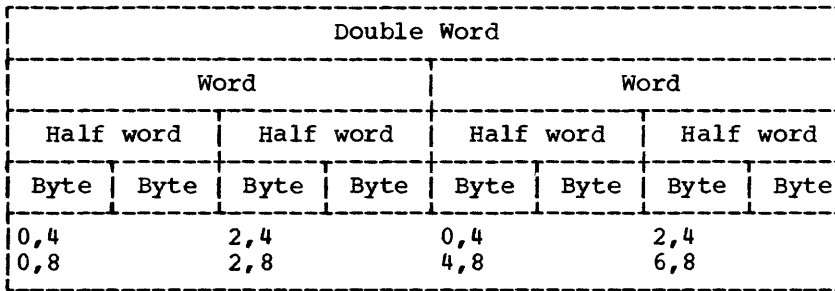


Figure 5-5. CNOP Alignment

b,w Specifies

- 0,4 Beginning of a word
- 2,4 Middle of a word
- 0,8 Beginning of a double word
- 2,8 Second half word of a double word
- 4,8 Middle (third half word) of a double word
- 6,8 Fourth half word of a double word

Figure 5-5 shows the position in a double word that each of these pairs specifies. Note that both 0,4 and 2,4 specify two locations in a double word.

Assume that the Location Counter is currently aligned at a double-word boundary. Then the CNOP instruction in this sequence:

Name	Operation	Operand
	CNOP	0,8
	BALR	2,14

has no effect; it is merely printed in the assembly listing. However, this sequence:

Name	Operation	Operand
	CNOP	6,8
	BALR	2,14

causes three branch-on-conditions (no-operations) to be generated, thus aligning the BALR instruction at the last half-word in a double word as follows:

Name	Operation	Operand
	BCR	0,0
	BCR	0,0
	BCR	0,0
	BALR	2,14

After the BALR instruction is generated, the Location Counter is at a double-word boundary, thereby insuring an unbroken instruction flow.

COPY -- COPY PREDEFINED SOURCE CODING

The COPY instruction obtains source-language coding from a library and includes it in the program currently being assembled. The format of the COPY instruction statement is as follows:

Name	Operation	Operand
Blank	COPY	One symbol

The operand is a symbol that identifies the section of coding to be copied.

The assembler inserts the requested coding immediately after the COPY statement is encountered. The requested coding may not contain another COPY statement.

If identical COPY statements are encountered, the coding they request is brought into the program each time.

END -- END ASSEMBLY

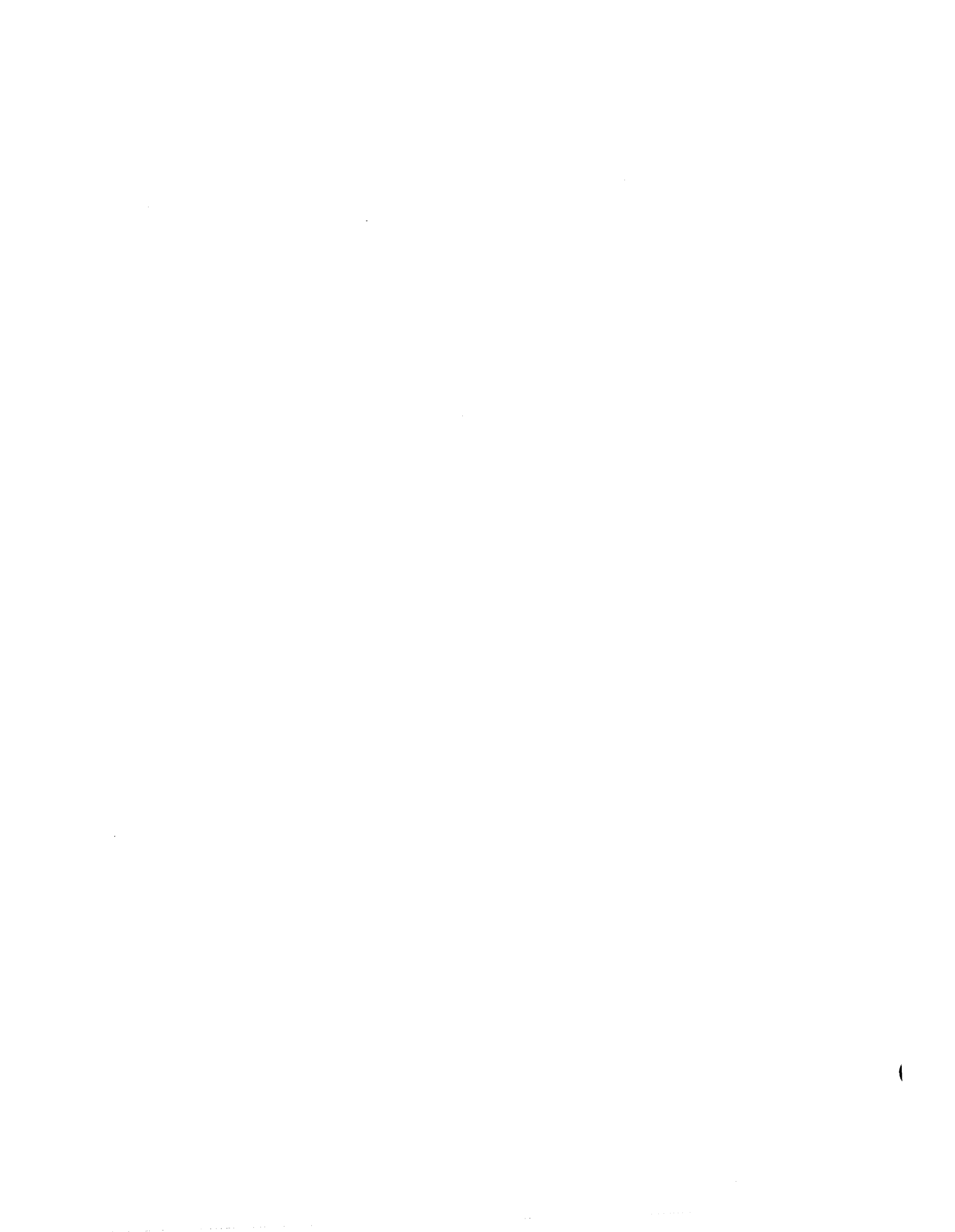
The END instruction terminates the assembly of a program. It may also designate a point in the program or in a separately assembled program to which control may be transferred after the program is loaded. The END instruction must always be the last statement in the source program.

The format of the END instruction statement is as follows:

Name	Operation	Operand
Blank	END	A relocatable expression or blank

The operand specifies the point to which control is transferred when loading is complete. This point is usually the first machine-instruction in the program, as shown in the following sequence.

Name	Operation	Operand
NAME	CSECT	
AREA	DS	50F
BEGIN	BALR	2,0
	USING	*,2
	.	
	.	
	.	
	END	BEGIN



PART II -- THE MACRO LANGUAGE

SECTION 6: INTRODUCTION TO THE MACRO LANGUAGE

SECTION 7: HOW TO PREPARE MACRO-DEFINITIONS

SECTION 8: HOW TO WRITE MACRO-INSTRUCTIONS

SECTION 9: HOW TO WRITE CONDITIONAL ASSEMBLY INSTRUCTIONS

SECTION 10: EXTENDED FEATURES OF THE MACRO LANGUAGE

The Operating System/360 macro language is an extension of the Operating System/360 assembler language.

The macro language provides the programmer with a convenient way to write a definition that can be used to generate a desired sequence of assembler language statements many times in one or more programs.

The definition is written only once, and a single statement, a macro-instruction statement, is written each time a programmer wants to generate the desired sequence of assembler language statements.

This facility simplifies the coding of programs, reduces the chance of committing programming errors, and ensures that standard sequences of assembler language statements are used to accomplish desired functions.

An additional facility, called conditional assembly, allows one to specify assembler language statements which may or may not be assembled, depending upon conditions evaluated at assembly time. These conditions are usually tests of values, which may be defined, set, changed, and tested during the course of the assembly itself. The conditional assembly facility may be used without using macro-instruction statements.

THE MACRO-INSTRUCTION STATEMENT

A macro-instruction statement (hereinafter called a macro-instruction) is a source program statement that is processed by the assembler, just as assembler language statements are source program statements that are processed by the assembler.

The assembler generates a sequence of assembler language statements for each occurrence of the same macro-instruction. The generated statements are then processed like any other assembler language statement.

Three types of macro-instructions may be written. They are: positional, keyword, and mixed-mode macro-instructions.

Positional macro-instructions permit the programmer to write the operands of a

macro-instruction in a fixed order. Keyword macro-instructions permit the programmer to write the operands of a macro-instruction in a variable order. Mixed-mode macro-instructions permit the programmer to use the features of both positional and keyword macro-instructions in the same macro-instruction.

THE MACRO-DEFINITION

Before a macro-instruction can be assembled, a macro-definition must be available to the assembler.

A macro-definition is a set of statements that provides the assembler with: (1) the mnemonic operation code and the format of the macro-instruction, and (2) the sequence of statements the assembler generates when the macro-instruction appears in the source program.

Every macro-definition consists of a macro-definition header statement, a macro-instruction prototype statement, one or more model statements, COPY statements, MEXIT, MNOTE, or conditional assembly instructions, and a macro-definition trailer statement.

The macro-definition header and trailer statements indicate to the assembler the beginning and end, respectively, of a macro-definition.

The macro-instruction prototype statement specifies the mnemonic operation code and the format of the macro-instruction.

The model statements are used by the assembler to generate the assembler language statements that replace each occurrence of the macro-instruction.

The COPY statements may be used to copy model statements, MEXIT, MNOTE or conditional assembly instructions from a system library into a macro-definition.

The MEXIT instruction can be used to terminate processing of a macro-definition.

The MNOTE instruction can be used to generate an error message when the rules for writing a particular macro-instruction are violated.

The conditional assembly instructions may be used to vary the sequence of statements generated for each occurrence of a macro-instruction. Conditional assembly instructions may also be used outside macro-definitions, i.e., among the assembler language statements in the program.

THE MACRO LIBRARY

The same macro-definition may be made available to more than one source program by placing the macro-definition in the macro library. The macro library is a collection of macro-definitions that can be used by all the assembler language programs in an installation. Once a macro-definition has been placed in the macro library it may be used by writing its corresponding macro-instruction in a source program. The procedure for placing macro-definitions in the macro library is described in the data management publication.

SYSTEM MACRO-INSTRUCTIONS

The macro-instructions that correspond to macro-definitions prepared by IBM are called system macro-instructions. System macro-instructions are described in the control program services publication.

VARYING THE GENERATED STATEMENTS

Each time a macro-instruction appears in the source program it is replaced by the same sequence of assembler language statements, unless one or more conditional assembly instructions appear in the macro-definition. Conditional assembly instructions are used to vary the number and format of the generated statements.

VARIABLE SYMBOLS

A variable symbol is a type of symbol that is assigned different values by either the programmer or the assembler. Thus, variable symbols allow different values to be assigned to one symbol. When the assembler uses a macro-definition to determine what statements are to replace a macro-instruction, variable symbols in the model statements are replaced with the values assigned to them. By changing the values

assigned to variable symbols the programmer has the ability to vary parts of the generated statements.

A variable symbol is written as an ampersand followed by from one through seven letters and/or digits, the first of which must be a letter. Elsewhere, two ampersands must be used to represent an ampersand.

Types of Variable Symbols

There are three types of variable symbols: symbolic parameters, system variable symbols, and SET symbols. The SET symbols are further broken down into SETA symbols, SETB symbols, and SETC symbols. The three types of variable symbols differ in the way they are assigned values.

Assigning Values to Variable Symbols

Symbolic parameters are assigned values by the programmer each time he writes a macro-instruction.

System variable symbols are assigned values by the assembler each time it processes a macro-instruction.

SET symbols are assigned values by the programmer by means of conditional assembly instructions.

Global SET Symbols

The values assigned to SET symbols in one macro-definition may be used to vary the statements that appear in other macro-definitions. All SET symbols used for this purpose must be defined by the programmer as global SET symbols. All other SET symbols (i.e., those which may be used to vary statements that appear in the same macro-definition) must be defined by the programmer as local SET symbols. Local SET symbols and the other variable symbols (that is, symbolic parameters and system variable symbols) are local variable symbols. Global SET symbols are global variable symbols.

ORGANIZATION OF THIS PART OF THE PUBLICATION

Sections 7 and 8 describe the basic rules for preparing macro-definitions and for writing macro-instructions.

Section 9 describes the rules for writing conditional assembly instructions.

Section 10 describes additional features of the macro language, including rules for defining global SET symbols, preparing key-

word and mixed-mode macro-definitions, and writing keyword and mixed-mode macro-instructions.

Appendix G contains a reference summary of the entire macro language.

Examples of the use of all of the features of the language appear throughout the remainder of the publication. These examples illustrate the use of particular features. However, they are not meant to show the full versatility of these features.

SECTION 7: HOW TO PREPARE MACRO-DEFINITIONS

A macro-definition consists of:

1. A macro-definition header statement.
2. A macro-instruction prototype statement.
3. One or more model statements, COPY statements, MEXIT, MNOTE, or conditional assembly instructions.
4. A macro-definition trailer statement.

Except for MEXIT, MNOTE, and conditional assembly instructions, this section of the publication describes all of the statements that may be used to prepare macro-definitions. Conditional assembly instructions are described in Section 9. MEXIT and MNOTE instructions are described in Section 10.

Macro-definitions appearing in a source program must appear before all PUNCH and REPRO statements and all statements which pertain to the first control section. Specifically, only the listing control instructions (EJECT, PRINT, SPACE, and TITLE), ICTL and ISEQ instructions, and comments statements may occur before the macro-definitions. All but the ICTL instruction may appear between macro-definitions if there is more than one definition in the source program.

MACRO -- MACRO-DEFINITION HEADER

The macro-definition header statement indicates the beginning of a macro-definition. It must be the first statement in every macro-definition. The format of this statement is:

Name	Operation	Operand
Blank	MACRO	Blank

MEND -- MACRO-DEFINITION TRAILER

The macro-definition trailer statement indicates the end of a macro-definition. It must be the last statement in every macro-definition. The format of this statement is:

Name	Operation	Operand
Blank	MEND	Blank

MACRO-INSTRUCTION PROTOTYPE

The macro-instruction prototype statement (hereinafter called the prototype statement) specifies the mnemonic operation code and the format of all macro-instructions that refer to the macro-definition. It must be the second statement of every macro-definition. The format of this statement is:

Name	Operation	Operand
A symbolic parameter or blank	A symbol	Zero or more symbolic parameters, separated by commas

The symbolic parameters are used in the macro-definition to represent the name field and operands of the corresponding macro-instruction. A complete description of symbolic parameters appears under "Model Statements."

The name field of the prototype statement may be blank or it may contain a symbolic parameter.

The symbol in the operation field is the mnemonic operation code that must appear in all macro-instructions that refer to this macro-definition. The mnemonic operation code must not be the same as the mnemonic operation code of another macro-definition in the source program or of a machine instruction or assembler instruction.

The operand field may contain zero or more symbolic parameters separated by commas.

The following is a prototype statement.

Name	Operation	Operand
&NAME	MOVE	&TO,&FROM

Statement Format

The prototype statement may be written in a format different from that used for assembler language statements. The normal format is described in Part I of this publication. The alternate format described here allows the programmer to write an operand on each line, and allows the interspersing of operands and comments in the statement.

In the alternate format, as in the normal format, the name and operation fields must appear on the first line of the statement, and at least one blank must follow the operation field on that line. Both types of statement formats may be used in the same prototype statement.

The rules for using the alternate statement format are:

1. If an operand is followed by a comma and a blank, and the column after the end column contains a nonblank character, the operand field may be continued on the next line starting in the continue column. More than one operand may appear on the same line.
2. Comments may appear after the blank that indicates the end of an operand, up to and including the end column.
3. If the next line starts after the continue column, the information entered on the next line is considered to be comments, and the operand field is considered terminated. Any subsequent continuation lines are considered to contain only comments.

Note: A prototype statement may be written on as many continuation lines as there are operands.

The following examples illustrate: (1) the normal statement format, (2) the alternate statement format, and (3) the combination of both statement formats.

Name	Operation	Operand	Comments
NAME1	OP1	OPERAND1, OPERAND2, OPERAND3,	THIS IS THE NORMAL STATEMENT FORMAT
NAME2	OP2	OPERAND1, OPERAND2, OPERAND3,	THIS IS THE ALTERNATE STATEMENT FORMAT
NAME3	OP3	OPERAND1, OPERAND2, OPERAND3, OPERAND4, OPERAND5	THIS IS A COMBINATION OF BOTH STATEMENT FORMATS

MODEL STATEMENTS

Model statements are the macro-definition statements from which the desired sequences of assembler language statements are generated. One or more model statements may follow the prototype statement. A model statement consists of one to four fields. They are, from left to right, the name, operation, operand, and comments fields.

The name field may be blank, or it may contain a symbol or symbolic parameter.

The operation field may contain any machine or assembler instruction mnemonic operation code, except COPY, END, ICTL, ISEQ, PRINT, and START; or it may contain a symbolic parameter. Variable symbols may not be used to generate the following mnemonic operation codes, nor may variable symbols be used in the name and operand fields of these instructions: COPY, END, ICTL, ISEQ, PRINT, REPRO, and START. Variable symbols may not be used to generate a macro-instruction mnemonic operation code. Although COPY statements may not be used as model statements, they may be part of a macro-definition. The use of COPY statements is described under "COPY Statements."

The operand field may contain symbols, symbolic parameters, or other combinations of characters.

The comments field may contain any combination of characters.

If a REPRO statement is used as a model statement, the line following it must not contain symbolic parameters.

SYMBOLIC PARAMETERS

A symbolic parameter is a type of variable symbol that is assigned values by the programmer when he writes a macro-instruction. The programmer may vary statements that are generated for each occurrence of a macro-instruction by varying the values assigned to symbolic parameters.

A symbolic parameter consists of an ampersand followed by from one through seven letters and/or digits, the first of which must be a letter. Elsewhere, two ampersands must be used to represent an ampersand.

The programmer should not use &SYS as the first four characters of a symbolic parameter.

The following are valid symbolic parameters:

&READER &LOOP2
 &A23456 &N
 &X4F2 &S4

The following are invalid symbolic parameters:

CARDAREA (first character is not an ampersand)
 &256B (first character after ampersand is not a letter)
 &AREA2456 (more than seven characters after the ampersand)
 &BCD%34 (contains a special character other than initial ampersand)
 &IN AREA (contains a special character, i.e., blank, other than initial ampersand)

Any symbolic parameters in a model statement must appear in the prototype statement of the macro-definition.

The following is an example of a macro-definition. Note that the symbolic parameters in the model statements appear in the prototype statement.

	Name	Operation	Operand
Header		MACRO	
Prototype	&NAME	MOVE	&TO, &FROM
Model	&NAME	ST	2, SAVE
Model		L	2, &FROM
Model		ST	2, &TO
Model		L	2, SAVE
Trailer		MEND	

Symbolic parameters in model statements are replaced by the characters of the macro-instruction that correspond to the symbolic parameters.

In the following example the characters HERE, FIELD A, and FIELD B of the MOVE macro-instruction correspond to the symbolic parameters &NAME, &TO, and &FROM, respectively, of the MOVE prototype statement.

Name	Operation	Operand
HERE	MOVE	FIELD A, FIELD B

Any occurrence of the symbolic parameters &NAME, &TO, and &FROM in a model statement will be replaced by the characters HERE, FIELD A, and FIELD B, respectively. If the preceding macro-instruction was used in a source program, the following assembler language statements would be generated:

Name	Operation	Operand
HERE	ST	2, SAVE
	L	2, FIELD B
	ST	2, FIELD A
	L	2, SAVE

The example below illustrates another use of the MOVE macro-instruction using different operands than those that appear in the preceding example.

	Name	Operation	Operand
Macro	LABEL	MOVE	IN, OUT
Generated	LABEL	ST	2, SAVE
Generated		L	2, OUT
Generated		ST	2, IN
Generated		L	2, SAVE

If a symbolic parameter appears in the comments field of a model statement, it is not replaced by the corresponding characters of the macro-instruction.

Concatenating Symbolic Parameters with Other Characters or Other Symbolic Parameters

If a symbolic parameter in a model statement is immediately preceded or followed by other characters or another symbolic parameter, the characters that correspond to the symbolic parameter are combined, in the generated statement, with the other characters or the characters that correspond to the other symbolic parameter. This process is called concatenation.

The macro-definition, macro-instruction, and generated statements in the following example illustrate these rules.

	Name	Operation	Operand
Header		MACRO	
Prototype	&NAME	MOVE	&TY, &P, &TO, &FROM
Model	&NAME	ST&TY	2, SAVEAREA
Model		L&TY	2, &P&FROM
Model		ST&TY	2, &P&TO
Model		L&TY	2, SAVEAREA
Trailer		MEND	
Macro	HERE	MOVE	D, FIELD, A, B
Generated	HERE	STD	2, SAVEAREA
Generated		LD	2, FIELD B
Generated		STD	2, FIELD A
Generated		LD	2, SAVEAREA

The symbolic parameter &TY is used in each of the four model statements to vary the mnemonic operation code of each of the generated statements. The character D in the macro-instruction corresponds to symbolic parameter &TY. Since &TY is preceded by other characters (i.e., ST and L) in the model statements, the character that corresponds to &TY (i.e., D) is concatenated with the other characters to form the operation fields of the generated statements.

The symbolic parameters &P, &TO, and &FROM are used in two of the model statements to vary part of the operand fields of the corresponding generated statements. The characters FIELD, A, and B correspond to the symbolic parameters &P, &TO, and &FROM, respectively. Since &P is followed by &FROM in the second model statement, the characters that correspond to them (i.e., FIELD and B) are concatenated to form part of the operand field of the second generated statement. Similarly, FIELD and A are concatenated to form part of the operand field of the third generated statement.

If the programmer wishes to concatenate a symbolic parameter with a letter, digit, left parenthesis, or period following the symbolic parameter he must immediately follow the symbolic parameter with a period. A period is optional if the symbolic parameter is to be concatenated with another symbolic parameter, or a special character other than a left parenthesis or another period that follows it.

If a symbolic parameter is immediately followed by a period, then the symbolic parameter and the period are replaced by the characters that correspond to the symbolic parameter. A period that immediately follows a symbolic parameter does not appear in the generated statement.

The following macro-definition, macro-instruction, and generated statements illustrate these rules.

	Name	Operation	Operand
Header		MACRO	
Prototype	&NAME	MOVE	&P, &S, &R1, &R2
Model	&NAME	ST	&R1, &S. (&R2)
Model		L	&R1, &P.B
Model		ST	&R1, &P.A
Model		L	&R1, &S. (&R2)
Trailer		MEND	
Macro	HERE	MOVE	FIELD, SAVE, 2, 4
Generated	HERE	ST	2, SAVE (4)
Generated		L	2, FIELD B
Generated		ST	2, FIELD A
Generated		L	2, SAVE (4)

The symbolic parameter &P is used in the second and third model statements to vary part of the operand field of each of the corresponding generated statements. The characters FIELD of the macro-instruction correspond to &P. Since &P is to be concatenated with a letter (i.e., B and A) in each of the statements, a period immediately follows &P in each of the model statements. The period does not appear in the generated statements.

Similarly, symbolic parameter &S is used in the first and fourth model statements to vary the operand fields of the corresponding generated statements. &S is followed by a period in each of the model statements, because it is to be concatenated with a left parenthesis. The period does not appear in the generated statements.

Comments Statements

A model statement may be a comments statement. A comments statement consists of an asterisk in the begin column, followed by comments. The comments statement is used by the assembler to generate an assembler language comments statement, just as other model statements are used by the assembler to generate assembler language statements.

The programmer may also write comments statements in a macro-definition which are not to be generated. These statements must have a period in the begin column, immediately followed by an asterisk and the comments.

The first statement in the following example will be used by the assembler to generate a comments statement; the second statement will not.

Name	Operation	Operand
* THIS STATEMENT WILL BE GENERATED		
.* THIS ONE WILL NOT BE GENERATED		

COPY STATEMENTS

COPY statements may be used to copy model statements and MEXIT, MNOTE, and conditional assembly instructions into a macro-definition, just as they may be used outside macro-definitions to copy source statements into an assembler language program.

The format of this statement is:

Name	Operation	Operand
Blank	COPY	A symbol

The symbol in the operand field identifies the section of coding to be copied. Any statement that may be used in a macro-definition may be part of the copied coding, except MACRO, MEND, COPY, and prototype statements.

A COPY statement is not a model statement, since it is not used by the assembler to generate a COPY statement.

The format of a macro-instruction is:

Name	Operation	Operand
A symbol or blank	Mnemonic operation code	Zero or more operands, separated by commas.

The name field of the macro-instruction may contain a symbol. The symbol will not be defined unless a symbolic parameter appears in the name field of the prototype and the same parameter appears in the name field of a generated model statement.

The operation field contains the mnemonic operation code of the macro-instruction. The mnemonic operation code must be the same as the mnemonic operation code of a macro-definition in the source program or in the macro library.

The macro-definition with the same mnemonic operation code is used by the assembler to process the macro-instruction. If a macro-definition in the source program and one in the macro library have the same mnemonic operation code, the macro-definition in the source program is used.

The placement and order of the operands in the macro-instruction is determined by the placement and order of the symbolic parameters in the operand field of the prototype statement.

MACRO-INSTRUCTION OPERANDS

Any combination of up to 255 characters may be used as a macro-instruction operand provided that the following rules concerning quotation marks, parentheses, equal signs, ampersands, commas, and blanks are observed.

Paired Quotation Marks: An operand may contain one or more quoted strings. A quoted string is any sequence of characters that begins and ends with a quotation mark and contains an even number of quotation marks.

The first quoted string starts with the first quotation mark in the operand. Subsequent quoted strings start with the first

quotation mark after the quotation mark that ends the previous quoted string.

A quoted string ends with the first even-numbered quotation mark that is not immediately followed by a quotation mark.

The first and last quotation marks of a quoted string are called paired quotation marks. The following example contains two quoted strings. The first and fourth and the fifth and sixth quotation marks are each paired quotation marks.

```
'A''B'C'D'
```

A quotation mark immediately preceded by the letter L, and immediately followed by a letter is not considered in determining paired quotation marks. For instance, in the following example the middle quotation mark is not considered.

```
'L'SYMBOL'
```

Paired Parentheses: There must be an equal number of left and right parentheses. The nth left parenthesis must appear to the left of the nth right parenthesis.

Paired parentheses are a left parenthesis and a following right parenthesis without any other parentheses intervening. If there is more than one pair, each additional pair is determined by removing any pairs already recognized and reapplying the above rule for paired parentheses. For instance, in the following example the first and fourth, the second and third, and the fifth and sixth parentheses are each paired parentheses.

```
(A (B) C) D (E)
```

A parenthesis that appears between paired quotation marks is not considered in determining paired parentheses. For instance, in the following example the middle parenthesis is not considered.

```
(')')
```

Equal Signs: An equal sign can only occur as the first character in an operand or between paired quotation marks or paired parentheses. The following examples illustrate these rules.

```
=F'32'  
'C=D'  
E (F=G)
```

Ampersands: Except as noted under "Inner Macro-Instructions," each sequence of consecutive ampersands must be an even number of ampersands. The following example illustrates this rule.

  123    

Commas: A comma indicates the end of an operand, unless it is placed between paired quotation marks or paired parentheses. The following example illustrates this rule.

(A,B) C', '

Blanks: Except as noted under "Statement Format," a blank indicates the end of the operand field, unless it is placed between paired quotation marks. The following example illustrates this rule.

'A B C'

The following are valid macro-instruction operands:

```
SYMBOL      A+2
123          (TO (8) ,FROM)
X'189A'     0 (2,3)
*           =F'4096'
L'NAME      AB  9
'TEN = 10'  'PARENTHESIS IS )'
'QUOTE IS'' 'COMMA IS ,'
```

The following are invalid macro-instruction operands:

```
W'NAME      (odd number of quotation
              marks)
5A) B       (number of left parentheses
              does not equal number of
              right parentheses)
(15 B)      (blank not placed between
              paired quotation marks)
'ONE' IS '1' (blank not placed between
              paired quotation marks)
```

STATEMENT FORMAT

Macro-instructions may be written using the same alternate format that can be used to write prototype statements. If this format is used, a blank does not always indicate the end of the operand field. The alternate format is described in Section 7, under the subsection "Macro-Instruction Prototype."

OMITTED OPERANDS

If an operand that appears in the prototype statement is omitted from the macro-instruction, then the comma that would have separated it from the next operand must be present. If the last operand(s) is omitted from a macro-instruction, then the comma(s) separating the last operand(s) from the next previous operand may be omitted.

The following example shows a macro-instruction preceded by its corresponding prototype statement. The macro-instruction operands that correspond to the third and sixth operands of the prototype statement are omitted in this example.

Name	Operation	Operand
	EXAMPLE	�A, �B, �C, �D, �E, �F
	EXAMPLE	17, *+4, , AREA, FIELD (6)

If the symbolic parameter that corresponds to an omitted operand is used in a model statement, a null character value replaces the symbolic parameter in the generated statement, i.e., in effect the symbolic parameter is removed.

For example, the first statement below is a model statement that contains the symbolic parameter  C. If the operand that corresponds to  C was omitted from the macro-instruction, the second statement below would be generated from the model statement.

Name	Operation	Operand
	MVC	THERE�C.25, THIS
	MVC	THERE25, THIS

OPERAND SUBLISTS

An operand of a macro-instruction may be a sublist.

SUBLISTS provide the programmer with a convenient way to refer to: (1) a collection of macro-instruction operands as a single operand, or (2) a single operand in a collection of operands.

A sublist consists of one or more operands separated by commas and enclosed in paired parentheses. The entire sublist,

including the parentheses, is considered to be one macro-instruction operand.

If a macro-instruction is written in the alternate statement format, each sublist operand may be written on a separate line; the macro-instruction may be written on as many lines as there are operands, including sublist operands.

If &P1 is a symbolic parameter in a prototype statement, and the corresponding operand of a macro-instruction is a sublist, then &P1(n) may be used in a model statement to refer to the nth operand of the sublist, where n may be a decimal integer. (n may also be any arithmetic expression allowed in a SETA instruction. The SETA instruction is described in Section 9.)

For example, consider the following macro-definition, macro-instruction, and generated statements.

	Name	Operation	Operand
Header		MACRO	
Prototype		ADD	&NUM, ®, &AREA
Model		L	®, &NUM (1)
Model		A	®, &NUM (2)
Model		A	®, &NUM (3)
Model		ST	®, &AREA
Trailer		MEND	
Macro		ADD	(A,B,C) , 6, SUM
Generated		L	6, A
Generated		A	6, B
Generated		A	6, C
Generated		ST	6, SUM

The operand of the macro-instruction that corresponds to symbolic parameter &NUM is a sublist. One of the operands in the sublist is referred to in the operand field of three of the model statements. For example, &NUM(1) refers to the first operand in the sublist corresponding to symbolic parameter &NUM. The first operand of the sublist is A. Therefore, A replaces &NUM(1) to form part of the generated statement.

Note: When referring to an operand in a sublist, the left parenthesis of the sublist notation must immediately follow the last character of the symbolic parameter, e.g., &NUM(1). A period should not be placed between the left parenthesis and the last character of the symbolic parameter.

A period may be used between these two characters only when the programmer wants to concatenate the left parenthesis with the characters that the symbolic parameter represents. The following example shows what would be generated if a period appeared between the left parenthesis and the last character of the symbolic parameter in the first model statement of the above example.

	Name	Operation	Operand
Prototype		ADD	&NUM, ®, &AREA
Model		L	®, &NUM. (1)
Macro		ADD	(A,B,C) , 6, SUM
Generated		L	6, (A,B,C) (1)

The symbolic parameter &NUM is used in the operand field of the model statement. The characters (A,B,C) of the macro-instruction correspond to &NUM. Since &NUM is immediately followed by a period, &NUM and the period are replaced by (A,B,C). The period does not appear in the generated statement. The resulting generated statement is an invalid assembler language statement.

INNER MACRO-INSTRUCTIONS

A macro-instruction may be used as a model statement in a macro-definition. Macro-instructions used as model statements are called inner macro-instructions.

A macro-instruction that is not used as a model statement is referred to as an outer macro-instruction.

Any symbolic parameters used in an inner macro-instruction are replaced by the corresponding characters of the outer macro-instruction.

The macro-definition corresponding to an inner macro-instruction is used to generate the statements that replace the inner macro-instruction.

The ADD macro-instruction of the previous example is used as an inner macro-instruction in the following example.

The inner macro-instruction contains two symbolic parameters, &S and &T. The characters (X,Y,Z) and J of the macro-instruction correspond to &S and &T,

respectively. Therefore, these characters replace the symbolic parameters in the operand field of the inner macro-instruction.

The assembler then uses the macro-definition that corresponds to the inner macro-instruction to generate statements to replace the inner macro-instruction. The fourth through seventh generated statements have been generated for the inner macro-instruction.

Note: An ampersand that is part of a symbolic parameter is not considered in determining whether a macro-instruction operand contains an even number of consecutive ampersands.

LEVELS OF MACRO-INSTRUCTIONS

	Name	Operation	Operand
Header		MACRO	
Prototype		COMP	&R1, &R2, &S, &T, &U
Model		SR	&R1, &R2
Model		C	&R1, &T
Model		BNE	&U
Inner		ADD	&S, 12, &T
Model	&U	A	&R1, &T
Trailer		MEND	
Macro	K	COMP	10, 11, (X, Y, Z), J, K
Generated		SR	10, 11
Generated		C	10, J
Generated		BNE	K
Generated		L	12, X
Generated		A	12, Y
Generated		A	12, Z
Generated		ST	12, J
Generated	K	A	10, J

A macro-definition that corresponds to an outer macro-instruction may contain any number of inner macro-instructions. The outer macro-instruction is called a first level macro-instruction. Each of the inner macro-instructions is called a second level macro-instruction.

The macro-definition that corresponds to a second level macro-instruction may contain any number of inner macro-instructions. These macro-instructions are called third level macro-instructions, etc.

The number of levels of macro-instructions that may be used depends upon the complexity of the macro-definition and the amount of storage available. This is described in detail in Appendix H.

SECTION 9: HOW TO WRITE CONDITIONAL ASSEMBLY INSTRUCTIONS

The conditional assembly instructions allow the programmer to: (1) define and assign values to SET symbols that can be used to vary parts of generated statements, and (2) vary the sequence of generated statements. Thus, the programmer can use these instructions to generate many different sequences of statements from the same macro-definition.

There are 12 conditional assembly instructions, 9 of which are described in this section. The other three conditional assembly instructions -- GBLA, GBLB, and GBLC -- are described in Section 10. The instructions described in this section are:

LCLA	SETA	AIF
LCLB	SETB	AGO
LCLC	SETC	ANOP

All of the conditional assembly instructions may be used anywhere in an assembler language source program. The primary use of these instructions, however, is in macro-definitions.

Where the use of an instruction outside macro-definitions differs from its use within macro-definitions, the difference is described in the subsequent text.

The LCLA, LCLB, and LCLC instructions may be used to define and assign initial values to SET symbols.

The SETA, SETB, and SETC instructions may be used to assign arithmetic, binary, and character values, respectively, to SET symbols. The SETB instruction is described after the SETA and SETC instructions, because the operand field of the SETB instruction is a combination of the operand fields of the SETA and SETC instructions.

The AIF, AGO, and ANOP instructions may be used in conjunction with sequence symbols to vary the sequence in which statements are processed by the assembler. The programmer can test attributes assigned by the assembler to macro-instruction operands to determine which statements are to be processed.

Examples illustrating the use of each conditional assembly instruction are included throughout this section. A chart summarizing the elements that can be used in each instruction appears at the end of this section.

SET SYMBOLS

SET symbols are one type of variable symbol. The symbolic parameters discussed in Section 7 are another type of variable symbol. SET symbols differ from symbolic parameters in three ways: (1) where they can be used in an assembler language source program, (2) how they are assigned values, and (3) whether or not the values assigned to them can be changed.

Symbolic parameters can only be used in macro-definitions, whereas SET symbols can be used inside and outside macro-definitions.

Symbolic parameters are assigned values when the programmer writes a macro-instruction, whereas SET symbols are assigned values when the programmer writes SETA, SETB, and SETC conditional assembly instructions.

Each symbolic parameter is assigned a single value for one use of a macro-definition, whereas the values assigned to each SETA, SETB, and SETC symbol can change during one use of a macro-definition.

Defining SET Symbols

SET symbols must be defined by the programmer before they are used. When a SET symbol is defined it is assigned an initial value. SET symbols may be assigned new values by means of the SETA, SETB, and SETC instructions. A SET symbol is defined when it appears in the operand field of an LCLA, LCLB, or LCLC instruction.

Using Variable Symbols

The SETA, SETB, and SETC instructions may be used to change the values assigned to SETA, SETB, and SETC symbols, respectively. When a SET symbol appears in the name, operation, or operand field of a statement, the current value of the SET symbol (i.e., the last value assigned to it) replaces the SET symbol in the statement.

For example, if %A is a symbolic parameter, and the corresponding characters of

the macro-instruction are the symbol HERE, then HERE replaces each occurrence of &A in the macro-definition. However, if &A is a SET symbol, the value assigned to &A can be changed, and a different value can replace each occurrence of &A in the macro-definition.

The same variable symbol may not be used as a symbolic parameter and as a SET symbol in the same macro-definition.

The following illustrates this rule.

Name	Operation	Operand
&NAME	MOVE	&TO, &FROM

If the statement above is a prototype statement, then &NAME, &TO, and &FROM may not be used as SET symbols in the macro-definition.

The same variable symbol may not be used as two different types of SET symbols in the same macro-definition. Similarly, the same variable symbol may not be used as two different types of SET symbols outside macro-definitions.

For example, if &A is a SETA symbol in a macro-definition, it cannot be used as a SETC symbol in that definition. Similarly, if &A is a SETA symbol outside macro-definitions, it cannot be used as a SETC symbol outside macro-definitions.

The same variable symbol may be used in two or more macro-definitions and outside macro-definitions. If such is the case, the variable symbol will be considered a different variable symbol each time it is used.

For example, if &A is a variable symbol (either SET symbol or symbolic parameter) in one macro-definition, it can be used as a variable symbol (either SET symbol or symbolic parameter) in another definition. Similarly, if &A is a variable symbol (SET symbol or symbolic parameter) in a macro-definition, it can be used as a SET symbol outside macro-definitions.

All variable symbols may be concatenated with other characters in the same way that symbolic parameters may be concatenated with other characters. The rules for concatenating symbolic parameters with other characters are in Section 7 under the subsection "Model Statements."

Variable symbols in macro-instructions are replaced by the values assigned to them, immediately prior to the start of

processing the definition. If a SET symbol is used in the operand field of a macro-instruction, and the value assigned to the SET symbol is equivalent to the sublist notation, the operand is not considered a sublist.

ATTRIBUTES

The assembler assigns attributes to macro-instruction operands and to all literals and all symbols defined in the program.

There are six kinds of attributes. They are: type, length, scaling, integer, count, and number. Each kind of attribute is discussed in the paragraphs that follow.

If an outer macro-instruction operand is a symbol or a literal, then the attributes of the operand are the same as the corresponding attributes of the symbol or literal. The symbol must appear in the name field of an assembler language statement or in the operand field of an EXTRN statement in the program. The statement must be outside macro-definitions and must not contain any variable symbols.

If an inner macro-instruction operand is a symbolic parameter, then the attributes of the operand are the same as the attributes of the corresponding outer macro-instruction operand.

If a macro-instruction operand is a sublist, the programmer may refer to the attributes of either the sublist or each operand in the sublist. The type, length, scaling, and integer attributes of a sublist are the same as the corresponding attributes of the first operand in the sublist.

All the attributes of macro-instruction operands may be referred to in conditional assembly instructions within macro-definitions. However, only the type, length, scaling, and integer attributes of symbols may be referred to in conditional assembly instructions outside macro-definitions. Attributes of symbols appearing in the name field of generated statements may not be referred to in conditional assembly instructions inside or outside macro definitions.

Each attribute has a notation associated with it. The notations are:

<u>Attribute</u>	<u>Notation</u>
Type	T'
Length	L'
Scaling	S'

Integer I'
 Count K'
 Number N'

The programmer may refer to an attribute in the following ways:

1. In a statement that is outside macro-definitions, he may write the notation for the attribute immediately followed by a symbol. (e.g., L'NAME refers to the length attribute of the symbol NAME.)
2. In a statement that is in a macro-definition, he may write the notation for the attribute immediately followed by a symbolic parameter. (e.g., L'&NAME refers to the length attribute of the characters in the macro-instruction that correspond to symbolic parameter &NAME; L'&NAME(2) refers to the length attribute of the second operand in the sublist that corresponds to symbolic parameter &NAME.)

Type Attribute (T')

The type attribute of a macro-instruction operand, a literal, or a symbol is a letter.

The following letters are used for symbols that name DC and DS statements and for outer macro-instruction operands that are either literals or symbols that name DC or DS statements.

A A-type address constant, implied length, aligned.
 B Binary constant.
 C Character constant.
 D Long floating-point constant, implied length, aligned.
 E Short floating-point constant, implied length, aligned.
 F Full-word fixed-point constant, implied length, aligned.
 G Fixed-point constant, explicit length.
 H Half-word fixed-point constant, implied length, aligned.
 K Floating-point constant, explicit length.
 P Packed decimal constant.
 R A-, S-, V-, or Y-type address constant, explicit length.
 S S-type address constant, implied length, aligned.
 V V-type address constant, implied length, aligned.
 X Hexadecimal constant.
 Y Y-type address constant, implied length, aligned.
 Z Zoned decimal constant.

The following letters are used for symbols (and outer macro-instruction operands that are symbols) that name statements other than DC or DS statements, or that appear in the operand field of an EXTRN statement.

I Machine instruction
 J Control section name
 M Macro-instruction
 T External symbol
 W CCW assembler instruction

The following letters are used for inner and outer macro-instruction operands only.

N Self-defining term
 O Omitted operand

The following letter is used for symbols whose attributes are not available, and for inner and outer macro-instruction operands that cannot be assigned any of the above letters. This includes inner macro-instruction operands that are symbols or literals. This letter is also assigned to symbols that name EQU statements.

U Undefined

The programmer may refer to a type attribute in the operand field of a SETC instruction, or in character relations in the operand fields of SETB or AIF instructions.

Length (L'), Scaling (S'), and Integer (I') Attributes

The length, scaling, and integer attributes of macro-instruction operands, literals, and symbols are numeric values.

The length attribute of a symbol (or of a macro-instruction operand that is a symbol) is as described in Part I of this publication.

Conditional assembly instructions must not refer to the length attributes of symbols or macro-instruction operands whose type attributes are the letters M, N, O, T, or U.

Scaling and integer attributes are provided for fixed-point, floating-point, and decimal literals and for symbols that name fixed-point, floating-point, and decimal fields.

Fixed Point: The scaling attribute of a fixed-point number is the number of bits occupied by the fractional portion of the fixed-point number. The integer attribute of a fixed-point number is the number of

bits occupied by the integral portion of the fixed-point number.

Floating Point: The scaling attribute of a floating-point number is the number of hexadecimal zeros in the leftmost portion of the fraction. The integer attribute of a floating-point number is the number of significant hexadecimal digits in the fraction.

Decimal: The scaling attribute of a decimal number is the number of decimal digits to the right of the decimal point. The integer attribute of a decimal number is the number of decimal digits to the left of the decimal point.

Scaling and integer attributes are available for symbols and macro-instruction operands only if their type attributes are H, F, and G (fixed point); D, E, and K (floating point); or P and Z (decimal).

The programmer may refer to the length, scaling, and integer attributes in the operand field of a SETA instruction, or in arithmetic relations in the operand fields of SETB or AIF instructions.

Count Attribute (K')

The programmer may refer to the count attribute of macro-instruction operands only.

The count attribute is a value equal to the number of characters in the macro-instruction operand, excluding commas. If the operand is a sublist, the count attribute includes the beginning and ending parentheses and the commas within the sublist. The count attribute of an omitted operand is zero.

If a macro-instruction operand contains variable symbols, the characters that replace the variable symbols, rather than the variable symbols, are used to determine the count attribute.

The programmer may refer to the count attribute in the operand field of a SETA instruction, or in arithmetic relations in the operand fields of SETB and AIF instructions that are part of a macro-definition.

Number Attribute (N')

The programmer may refer to the number attribute of macro-instruction operands only.

The number attribute is a value equal to the number of operands in an operand sublist. The number of operands in an operand sublist is equal to one plus the number of commas that indicate the end of an operand in the sublist.

The following examples illustrates this rule.

(A,B,C,D,E)	5 operands
(A,,C,D,E)	5 operands
(A,B,C,D)	4 operands
(,B,C,D,E)	5 operands
(A,B,C,D,)	5 operands
(A,B,C,D,,)	6 operands

If the macro-instruction operand is not a sublist, the number attribute is one. If the macro-instruction operand is omitted, the number attribute is zero.

The programmer may refer to the number attribute in the operand field of a SETA instruction, or in arithmetic relations in the operand fields of SETB and AIF instructions that are part of a macro-definition.

Assigning Attributes to Symbols

The integer attribute is computed from the length and scaling attributes.

Fixed Point: The integer attribute of a fixed-point number is equal to eight times the length attribute of the number minus the scaling attribute minus one; i.e., $I' = 8 * L' - S' - 1$.

Each of the following statements defines a fixed-point field. The length attribute of HALFCON is 2, the scaling attribute is 6, and the integer attribute is 9. The length attribute of ONECON is 4, the scaling attribute is 8, and the integer attribute is 23.

Name	Operation	Operand
HALFCON	DC	HS6'-25.93'
ONECON	DC	FS8'100.3E-2'

Floating Point: The integer attribute of a floating-point number is equal to two times the difference between the length attribute of the number and one, minus the scaling attribute; i.e., $I' = 2 * (L' - 1) - S'$.

Each of the following statements defines a floating-point field. The length attri-

bute of SHORT is 4, the scaling attribute is 2, and the integer attribute is 4. The length attribute of LONG is 8, the scaling attribute is 5, and the integer attribute is 9.

Name	Operation	Operand
SHORT	DC	ES2'46.415'
LONG	DC	DS5'-3.729'

Decimal: The integer attribute of a packed decimal number is equal to two times the length attribute of the number minus the scaling attribute minus one; i.e., $I' = 2 * L' - S' - 1$. The integer attribute of a zoned decimal number is equal to the difference between the length attribute and the scaling attribute; i.e., $I' = L' - S'$.

Each of the following statements defines a decimal field. The length attribute of FIRST is 2, the scaling attribute is 2, and the integer attribute is 1. The length attribute of SECOND is 3, the scaling attribute is 0, and the integer attribute is 3. The length attribute of THIRD is 4, the scaling attribute is 2, and the integer attribute is 2. The length attribute of FOURTH is 3, the scaling attribute is 2, and the integer attribute is 3.

Name	Operation	Operand
FIRST	DC	P'+1.25'
SECOND	DC	Z'-543'
THIRD	DC	Z'79.68'
FOURTH	DC	P'79.68'

SEQUENCE SYMBOLS

The name field of a statement may contain a sequence symbol. Sequence symbols provide the programmer with the ability to vary the sequence in which statements are processed by the assembler.

A sequence symbol is used in the operand field of an AIF or AGO statement to refer to the statement named by the sequence symbol.

A sequence symbol may be used in the name field of any statement that does not contain a symbol or SET symbol, except a prototype statement, or a MACRO, LCLA, LCLB, LCLC, GBLA, GBLB, or GBLC instruction.

A sequence symbol consists of a period followed by one through seven letters and/or digits, the first of which must be a letter.

The following are valid sequence symbols:

.READER	.A23456
.LOOP2	.X4F2
.N	.S4

The following are invalid sequence symbols:

CARDAREA	(first character is not a period)
.246B	(first character after period is not a letter)
.AREA2456	(more than seven characters after period)
.BCD%84	(contains a special character other than initial period)
.IN AREA	(contains a special character, i.e., blank, other than initial period)

If a sequence symbol appears in the name field of a macro-instruction, and the corresponding prototype statement contains a symbolic parameter in the name field, the sequence symbol does not replace the symbolic parameter wherever it is used in the macro-definition.

The following example illustrates this rule.

	Name	Operation	Operand
		MACRO	
1	&NAME	MOVE	&TO, &FROM
2	&NAME	ST	2, &SAVEAREA
		L	2, &FROM
		ST	2, &TO
		L	2, &SAVEAREA
		MEND	
3	.SYM	MOVE	FIELD A, FIELD B
4		ST	2, &SAVEAREA
		L	2, FIELD B
		ST	2, FIELD A
		L	2, &SAVEAREA

The symbolic parameter &NAME is used in the name field of the prototype statement (statement 1) and the first model statement (statement 2). In the macro-instruction (statement 3) a sequence symbol (.SYM) corresponds to the symbolic parameter &NAME. &NAME is not replaced by .SYM, and, therefore, the generated statement

(statement 4) does not contain an entry in the name field.

The expression in the operand field is evaluated as a signed 32-bit arithmetic value which is assigned to the SETA symbol in the name field. The minimum and maximum allowable values of the expression are -2^{31} and $+2^{31}-1$, respectively.

LCLA, LCLB, LCLC -- DEFINE SET SYMBOLS

The format of these instructions is:

Name	Operation	Operand
Blank	LCLA, LCLB, or LCLC	One or more variable symbols, that are to be used as SET symbols, separated by commas

The LCLA, LCLB, and LCLC instructions are used to define and assign initial values to SETA, SETB, and SETC symbols, respectively. The SETA, SETB, and SETC symbols are assigned the initial values of 0, 0, and null character value, respectively.

The programmer should not define any SET symbol whose first four characters are &SYS.

All LCLA, LCLB, or LCLC instructions in a macro-definition must appear immediately after the prototype statement, and all GBLA, GBLB or GBLC instructions, or another LCLA, LCLB, or LCLC instruction. All LCLA, LCLB, or LCLC instructions outside macro-definitions must appear after all macro-definitions in the source program, after all GBLA, GBLB, and GBLC instructions outside macro-definitions, before all conditional assembly instructions, and PUNCH and REPRO statements outside macro-definitions, and before the first control section of the program.

SETA -- SET ARITHMETIC

The SETA instruction may be used to assign an arithmetic value to a SETA symbol. The format of this instruction is:

Name	Operation	Operand
A SETA symbol	SETA	An arithmetic expression

The expression may consist of one term or an arithmetic combination of terms. The terms that may be used alone or in combination with each other are self-defining terms, variable symbols, and the length, scaling, integer, count, and number attributes. Self-defining terms are described in Part I of this publication.

Note: A SETC variable symbol may appear in a SETA expression only if the value of the SETC variable is one to eight decimal digits. The decimal digits will be converted to a positive arithmetic value.

The arithmetic operators that may be used to combine the terms of an expression are + (addition), - (subtraction), * (multiplication), and / (division).

An expression may not contain two terms or two operators in succession, nor may it begin with an operator.

The following are valid operand fields of SETA instructions:

```
&AREA+X'2D'   I'%N/25
&BETA*10      &EXIT-S'%ENTRY+1
L'%HERE+32    29
```

The following are invalid operand fields of SETA instructions:

```
&AREAX'C'      (two terms in succession)
&FIELD+-      (two operators in succession)
-&DELTA*2      (begins with an operator)
**+32          (begins with an operator;
                two operators in succession)
NAME/15        (NAME is not a valid term)
```

Evaluation of Arithmetic Expressions

The procedure used to evaluate the arithmetic expression in the operand field of a SETA instruction is the same as that used to evaluate arithmetic expressions in assembler language statements. The only

difference between the two types of arithmetic expressions is the terms that are allowed in each expression.

The following evaluation procedure is used:

1. Each term is given its numerical value.
2. The arithmetic operations are performed moving from left to right. However, multiplication and/or division are performed before addition and subtraction.
3. The computed result is the value assigned to the SETA symbol in the name field.

The arithmetic expression in the operand field of a SETA instruction may contain one or more sequences of arithmetically combined terms that are enclosed in parentheses. A sequence of parenthesized terms may appear within another parenthesized sequence.

The following are examples of SETA instruction operand fields that contain parenthesized sequences of terms.

```
(L'&HERE+32) *29
&AREA+X'2D' / (&EXIT-S'&ENTRY+1)
&BETA*10*(I'&N/25/(&EXIT-S'&ENTRY+1))
```

The parenthesized portion or portions of an arithmetic expression are evaluated before the rest of the terms in the expression are evaluated. If a sequence of parenthesized terms appears within another parenthesized sequence, the innermost sequence is evaluated first.

Using SETA Symbols

The arithmetic value assigned to a SETA symbol is substituted for the SETA symbol when it is used in the operand field of a SETA instruction, or in arithmetic relations in the operand fields of SETB and AIF instructions. If the SETA symbol is used in any other statement, the arithmetic value is converted to an unsigned integer, with leading zeros removed. If the value is zero, it is converted to a single zero.

The following example illustrates this rule:

Name	Operation	Operand
	MACRO	
&NAME	MOVE	&TO, &FROM
	LCLA	&A, &B, &C, &D
1 &A	SETA	10
2 &B	SETA	12
3 &C	SETA	&A-&B
4 &D	SETA	&A+&C
&NAME	ST	2, SAVEAREA
5	L	2, &FROM&C
6	ST	2, &TO&D
	L	2, SAVEAREA
	MEND	
HERE	MOVE	FIELDA, FIELDB
HERE	ST	2, SAVEAREA
	L	2, FIELDB2
	ST	2, FIELDA8
	L	2, SAVEAREA

Statements 1 and 2 assign to the SETA symbols &A and &B the arithmetic values +10 and +12, respectively. Therefore, statement 3 assigns the SETA symbol &C the arithmetic value -2. When &C is used in statement 5, the arithmetic value -2 is converted to the unsigned integer 2. When &C is used in statement 4, however, the arithmetic value -2 is used. Therefore, &D is assigned the arithmetic value +8. When &D is used in statement 6, the arithmetic value +8 is converted to the unsigned integer 8.

The following example shows how the value assigned to a SETA symbol may be changed in a macro-definition.

Name	Operation	Operand
	MACRO	
&NAME	MOVE	&TO&FROM
	LCLA	&A
1 &A	SETA	5
&NAME	ST	2, SAVEAREA
2	L	2, &FROM&A
3 &A	SETA	8
4	ST	2, &TO&A
	L	2, SAVEAREA
	MEND	
HERE	MOVE	FIELDA, FIELDB
HERE	ST	2, SAVEAREA
	L	2, FIELDB5
	ST	2, FIELDA8
	L	2, SAVEAREA

Statement 1 assigns the arithmetic value +5 to SETA symbol &A. In statement 2, &A is converted to the unsigned integer 5. Statement 3 assigns the arithmetic value +8 to &A. In statement 4, therefore, &A is converted to the unsigned integer 8, instead of 5.

A SETA symbol may be used with a symbolic parameter to refer to an operand in an operand sublist. If a SETA symbol is used for this purpose it must have been assigned a positive value.

Any expression that may be used in the operand field of a SETA instruction may be used to refer to an operand in an operand sublist.

Sublists are described in Section 8 under "Operand Sublists."

The following macro-definition may be used to add the last operand in an operand sublist to the first operand in an operand sublist and store the result at the first operand. A sample macro-instruction and generated statements follow the macro-definition.

Name	Operation	Operand
	MACRO	
1	ADDX	&NUMBER, ®
	LCLA	&LAST
2	SETA	N' &NUMBER
	L	®, &NUMBER (1)
3	A	®, &NUMBER (&LAST)
	ST	®, &NUMBER (1)
	MEND	
4	ADDX	(A, B, C, D, E), 3
	L	3, A
	A	3, E
	ST	3, A

&NUMBER is the first symbolic parameter in the operand field of the prototype statement (statement 1). The corresponding characters, (A, B, C, D, E), of the macro-instruction (statement 4) are a sublist. Statement 2 assigns to &LAST the arithmetic value +5, which is equal to the number of operands in the sublist. Therefore, in statement 3, &NUMBER (&LAST) is replaced by the fifth operand of the sublist.

SETC -- SET CHARACTER

The SETC instruction is used to assign a character value to a SETC symbol. The format of this instruction is:

Name	Operation	Operand
A SETC symbol	SETC	One operand, of the form described below

The operand field may consist of the type attribute, a character expression, a substring notation, or a concatenation of substring notations and character expressions. A SETA symbol may appear in the operand of a SETC statement. The result is the character representation of the decimal value, unsigned, with leading zeros removed. If the value is zero, one decimal zero is used.

Type Attribute

The character value assigned to a SETC symbol may be a type attribute. If the type attribute is used, it must appear alone in the operand field. The following example assigns to the SETC symbol &TYPE the letter that is the type attribute of the macro-instruction operand that corresponds to the symbolic parameter &ABC.

Name	Operation	Operand
&TYPE	SETC	T' &ABC

Character Expression

A character expression consists of any combination of characters enclosed in quotation marks.

The character value enclosed in quotation marks in the operand field is assigned to the SETC symbol in the name field. The maximum size character value that can be assigned to a SETC symbol is eight characters.

Evaluation of Character Expressions: The following statement assigns the character value AB%4 to the SETC symbol &ALPHA:

Name	Operation	Operand
&ALPHA	SETC	'AB%4'

More than one character expression may be concatenated into a single character expression by placing a period between the terminating quotation mark of one character expression and the opening quotation mark of the next character expression. For example, either of the following statements may be used to assign the character value ABCDEF to the SETC symbol &BETA.

Name	Operation	Operand
&BETA	SETC	'ABCDEF'
&BETA	SETC	'ABC'. 'DEF'

Two quotation marks must be used to represent a quotation mark that is part of a character expression.

The following statement assigns the character value L'SYMBOL to the SETC symbol &LENGTH.

Name	Operation	Operand
&LENGTH	SETC	'L''SYMBOL'

Variable symbols may be concatenated with other characters in the operand field of a SETC instruction according to the general rules for concatenating variable symbols with other characters (see Section 7).

If &ALPHA has been assigned the character value AB%4, the following statement may be used to assign the character value AB%4RST to the variable symbol &GAMMA.

Name	Operation	Operand
&GAMMA	SETC	'&ALPHA.RST'

Two ampersands must be used to represent an ampersand that is not part of a variable symbol. Both ampersands become part of the character value assigned to the SETC symbol. They are not replaced by a single ampersand.

The following statement assigns the character value HALF&& to the SETC symbol &AND.

Name	Operation	Operand
&AND	SETC	'HALF&&'

Substring Notation

The character value assigned to a SETC symbol may be a substring character value. Substring character values permit the programmer to assign part of a character value to a SETC symbol.

If the programmer wants to assign part of a character value to a SETC symbol, he must indicate to the assembler in the operand field of a SETC instruction: (1) the character value itself, and (2) the part of the character value he wants to assign to the SETC symbol. The combination of (1) and (2) in the operand field of a SETC instruction is called a substring notation. The character value that is assigned to the SETC symbol in the name field is called a substring character value.

Substring notation consists of a character expression, immediately followed by two arithmetic expressions that are separated from each other by a comma and are enclosed in parentheses. The two arithmetic expressions may be any expression that is allowed in the operand field of a SETA instruction.

The first expression indicates the first character in the character expression that is to be assigned to the SETC symbol in the name field. The second expression indicates the number of consecutive characters in the character expression (starting with the character indicated by the first expression) that are to be assigned to the SETC symbol.

The maximum size substring character value that can be assigned to a SETC symbol is eight characters. The maximum size character expression the substring character value can be chosen from is 255 characters.

The following are valid substring notations:

```
'&ALPHA' (2,5)
'AB%4' (&AREA+2,1)
'&ALPHA.RST' (6,&A)
'ABC&GAMMA' (&A,&AREA+2)
```

The following are invalid substring notations:

- '&BETA' (4,6)
(blanks between character value and arithmetic expressions)
- 'L'SYMBOL'(142-&XYZ)
(only one arithmetic expression)
- 'AB%4&ALPHA'(8 &FIELD*2)
(arithmetic expressions not separated by a comma)
- 'BETA'4,6
(arithmetic expressions not enclosed in parentheses)

Name	Operation	Operand
	MACRO	
&NAME	MOVE	&TO, &FROM
	LCLC	&PREFIX
1 &PREFIX	SETC	'FIELD'
&NAME	ST	2, SAVEAREA
2	L	2, &PREFIX&FROM
3 &PREFIX	SETC	'AREA'
4	ST	2, &PREFIX&TO
	L	2, SAVEAREA
	MEND	
HERE	MOVE	A, B
HERE	ST	2, SAVEAREA
	L	2, FIELD B
	ST	2, AREA A
	L	2, SAVEAREA

Using SETC Symbols

The character value assigned to a SETC symbol is substituted for the SETC symbol when it is used in the name, operation, or operand field of a statement.

For example, consider the following macro-definition, macro-instruction, and generated statements.

Statement 1 assigns the character value FIELD to the SETC symbol &PREFIX. Therefore, &PREFIX is replaced by FIELD in statement 2. Statement 3 assigns the character value AREA to &PREFIX. Therefore, &PREFIX is replaced by AREA, instead of FIELD, in statement 4.

The following example illustrates the use of a substring notation as the operand field of a SETC instruction.

Name	Operation	Operand
	MACRO	
&NAME	MOVE	&TO, &FROM
	LCLC	&PREFIX
1 &PREFIX	SETC	'FIELD'
&NAME	ST	2, SAVEAREA
2	L	2, &PREFIX&FROM
3	ST	2, &PREFIX&TO
	L	2, SAVEAREA
	MEND	
HERE	MOVE	A, B
HERE	ST	2, SAVEAREA
	L	2, FIELD B
	ST	2, FIELD A
	L	2, SAVEAREA

Statement 1 assigns the character value FIELD to the SETC symbol &PREFIX. In statements 2 and 3, &PREFIX is replaced by FIELD.

The following example shows how the value assigned to a SETC symbol may be changed in a macro-definition.

Name	Operation	Operand
	MACRO	
&NAME	MOVE	&TO, &FROM
	LCLC	&PREFIX
1 &PREFIX	SETC	'&TO' (1, 5)
&NAME	ST	2, SAVEAREA
2	L	2, &PREFIX&FROM
	ST	2, &TO
	L	2, SAVEAREA
	MEND	
HERE	MOVE	FIELD A, B
HERE	ST	2, SAVEAREA
	L	2, FIELD B
	ST	2, FIELD A
	L	2, SAVEAREA

Statement 1 assigns the substring character value FIELD (the first five characters corresponding to symbolic parameter &TO) to the SETC symbol &PREFIX. Therefore, FIELD replaces &PREFIX in statement 2.

Concatenating Substring Notations and Character Expressions: Substring notations may be concatenated with character expressions in the operand field of a SETC

instruction. If a substring notation follows a character expression, the two may be concatenated by placing a period between the terminating quotation mark of the character expression and the opening quotation mark of the substring notation.

For example, if &ALPHA has been assigned the character value AB%4, and &BETA has been assigned the character value ABCDEF, then the following statement assigns &GAMMA the character value AB%4BCD.

Name	Operation	Operand
&GAMMA	SETC	'&ALPHA'.'&BETA'(2,3)

If a substring notation precedes a character expression or another substring notation, the two may be concatenated by writing the opening quotation mark of the second item immediately after the closing parenthesis of the substring notation.

The programmer may optionally place a period between the closing parenthesis of a substring notation and the opening quotation mark of the next item in the operand field.

If &ALPHA has been assigned the character value AB%4, and &ABC has been assigned the character value 5RS, either of the following statements may be used to assign &WORD the character value AB%45RS.

Name	Operation	Operand
&WORD	SETC	'&ALPHA'(1,4) '&ABC'
&WORD	SETC	'&ALPHA'(1,4) '&ABC'(1,3)

If a SETC symbol is used in the operand field of a SETA instruction, the character value assigned to the SETC symbol must be a self-defining term.

If a SETA symbol is used in the operand field of a SETC statement, the arithmetic value is converted to an unsigned integer with leading zeros removed. If the value is zero, it is converted to a single zero.

SETB -- SET BINARY

The SETB instruction may be used to assign the binary value 0 or 1 to a SETB symbol. The format of this instruction is:

Name	Operation	Operand
A SETB symbol	SETB	A 0 or a 1, or a logical expression enclosed in parentheses

The operand field may contain a 0 or a 1 or a logical expression enclosed in parentheses. A logical expression is evaluated to determine if it is true or false; the SETB symbol in the name field is then assigned the binary value 1 or 0 corresponding to true or false, respectively.

A logical expression consists of one term or a logical combination of terms. The terms that may be used alone or in combination with each other are arithmetic relations, character relations, and SETB symbols. The logical operators used to combine the terms of an expression are AND, OR, and NOT.

An expression may not contain two terms in succession. A logical expression may contain two operators in succession only if the first operator is either AND or OR and the second operator is NOT. A logical expression may begin with the operator NOT. It may not begin with the operators AND or OR.

An arithmetic relation consists of two arithmetic expressions connected by a relational operator. A character relation consists of two character values connected by a relational operator. The relational operators are EQ (equal), NE (not equal), LT (less than), GT (greater than), LE (less than or equal), and GE (greater than or equal).

Any expression that may be used in the operand field of a SETA instruction, may be used as an arithmetic expression in the operand field of a SETB instruction. Anything that may be used in the operand field of a SETC instruction, may be used as a character value in the operand field of a SETB instruction. This includes substring and type attribute notations. The maximum size of the character values that can be compared is 255 characters. If the two character values are of unequal size, then the smaller one will always compare less than the larger one.

The relational operators must be immediately preceded and followed by at least one blank. Each relation may or may not be enclosed in parentheses. If a relation is not enclosed in parentheses, it must be separated from the logical operators by at least one blank. A relation enclosed in parentheses need not be separated by any

blanks from the logical operators. However, blanks may be optionally placed between logical operators and relations enclosed in parentheses.

The following are valid operand fields of SETB instructions:

```
1
(&AREA+2 GT 29)
('AB%4' EQ '&ALPHA')
(T'&ABC NE T'&XYZ)
(T'&P12 EQ 'F')
(&AREA+2 GT 29 OR &B)
(NOT &B AND &AREA+X'2D' GT 29)
```

The following are invalid operand fields of SETB instructions:

```
&B (not enclosed in parentheses)
(T'&P12 EQ 'F' &B)
    (two terms in succession)
('AB%4' EQ 'ALPHA' NOT &B)
    (the NOT operator must be
     preceded by AND or OR)
(AND T'&P12 EQ 'F')
    (expression begins with AND)
```

Evaluation of Logical Expressions

The following procedure is used to evaluate a logical expression in the operand field of a SETB instruction:

1. Each term (i.e., arithmetic relation, character relation, or SETB symbol) is evaluated and given its logical value (true or false).
2. The logical operations are performed moving from left to right. However, NOTs are performed before ANDs, and ANDs are performed before ORs.
3. The computed result is the value assigned to the SETB symbol in the name field.

The logical expression in the operand field of a SETB instruction may contain one or more sequences of logically combined terms that are enclosed in parentheses. A sequence of parenthesized terms may appear within another parenthesized sequence.

The following are examples of SETB instruction operand fields that contain parenthesized sequences of terms.

```
(NOT (&B AND &AREA+X'2D' GT 29))
(&B AND (T'&P12 EQ 'F' OR &B))
```

The parenthesized portion or portions of a logical expression are evaluated before the rest of the terms in the expression are

evaluated. If a sequence of parenthesized terms appears within another parenthesized sequence, the innermost sequence is evaluated first.

Using SETB Symbols

The logical value assigned to a SETB symbol is used for the SETB symbol appearing in the operand field of an AIF instruction or another SETB instruction.

If a SETB symbol is used in the operand field of a SETA instruction, or in arithmetic relations in the operand fields of AIF and SETB instructions, the binary values 1 (true) and 0 (false) are converted to the arithmetic values +1 and +0, respectively.

If a SETB symbol is used in the operand field of a SETC instruction, in character relations in the operand fields of AIF and SETB instructions, or in any other statement, the binary values 1 (true) and 0 (false), are converted to the character values 1 and 0, respectively.

The following example illustrates these rules. It is assumed that L'&TO EQ 4 is true, and S'&TO EQ 0 is false.

Name	Operation	Operand
	MACRO	
&NAME	MOVE	&TO, &FROM
	LCLA	&A1
	LCLB	&B1, &B2
	LCLC	&C1
1 &B1	SETB	(L'&TO EQ 4)
2 &B2	SETB	(S'&TO EQ 0)
3 &A1	SETA	&B1
4 &C1	SETC	'&B2'
	ST	2, SAVEAREA
	L	2, &FROM&A1
	ST	2, &TO&C1
	L	2, SAVEAREA
HERE	MOVE	FIELDA, FIELDB
HERE	ST	2, SAVEAREA
	L	2, FIELDB1
	ST	2, FIELD&A0
	L	2, SAVEAREA

Because the operand field of statement 1 is true, &B1 is assigned the binary value 1. Therefore, the arithmetic value +1 is substituted for &B1 in statement 3. Because the operand field of statement 2 is

false, &B2 is assigned the binary value 0. Therefore, the character value 0 is substituted for &B2 in statement 4.

.X4F2 (no logical expression)
 (T'&ABC NE T'&XYZ) .X4F2
 (blanks between logical expression and sequence symbol)

AIF -- CONDITIONAL BRANCH

The AIF instruction is used to conditionally alter the sequence in which source program statements are processed by the assembler. The format of this instruction is:

Name	Operation	Operand
A sequence symbol or blank	AIF	A logical expression enclosed in parentheses, immediately followed by a sequence symbol

Any logical expression that may be used in the operand field of a SETB instruction may be used in the operand field of an AIF instruction. The sequence symbol in the operand field must immediately follow the closing parenthesis of the logical expression.

The logical expression in the operand field is evaluated to determine if it is true or false. If the expression is true, the statement named by the sequence symbol in the operand field is the next statement processed by the assembler. If the expression is false, the next sequential statement is processed by the assembler.

The statement named by the sequence symbol may precede or follow the AIF instruction.

If an AIF instruction is in a macro-definition, then the sequence symbol in the operand field must appear in the name field of a statement in the definition. If an AIF instruction appears outside macro-definitions, then the sequence symbol in the operand field must appear in the name field of a statement outside macro-definitions.

The following are valid operand fields of AIF instructions:

```
(&AREA+X'2D' GT 29) .READER
(T'&P12 EQ 'F') .THERE
```

The following are invalid operand fields of AIF instructions:

```
(T'&ABC NE T'&XYZ) (no sequence symbol)
```

The following macro-definition may be used to generate the statements needed to move a full-word fixed-point number from one storage area to another. The statements will be generated only if the type attribute of both storage areas is the letter F.

Name	Operation	Operand
	MACRO	
&N	MOVE	&T, &F
1	AIF	(T'&T NE T'&F) .END
2	AIF	(T'&T NE 'F') .END
3	ST	2, SAVEAREA
	L	2, &F
	ST	2, &T
	L	2, SAVEAREA
4	.END	MEND

The logical expression in the operand field of statement 1 has the value true if the type attributes of the two macro-instruction operands are not equal. If the type attributes are equal, the expression has the logical value false.

Therefore, if the type attributes are not equal, statement 4 (the statement named by the sequence symbol .END) is the next statement processed by the assembler. If the type attributes are equal, statement 2 (the next sequential statement) is processed.

The logical expression in the operand field of statement 2 has the value true if the type attribute of the first macro-instruction operand is not the letter F. If the type attribute is the letter F, the expression has the logical value false.

Therefore, if the type attribute is not the letter F, statement 4 (the statement named by the sequence symbol .END) is the next statement processed by the assembler. If the type attribute is the letter F, statement 3 (the next sequential statement) is processed.

AGO -- UNCONDITIONAL BRANCH

The AGO instruction is used to unconditionally alter the sequence in which source program statements are processed by

the assembler. The format of this instruction is:

Name	Operation	Operand
A sequence symbol or blank	AGO	A sequence symbol

The statement named by the sequence symbol in the operand field is the next statement processed by the assembler.

The statement named by the sequence symbol may precede or follow the AGO instruction.

If an AGO instruction is part of a macro-definition, then the sequence symbol in the operand field must appear in the name field of a statement that is in that definition. If an AGO instruction appears outside macro-definitions, then the sequence symbol in the operand field must appear in the name field of a statement outside macro-definitions.

The following example illustrates the use of the AGO instruction.

Name	Operation	Operand
	MACRO	
1	&NAME MOVE	&T,&F
2	AIF	(T'&T EQ 'F').FIRST
3	AGO	.END
4	.FIRST AIF	(T'&T NE T'&F).END
	&NAME ST	2,SAVEAREA
	L	2,&F
	ST	2,&T
	L	2,SAVEAREA
4	.END	MEND

Statement 1 is used to determine if the type attribute of the first macro-instruction operand is the letter F. If the type attribute is the letter F, statement 3 is the next statement processed by the assembler. If the type attribute is not the letter F, statement 2 is the next statement processed by the assembler.

Statement 2 is used to indicate to the assembler that the next statement to be processed is statement 4 (the statement named by sequence symbol .END).

ANOP -- ASSEMBLY NO OPERATION

The ANOP instruction facilitates conditional and unconditional branching to statements named by symbols or variable symbols.

The format of this instruction is:

Name	Operation	Operand
A sequence symbol	ANOP	Blank

If the programmer wants to use an AIF or AGO instruction to branch to another statement, he must place a sequence symbol in the name field of the statement to which he wants to branch. However, if the programmer has already entered a symbol or variable symbol in the name field of that statement, he cannot place a sequence symbol in the name field. Instead, the programmer must place an ANOP instruction before the statement and then branch to the ANOP instruction. This has the same effect as branching to the statement immediately after the ANOP instruction.

The following example illustrates the use of the ANOP instruction.

Name	Operation	Operand
	MACRO	
	&NAME MOVE	&T,&F
	LCLC	&TYPE
1	AIF	(T'&T EQ 'F').FTYPE
2	&TYPE SETC	'E'
3	.FTYPE ANOP	
4	&NAME ST&TYPE	2,SAVEAREA
	L&TYPE	2,&F
	ST&TYPE	2,&T
	L&TYPE	2,SAVEAREA
	MEND	

Statement 1 is used to determine if the type attribute of the first macro-instruction operand is the letter F. If the type attribute is not the letter F, statement 2 is the next statement processed by the assembler. If the type attribute is the letter F, statement 4 should be processed next. However, since there is a variable symbol (&NAME) in the name field of statement 4, the required sequence symbol (.FTYPE) cannot be placed in the name field. Therefore, an ANOP instruction (statement 3) must be placed before statement 4.

SECTION 10: EXTENDED FEATURES OF THE MACRO LANGUAGE

The extended features of the macro language allow the programmer to:

1. Terminate processing of a macro-definition.
2. Generate error messages.
3. Define global SET symbols.
4. Define subscripted SET symbols.
5. Use system variable symbols.
6. Prepare keyword and mixed-mode macro-definitions and write keyword and mixed-mode macro-instructions.
7. Use other System/360 macro-definitions.

The following example illustrates the use of the MEXIT instruction.

Name	Operation	Operand
	MACRO	
&NAME	MOVE	&T,&F
1	AIF	(T'&T EQ 'F').OK
2	MEXIT	
3	ANOP	
.OK	ST	2,SAVEAREA
&NAME	L	2,&F
	ST	2,&T
	L	2,SAVEAREA
	MEND	

MEXIT -- MACRO-DEFINITION EXIT

The MEXIT instruction is used to indicate to the assembler that it should terminate processing of a macro-definition. The format of this instruction is:

Name	Operation	Operand
A sequence symbol or blank	MEXIT	Blank

The MEXIT instruction may only be used in a macro-definition.

If the assembler processes an MEXIT instruction that is in a macro-definition corresponding to an outer macro-instruction, the next statement processed by the assembler is the next statement outside macro-definitions.

If the assembler processes an MEXIT instruction that is in a macro-definition corresponding to a second or third level macro-instruction, the next statement processed by the assembler is the next statement after the second or third level macro-instruction instruction in the macro-definition, respectively.

MEXIT should not be confused with MEND. MEND indicates the end of a macro-definition. MEND must be the last statement of every macro-definition, including those that contain one or more MEXIT instructions.

Statement 1 is used to determine if the type attribute of the first macro-instruction operand is the letter F. If the type attribute is the letter F, the assembler processes the remainder of the macro-definition starting with statement 3. If the type attribute is not the letter F, the next statement processed by the assembler is statement 2. Statement 2 indicates to the assembler that it is to terminate processing of the macro-definition.

MNOTE -- REQUEST FOR ERROR MESSAGE

The MNOTE instruction may be used to request the assembler to generate an error message. The format of this instruction is:

Name	Operation	Operand
A sequence symbol or blank	MNOTE	A severity code, followed by a comma, followed by any combination of characters enclosed in quotation marks.

The MNOTE instruction may only be used in a macro-definition.

The severity code may be a decimal integer from 0 through 7. If the severity

code is omitted, 0 is assumed. The severity code indicates the severity of the error. A higher severity code indicates a more serious error.

When an MNOTE instruction is processed by the assembler, the characters enclosed in quotation marks are provided in the source program listing the same way that other error messages are provided in the program listing.

Two quotation marks must be used to represent a quotation mark enclosed in quotation marks in the operand field of an MNOTE instruction. One quotation mark will be listed for each pair of quotation marks in the operand field.

If any variable symbols are used in the operand field of an MNOTE instruction, they will be replaced by the values assigned to them.

Two ampersands must be used to represent an ampersand that is not part of a variable symbol in the operand field of an MNOTE statement. One ampersand will be listed for each pair of ampersands in the operand field.

The following example illustrates the use of the MNOTE instruction.

Name	Operation	Operand
	MACRO	
1	&NAME MOVE	&T,&F
2	AIF	(T'&T NE T'&F).M1
3	&NAME ST	2,SAVEAREA
	L	2,&F
	ST	2,&T
	L	2,SAVEAREA
4	.M1 MEXIT	
	MNOTE	'TYPE NOT SAME'
5	.M2 MEXIT	
	MNOTE	'TYPE NOT F'
	MEND	

Statement 1 is used to determine if the type attributes of both macro-instruction operands are the same. If they are, statement 2 is the next statement processed by the assembler. If they are not, statement 4 is the next statement processed by the assembler. Statement 4 causes an error message -- TYPE NOT SAME -- to be printed in the source program listing.

Statement 2 is used to determine if the type attribute of the first macro-instruction operand is the letter F. If the type attribute is the letter F,

statement 3 is the next statement processed by the assembler. If the attribute is not the letter F, statement 5 is the next statement processed by the assembler. Statement 5 causes an error message -- TYPE NOT F -- to be printed in the source program listing.

GLOBAL AND LOCAL VARIABLE SYMBOLS

The following are local variable symbols:

1. Symbolic parameters.
2. Local SET symbols.
3. System variable symbols.

Global SET symbols are the only global variable symbols.

The GBIA, GBLB, and GBLC instructions define global SET symbols, just as the LCLA, LCLB, and LCLC instructions define the SET symbols described in Section 9. Hereinafter, SET symbols defined by LCLA, LCLB, and LCLC instructions will be called local SET symbols.

Global SET symbols communicate values between statements in one or more macro-definitions and statements outside macro-definitions. However, local SET symbols communicate values between statements in the same macro-definition, or between statements outside macro-definitions.

If a local SET symbol is defined in two or more macro-definitions, or in a macro-definition and outside macro-definitions, the SET symbol is considered to be a different SET symbol in each case. However, a global SET symbol is the same SET symbol each place it is defined.

A SET symbol must be defined as a global SET symbol in each macro-definition in which it is to be used as a global SET symbol. A SET symbol must be defined as a global SET symbol outside macro-definitions, if it is to be used as a global SET symbol outside macro-definitions.

If the same SET symbol is defined as a global SET symbol in one or more places, and as a local SET symbol elsewhere, it is considered the same symbol wherever it is defined as a global SET symbol, and a different symbol wherever it is defined as a local SET symbol.

Defining Local and Global SET Symbols

Local SET symbols are defined when they appear in the operand field of an LCLA, LCLB, or LCIC instruction. These instructions are discussed in Section 9 under "Defining SET Symbols."

Global SET symbols are defined when they appear in the operand field of a GBLA, GBLB, or GBLC instruction. The formats of these instructions are:

Name	Operation	Operand
Blank	GBLA, GBLB, or GBLC	One or more variable symbols that are to be used as SET symbols, separated by commas

The GBLA, GBLB, and GBLC instructions define global SETA, SETB, and SETC symbols, respectively, and assign the same initial values as the corresponding types of local SET symbols. However, a global SET symbol is assigned an initial value by only the first GBLA, GBLB, or GBLC instruction processed in which the symbol appears. Subsequent GBLA, GBLB, or GBLC instructions processed by the assembler do not affect the value assigned to the SET symbol.

The programmer should not define any global SET symbols whose first four characters are &SYS.

If a GBLA, GBLB, or GBLC instruction is part of a macro-definition, it must immediately follow the prototype statement, or another GBLA, GBLB, or GBLC instruction. GBLA, GBLB, and GBLC instructions outside macro-definitions must appear after all macro-definitions in the source program, before all conditional assembly instructions and PUNCH and REPRO statements outside macro-definitions, and before the first control section of the program.

All GBLA, GBLB, and GBLC instructions in a macro-definition must appear before all LCLA, LCLB, and LCLC instructions in that macro-definition. All GBLA, GBLB, and GBLC instructions outside macro-definitions must appear before all LCLA, LCLB, and LCLC instructions outside macro-definitions.

Using Global and Local SET Symbols

The following examples illustrate the use of global and local SET symbols. Each

example consists of two parts. The first part is an assembler language source program. The second part shows the statements that would be generated by the assembler after it processed the statements in the source program.

Example 1: This example illustrates how the same SET symbol can be used to communicate (1) values between statements in the same macro-definitions, and (2) different values between statements outside macro-definitions.

Name	Operation	Operand
	MACRO	
&NAME	LOADA	
1	LCLA	&A
2	LR	15,&A
3	SETA	&A+1
	MEND	
4	LCLA	&A
FIRST	LOADA	
5	LR	15,&A
6	LOADA	
	LR	15,&A
	END	FIRST
FIRST	LR	15,0
	LR	15,0
	LR	15,0
	LR	15,0
	END	FIRST

&A is defined as a local SETA symbol in a macro-definition (statement 1) and outside macro-definitions (statement 4). &A is used twice within the macro-definition (statements 2 and 3) and twice outside macro-definitions (statements 5 and 6).

Since &A is a local SETA symbol in the macro-definition and outside macro-definitions, it is one SETA symbol in the macro-definition, and another SETA symbol outside macro-definitions. Therefore, statement 3 (which is in the macro-definition) does not affect the value used for &A in statements 5 and 6 (which are outside macro-definitions).

Example 2: This example illustrates how a SET symbol can be used to communicate values between statements that are part of a macro-definition and statements outside macro-definitions.

	Name	Operation	Operand
		MACRO	
1	&NAME	LOADA	
		GBLA	&A
2	&NAME	LR	15,&A
3	&A	SETA	&A+1
		MEND	
4		GBLA	&A
	FIRST	LOADA	
5		LR	15,&A
		LOADA	
6		LR	15,&A
		END	FIRST
	FIRST	LR	15,0
		LR	15,1
		LR	15,1
		LR	15,2
		END	FIRST

&A is defined as a global SETA symbol in a macro-definition (statement 1) and outside macro-definitions (statement 4). &A is used twice within the macro-definition (statements 2 and 3) and twice outside macro-definitions (statements 5 and 6).

Since &A is a global SETA symbol in the macro-definition and outside macro-definitions, it is the same SETA symbol in both cases. Therefore, statement 3 (which is in the macro-definition) affects the value used for &A in statements 5 and 6 (which are outside macro-definitions).

Example 3: This example illustrates how the same SET symbol can be used to communicate: (1) values between statements in one macro-definition, and (2) different values between statements in a different macro-definition.

&A is defined as a local SETA symbol in two different macro-definitions (statements 1 and 4). &A is used twice within each macro-definition (statements 2, 3, 5, and 6).

Since &A is a local SETA symbol in each macro-definition, it is one SETA symbol in one macro-definition, and another SETA symbol in the other macro-definition. Therefore, statement 3 (which is in one macro-definition) does not affect the value used for &A in statement 5 (which is in the other macro-definition). Similarly, statement 6 does not affect the value used for &A in statement 2.

	Name	Operation	Operand
		MACRO	
1	&NAME	LOADA	
		LCLA	&A
2	&NAME	LR	15,&A
3	&A	SETA	&A+1
		MEND	
		MACRO	
		LOADB	
4		LCLA	&A
5		LR	15,&A
6	&A	SETA	&A+1
		MEND	
	FIRST	LOADA	
		LOADB	
		LOADA	
		LOADB	
		END	FIRST
	FIRST	LR	15,0
		LR	15,0
		LR	15,0
		LR	15,0
		END	FIRST

Example 4: This example illustrates how a SET symbol can be used to communicate values between statements that are part of two different macro-definitions.

	Name	Operation	Operand
		MACRO	
	&NAME	LOADA	
1		GBLA	&A
2	&NAME	LR	15,&A
3	&A	SETA	&A+1
		MEND	
		MACRO	
		LOADB	
4		GBLA	&A
5		LR	15,&A
6	&A	SETA	&A+1
		MEND	
	FIRST	LOADA	
		LOADB	
		LOADA	
		LOADB	
		END	FIRST
	FIRST	LR	15,0
		LR	15,1
		LR	15,2
		LR	15,3
		END	FIRST

&A is defined as a global SETA symbol in two different macro-definitions (statements 1 and 4). &A is used twice within each macro-definition (statements 2, 3, 5 and 6).

Since &A is a global SETA symbol in each macro-definition, it is the same SETA symbol in each macro-definition. Therefore, statement 3 (which is in one macro-definition) affects the value used for &A in statement 5 (which is in the other macro-definition). Similarly, statement 6 affects the value used for &A in statement 2.

Example 5: This example illustrates how the same SET symbol can be used to communicate: (1) values between statements in two different macro-definitions, and (2) different values between statements outside macro-definitions.

	Name	Operation	Operand
		MACRO	
1	&NAME	LOADA	
		GBLA	&A
2	&NAME	LR	15,&A
3	&A	SETA	&A+1
		MEND	
		MACRO	
		LOADB	
4		GBLA	&A
5		LR	15,&A
6	&A	SETA	&A+1
		MEND	
7	FIRST	LCLA	&A
		LOADA	
		LOADB	
8		LR	15,&A
		LOADA	
		LOADB	
9		LR	15,&A
		END	FIRST
	FIRST	LR	15,0
		LR	15,1
		LR	15,0
		LR	15,2
		LR	15,3
		LR	15,0
		END	FIRST

&A is defined as a global SETA symbol in two different macro-definitions (statements 1 and 4), but it is defined as a local SETA symbol outside macro-definitions (statement 7). &A is used twice within each macro-definition and twice outside macro-definitions (statements 2, 3, 5, 6, 8 and 9).

Since &A is a global SETA symbol in each macro-definition, it is the same SETA symbol in each macro-definition. However, since &A is a local SETA symbol outside macro-definitions, it is a different SETA symbol outside macro-definitions.

Therefore, statement 3 (which is in one macro-definition) affects the value used for &A in statement 5 (which is in the other macro-definition), but it does not affect the value used for &A in statements 8 and 9 (which are outside macro-definitions). Similarly, statement 6 affects the value used for &A in statement 2, but it does not affect the value used for &A in statements 8 and 9.

Subscripted SET Symbols

Both global and local SET symbols may be defined as subscripted SET symbols. The local SET symbols defined in Section 9 were all nonsubscripted SET symbols.

Subscripted SET symbols provide the programmer with a convenient way to use one SET symbol plus a subscript to refer to many arithmetic, binary, or character values.

A subscripted SET symbol consists of a SET symbol immediately followed by a subscript that is enclosed in parentheses. The subscript may be any arithmetic expression that is allowed in the operand field of a SETA statement.

The following are valid subscripted SET symbols.

```
&READER(17)
&A23456(&S4)
&X4F2(25+&A2)
```

The following are invalid subscripted SET symbols.

```
&X4F2           (no subscript)
(25)           (no SET symbol)
&X4F2 (25)     (subscript does not
                immediately follow
                SET symbol)
```

Defining Subscripted SET Symbols: If the programmer wants to use a subscripted SET symbol, he must write in a GBLA, GBLB, GBLC, LCLA, LCLB, or LCLC instruction, a SET symbol immediately followed by a decimal integer enclosed in parentheses. The decimal integer, called a dimension, indicates the number of SET variables associated with the SET symbol. Every variable associated with a SET symbol is assigned an initial value that is the same as the

initial value assigned to the corresponding type of nonsubscripted SET symbol.

If a subscripted SET symbol is defined as global, the same dimension must be used with the SET symbol each time it is defined as global.

The maximum dimension that can be used with a SETA, SETB, or SETC symbol is 64, 255, and 64, respectively.

The following statements define the global SET symbols &SBOX, &WBOX, and &PSW, and the local SET symbol &TSW. &SBOX has 50 arithmetic variables associated with it, &WBOX has 20 character variables, &PSW and &TSW each have 230 binary variables.

Name	Operation	Operand
	GBLA	&SBOX (50)
	GBLC	&WBOX (20)
	GBLB	&PSW (230)
	LCLB	&TSW (230)

Using Subscripted SET Symbols: After the programmer has associated a number of SET variables with a SET symbol, he may assign values to each of the variables and use them in other statements.

If the statements in the previous example were part of a macro-definition, (and &A was defined as a SETA symbol in the same definition), the following statements could be part of the same macro-definition.

Name	Operation	Operand
1 &A	SETA	5
2 &PSW (&A)	SETB	(6 LT 2)
3 &TSW (9)	SETB	(&PSW (&A))
4	A	2,=F'&SBOX (45) '
5	CLI	AREA, ' &WBOX (17) '

Statement 1 assigns the arithmetic value 5 to the nonsubscripted SETA symbol &A. Statements 2 and 3 then assign the binary value 0 to subscripted SETB symbols &PSW (5) and &TSW (9), respectively. Statements 4 and 5 generate statements that add the value assigned to &SBOX(45) to general register 2, and compare the value assigned to &WBOX (17) to the value stored at AREA, respectively.

SYSTEM VARIABLE SYMBOLS

System variable symbols are local variable symbols that are assigned values automatically by the assembler. There are three system variable symbols: &SYSNDX, &SYSECT, and &SYSLIST. System variable symbols may be used in the name, operation and operand fields of statements in macro-definitions, but not in statements outside macro-definitions. They may not be defined as symbolic parameters or SET symbols, nor may they be assigned values by SETA, SETB, and SETC instructions.

&SYSNDX -- Macro-Instruction Index

The system variable symbol &SYSNDX may be concatenated with other characters to create unique names for statements generated from the same model statement.

&SYSNDX is assigned the four-digit number 0001 for the first macro-instruction processed by the assembler, and it is incremented by one for each subsequent inner and outer macro-instruction processed.

If &SYSNDX is used in a model statement, SETC or MNOTE instruction, or a character relation in a SETB or AIF instruction, the value substituted for &SYSNDX is the four-digit number of the macro-instruction being processed, including leading zeros.

If &SYSNDX appears in arithmetic expressions (e.g., in the operand field of a SETA instruction), the value used for &SYSNDX is an arithmetic value.

Throughout one use of a macro-definition, the value of &SYSNDX may be considered a constant, independent of any inner macro-instruction in that definition.

The example in the next column illustrates these rules. It is assumed that the first macro-instruction processed, OUTER 1, is the 106th macro-instruction processed by the assembler.

Statement 7 is the 106th macro-instruction processed. Therefore, &SYSNDX is assigned the number 0106 for that macro-instruction. The number 0106 is substituted for &SYSNDX when it is used in statements 4 and 6. Statement 4 is used to assign the character value 0106 to the SETC symbol &NDXNUM. Statement 6 is used to create the unique name B0106.

Name	Operation	Operand
	MACRO	
	INNER1	
1	A&SYSNDX	GBLC &NDXNUM
		SR 2,5
		CR 2,5
2		BE B&NDXNUM
3		B A&SYSNDX
	MEND	
	MACRO	
	OUTER1	
4	&NAME	GBLC &NDXNUM
	&NDXNUM	SETC '&SYSNDX'
	&NAME	SR 2,4
		AR 2,6
5		INNER1
6	B&SYSNDX	S 2,=F'1000'
	MEND	
7	ALPHA	OUTER1
8	BETA	OUTER1
	ALPHA	SR 2,4
		AR 2,6
	A0107	SR 2,5
		CR 2,5
		BE B0106
		B A0107
	B0106	S 2,=F'1000'
	BETA	SR 2,4
		AR 2,6
	A0109	SR 2,5
		CR 2,5
		BE B0108
		B A0109
	B0108	S 2,=F'1000'

&SYSECT -- Current Control Section

The system variable symbol &SYSECT may be used to represent the name of the control section in which a macro-instruction appears. For each inner and outer macro-instruction processed by the assembler, &SYSECT is assigned a value that is the name of the control section in which the macro-instruction appears.

When &SYSECT is used in a macro-definition, the value substituted for &SYSECT is the name of the last CSECT, DSECT, or START statement that occurs before the macro-instruction. If no named CSECT, DSECT, or START statements occur before a macro-instruction, &SYSECT is assigned a null character value for that macro-instruction.

CSECT or DSECT statements processed in a macro-definition affect the value for &SYSECT for any subsequent inner macro-instructions in that definition, and for any other outer and inner macro-instructions.

Throughout the use of a macro-definition, the value of &SYSECT may be considered a constant, independent of any CSECT or DSECT statements or inner macro-instructions in that definition.

The next example illustrates these rules.

Statement 5 is the 107th macro-instruction processed. Therefore, &SYSNDX is assigned the number 0107 for that macro-instruction. The number 0107 is substituted for &SYSNDX when it is used in statements 1 and 3. The number 0106 is substituted for the global SETC symbol &NDXNUM in statement 2.

Statement 8 is the 108th macro-instruction processed. Therefore, each occurrence of &SYSNDX is replaced by the number 0108. For example, statement 6 is used to create the unique name B0108.

When statement 5 is used to process the 108th macro-instruction, statement 5 becomes the 109th macro-instruction processed. Therefore, each occurrence of &SYSNDX is replaced by the number 0109. For example, statement 1 is used to create the unique name A0109.

Statement 8 is the last CSECT, DSECT, or START statement processed before statement 9 is processed. Therefore, &SYSECT is assigned the value MAINPROG for macro-instruction OUTER1 in statement 9. MAINPROG is substituted for &SYSECT when it appears in statement 6.

Statement 3 is the last CSECT, DSECT, or START statement processed before statement 4 is processed. Therefore, &SYSECT is assigned the value CSOUT1 for macro-instruction INNER in statement 4. CSOUT1 is substituted for &SYSECT when it appears in statement 2.

Statement 1 is used to generate a CSECT statement for statement 4. This is the last CSECT, DSECT, or START statement that appears before statement 5. Therefore, &SYSECT is assigned the value INA for macro-instruction INNER in statement 5. INA is substituted for &SYSECT when it appears in statement 2.

Name	Operation	Operand
1 2	MACRO	
	INNER	&INCSECT
	CSECT	
	DC	A (&SYSECT)
3 4 5 6	MEND	
	MACRO	
	OUTER1	
	CSECT	
	DS	100C
	INNER	INA
7	INNER	INB
	DC	A (&SYSECT)
	MEND	
	MACRO	
8 9 10	OUTER2	
	DC	A (&SYSECT)
	MEND	
MAINPROG	CSECT	
	DS	200C
CSOUT1	OUTER1	
	OUTER2	
MAINPROG	CSECT	
	DS	200C
CSOUT1	CSECT	
	DS	100C
INA	CSECT	
INB	DC	A (CSOUT1)
	CSECT	
	DC	A (INA)
	DC	A (MAINPROG)
	DC	A (INB)

Statement 1 is used to generate a CSECT statement for statement 5. This is the last CSECT, DSECT, or START statement that appears before statement 10. Therefore, &SYSECT is assigned the value INB for macro-instruction OUTER2 in statement 10. INB is substituted for &SYSECT when it appears in statement 7.

&SYSLIST -- Macro-Instruction Operand

The system variable symbol &SYSLIST provides the programmer with an alternative to symbolic parameters for referring to macro-instruction operands.

&SYSLIST and symbolic parameters may be used in the same macro-definition.

&SYSLIST(n) may be used to refer to the nth macro-instruction operand. In addition, if the nth operand is a sublist, then &SYSLIST(n,m) may be used to refer to the mth operand in the sublist, where n and

m may be any arithmetic expressions allowed in the operand field of a SETA statement.

The type, length, scaling, integer, and count attributes of &SYSLIST(n) and &SYSLIST(n,m) and the number attributes of &SYSLIST(n) and &SYSLIST may be used in conditional assembly instructions. N'&SYSLIST may be used to refer to the total number of operands in a macro-instruction statement. N'&SYSLIST(n) may be used to refer to the number of operands in a sublist. If the nth operand is omitted, N' is zero; if the nth operand is not a sublist, N' is one.

The following procedure is used to evaluate N'&SYSLIST:

1. A sublist is considered to be one operand.
2. The number of operands equals one plus the number of commas indicating the end of an operand.

Attributes are discussed in Section 7 under "Attributes."

KEYWORD MACRO-DEFINITIONS AND INSTRUCTIONS

Keyword macro-definitions provide the programmer with an alternate way of preparing macro-definitions.

A keyword macro-definition enables a programmer to reduce the number of operands in each macro-instruction that corresponds to the definition, and to write the operands in any order.

The macro-instructions that correspond to the macro-definitions described in Section 7 (hereinafter called positional macro-instructions and positional macro-definitions, respectively) require the operands to be written in the same order as the corresponding symbolic parameters in the operand field of the prototype statement.

In a keyword macro-definition, the programmer can assign standard values to any symbolic parameters that appear in the operand field of the prototype statement. The standard value assigned to a symbolic parameter is substituted for the symbolic parameter, if the programmer does not write anything in the operand field of the macro-instruction to correspond to the symbolic parameter.

When a keyword macro-instruction is written, the programmer need only write one operand for each symbolic parameter whose value he wants to change.

Keyword macro-definitions are prepared the same way as positional macro-definitions, except that the prototype statement is written differently, and &SYSLIST may not be used in the definition. The rules for preparing positional macro-definitions are in Section 7.

Name	Operation	Operand
&N	MOVE	&R=2, &A=S, &T=, &F=

Keyword Prototype

The format of this statement is:

Name	Operation	Operand
A symbolic parameter or blank	A symbol	One or more operands of the form described below, separated by commas

Each operand must consist of a symbolic parameter, immediately followed by an equal sign and optionally followed by a standard value.

A standard value that is part of an operand must immediately follow the equal sign.

Anything that may be used as an operand in a macro-instruction except variable symbols, may be used as a standard value in a keyword prototype statement. The rules for forming valid macro-instruction operands are detailed in Section 8.

The following are valid keyword prototype operands.

```
&READER=
&LOOP2=SYMBOL
&S4==F'4096'
```

The following are invalid keyword prototype operands.

```
CARDAREA      (no symbolic parameter)
&TYPE         (no equal sign)
&TWO =123     (equal sign does not
               immediately follow
               symbolic parameter)
&AREA= X'189A' (standard value does
               not immediately follow
               equal sign)
```

The following keyword prototype statement contains a symbolic parameter in the name field, and four operands in the operand field. The first two operands contain standard values. The mnemonic operation code is MOVE.

Keyword Macro-Instruction

After a programmer has prepared a keyword macro-definition he may use it by writing a keyword macro-instruction.

The format of a keyword macro-instruction is:

Name	Operation	Operand
A symbol, sequence symbol, or blank	Mnemonic operation code	Zero or more operands of the form described below, separated by commas

Each operand consists of a keyword immediately followed by an equal sign and an optional value. Anything that may be used as an operand in a positional macro-instruction may be used as a value in a keyword macro-instruction. The rules for forming valid positional macro-instruction operands are detailed in Section 8.

A keyword consists of one through seven letters and digits, the first of which must be a letter.

The keyword part of each keyword macro-instruction operand must correspond to one of the symbolic parameters that appears in the operand field of the keyword prototype statement. A keyword corresponds to a symbolic parameter if the characters of the keyword are identical to the characters of the symbolic parameter that follow the ampersand.

The following are valid keyword macro-instruction operands.

```
LOOP2=SYMBOL
S4==F'4096'
TO=
```

The following are invalid keyword macro-instruction operands.

```
&X4F2=0(2,3) (keyword does not begin
               with a letter)
CARDAREA=A+2 (keyword is more than
               seven characters)
=(TO(8), (FROM)) (no keyword)
```


The operands in a keyword macro-instruction may be written in any order. If an operand appeared in a keyword prototype statement, a corresponding operand does not have to appear in the keyword macro-instruction. If an operand is omitted, the comma that would have separated it from the next operand need not be written.

The following rules are used to replace the symbolic parameters in the statements of a keyword macro-definition.

1. If a symbolic parameter appears in the name field of the prototype statement, and the name field of the macro-instruction contains a symbol, the symbolic parameter is replaced by the symbol. If the name field of the macro-instruction is blank or contains a sequence symbol, the symbolic parameter is replaced by a null character value.
2. If a symbolic parameter appears in the operand field of the prototype statement, and the macro-instruction contains a keyword that corresponds to the symbolic parameter, the value assigned to the keyword replaces the symbolic parameter.
3. If a symbolic parameter was assigned a standard value by a prototype statement, and the macro-instruction does not contain a keyword that corresponds to the symbolic parameter, the standard value assigned to the symbolic parameter replaces the symbolic parameter. Otherwise, the symbolic parameter is replaced by a null character value.

Note: If a standard value is a self-defining term the type attribute assigned to the standard value is the letter N. If a standard value is omitted the type attribute assigned to the standard value is the letter O. All other standard values are assigned the type attribute U.

The following keyword macro-definition, keyword macro-instruction, and generated statements illustrate these rules.

Statement 1 assigns the standard values 2 and S to the symbolic parameters &R and &A, respectively. Statement 6 assigns the values FA, FB, and THERE to the keywords T, F, and A, respectively. The symbol HERE is used in the name field of statement 6.

Since a symbolic parameter (&N) appears in the name field of the prototype statement (statement 1), and the corresponding characters (HERE) of the macro-instruction (statement 6) are a symbol, &N is replaced by HERE in statement 2.

Name	Operation	Operand
	MACRO	
1 &N	MOVE	&R=2, &A=S, &T=, &F=
2 &N	ST	&R, &A
3	L	&R, &F
4	ST	&R, &T
5	L	&R, &A
	MEND	
6 HERE	MOVE	T=FA, F=FB, A=THERE
HERE	ST	2, THERE
	L	2, FB
	ST	2, FA
	L	2, THERE

Since &T appears in the operand field, of statement 1, and statement 6 contains the keyword (T) that corresponds to &T, the value assigned to T (FA) replaces &T in statement 4. Similarly, FB and THERE replace &F and &A in statement 3 and in statements 2 and 5, respectively. Note that the value assigned to &A in statement 6 is used instead of the value assigned to &A in statement 1.

Since &R appears in the operand field of statement 1, and statement 6 does not contain a corresponding keyword, the value assigned to &R (2), replaces &R in statements 2, 3, 4, and 5.

Operand Sublists: The value assigned to a keyword and the standard value assigned to a symbolic parameter may be an operand sublist. Anything that may be used as an operand sublist in a positional macro-instruction may be used as a value in a keyword macro-instruction and as a standard value in a keyword prototype statement. The rules for forming valid operand sublists are detailed in Section 8 under "Operand Sublists."

Keyword Inner Macro-Instructions: Keyword and positional inner macro-instructions may be used as model statements in either keyword or positional macro-definitions.

MIXED-MODE MACRO-DEFINITIONS AND INSTRUCTIONS

Mixed-mode macro-definitions allow the programmer to use the features of keyword and positional macro-definitions in the same macro-definition.

Mixed-mode macro-definitions are prepared the same way as positional macro-definitions, except that the prototype

statement is written differently, and &SYSLIST may not be used in the definition. The rules for preparing positional macro-definitions are in Section 7.

of the operand field is written in the same way that the operand field of a positional macro-instruction is written. The rules for writing positional macro-instructions are in Section 8.

Mixed-Mode Prototype

The format of this statement is:

Name	Operation	Operand
A symbolic parameter or blank	A symbol	Two or more operands of the form described below, separated by commas

The second part of the operand field corresponds to the keyword prototype operands. This part of the operand field is written in the same way that the operand field of a keyword macro-instruction is written. The rules for writing keyword macro-instructions are described above under "Keyword Macro-Instruction."

The following mixed-mode macro-definition, mixed-mode macro-instruction, and generated statements illustrate these facilities.

The operands must be valid operands of positional and keyword prototype statements. All the positional operands must precede the first keyword operand. The rules for forming positional operands are discussed in Section 7, under "Macro-Instruction Prototype." The rules for forming keyword operands are discussed above under "Keyword Prototype."

Name	Operation	Operand
1	&N MOVE &N	MACRO &TY, &P, &R, &TO=, &F= &R, SAVE &R, &P&F &R, &P&TO &R, SAVE
2	HERE HERE	MOVE H,,2,F=FB,TO=FA STH LH STH LH
		2,SAVE 2,FB 2,FA 2,SAVE

The following sample mixed-mode prototype statement contains three positional operands and two keyword operands.

Name	Operation	Operand
&N	MOVE	&TY, &P, &R, &TO=, &F=

The prototype statement (statement 1) contains three positional operands (&TY, &P, and &R) and two keyword operands (&TO and &F). In the macro-instruction (statement 2) the positional operands are written in the same order as the positional operands in the prototype statement (the second operand is omitted). The keyword operands are written in an order that is different from the order of keyword operands in the prototype statement.

Mixed-Mode Macro-Instruction

The format of a mixed-mode macro-instruction is:

Mixed-mode inner macro-instructions may be used as model statements in mixed-mode, keyword, and positional macro-definitions. Keyword and positional inner macro-instructions may be used as model statements in mixed-mode macro-definitions.

Name	Operation	Operand
A symbol, sequence symbol, or blank	Mnemonic operation code	Zero or more operands of the form described below, separated by commas

MACRO-DEFINITION COMPATIBILITY

The operand field consists of two parts. The first part corresponds to the positional prototype operands. This part

Macro-definitions prepared for use with the other System/360 assemblers having macro language facilities may be used with the Operating System/360 assembler provided

that all SET symbols are defined in an appropriate LCLB, GBLA, GBLB, or GBLC statement. The AIFB and AGOB instructions

will be processed by the Operating System/360 assembler the same way that the AIF and AGO instructions are processed.

APPENDIXES

APPENDIX A: CHARACTER CODES

APPENDIX B: MACHINE-INSTRUCTION MNEMONIC OPERATION CODES

APPENDIX C: ASSEMBLER INSTRUCTIONS

APPENDIX D: MACHINE-INSTRUCTION FORMAT

APPENDIX E: HEXADECIMAL-DECIMAL NUMBER CONVERSION TABLE

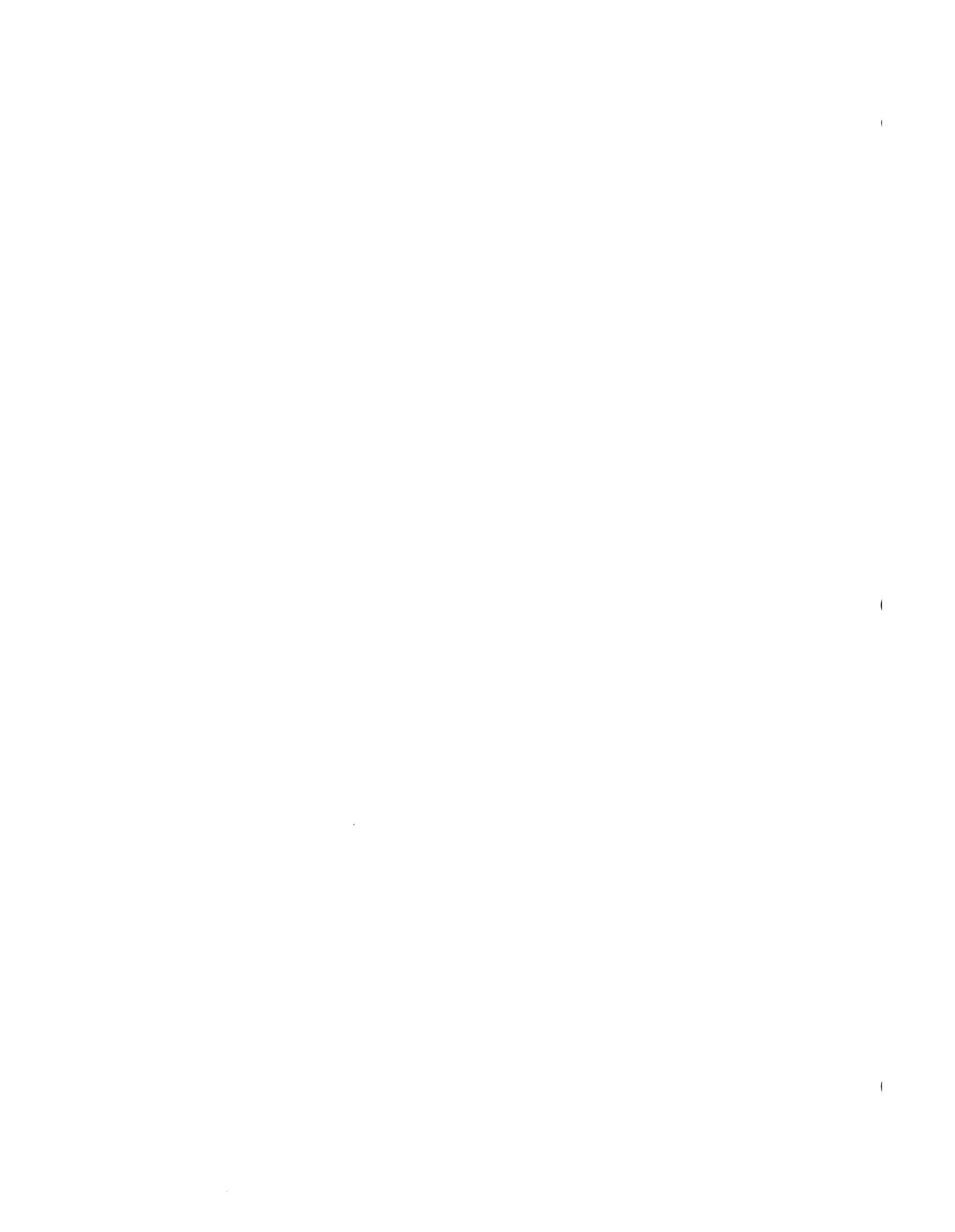
APPENDIX F: SUMMARY OF CONSTANTS

APPENDIX G: MACRO LANGUAGE SUMMARY

APPENDIX H: INTERNAL TABLE CAPACITIES

APPENDIX I: SAMPLE PROGRAM

APPENDIX J: ASSEMBLER LANGUAGES--FEATURES COMPARISON CHART



APPENDIX A: CHARACTER CODES

This appendix lists all System/360 card codes to which a printer graphic is assigned. See the principles of operation manual for additional card codes and their internal representation.

<u>Card Code</u>	<u>Printer Graphics</u>	<u>Internal Representation</u>	<u>Card Code</u>	<u>Printer Graphics</u>	<u>Internal Representation</u>
	blank	01000000	11,1	J	11010001
12,8,3	. (period)	01001011	11,2	K	11010010
12,8,4	<	01001100	11,3	L	11010011
12,8,5	(01001101	11,4	M	11010100
12,8,6	+	01001110	11,5	N	11010101
12	ε	01010000	11,6	O	11010110
11,8,3	\$	01011011	11,7	P	11010111
11,8,4	*	01011100	11,8	Q	11011000
11,8,5)	01011101	11,9	R	11011001
11	-	01100000	0,2	S	11100010
0,1	/	01100001	0,3	T	11100011
0,8,3	,	01101011	0,4	U	11100100
0,8,4	%	01101100	0,5	V	11100101
8,3	#	01111011	0,6	W	11100110
8,4	@	01111100	0,7	X	11100111
8,5	' (quote)	01111101	0,8	Y	11101000
8,6	=	01111110	0,9	Z	11101001
12,1	A	11000001	0	0	11110000
12,2	B	11000010	1	1	11110001
12,3	C	11000011	2	2	11110010
12,4	D	11000100	3	3	11110011
12,5	E	11000101	4	4	11110100
12,6	F	11000110	5	5	11110101
12,7	G	11000111	6	6	11110110
12,8	H	11001000	7	7	11110111
12,9	I	11001001	8	8	11111000
			9	9	11111001

APPENDIX B: MACHINE-INSTRUCTION MNEMONIC CODES

This appendix contains a table of the mnemonic operation codes for machine-instructions, arranged by machine format and hexadecimal operation code, and an alphabetical listing of the mnemonic operation codes.

The following list is an alphabetical listing of the mnemonic operation codes of all the machine instructions that can be represented in assembler language. The column headings in the list and the information each column provides are as follows:

Mnemonic Code: This column gives the mnemonic operation code for the machine instruction.

Instruction: This column contains the name of the instruction associated with the mnemonic.

Operation Code: This column contains the hexadecimal equivalent of the actual machine operation code.

Basic Machine Format: This column gives the basic machine format of the instruction: RR, RX, RS, SI or SS.

Operand Field Format: This column shows the symbolic format of the operand field for the particular mnemonic.

<u>Mnemonic Code</u>	<u>Instruction</u>	<u>Opera- tion Code</u>	<u>Basic Machine Format</u>	<u>Operand Field Format</u>
A	Add	5A	RX	R1,D2 (X2,B2)
AD	Add Normalized, Long	6A	RX	R1,D2 (X2,B2)
ADR	Add Normalized, Long	2A	RR	R1,R2
AE	Add Normalized, Short	7A	RX	R1,D2 (X2,B2)
AER	Add Normalized, Short	3A	RR	R1,R2
AH	Add Half Word	4A	RX	R1,D2 (X2,B2)
AL	Add Logical	5E	RX	R1,D2 (X2,B2)
ALR	Add Logical	1E	RR	R1,R2
AP	Add Decimal	FA	SS	D1 (L1, B1) ,D2 (L2,B2)
AR	Add	1A	RR	R1,R2
AU	Add Unnormalized, Short	7E	RX	R1,D2 (X2,B2)
AUR	Add Unnormalized, Short	3E	RR	R1,R2
AW	Add Unnormalized, Long	6E	RX	R1,D2 (X2,B2)
AWR	Add Unnormalized, Long	2E	RR	R1,R2
BAL	Branch and Link	45	RX	R1,D2 (X2,B2)
BALR	Branch and Link	05	RR	R1,R2
BC	Branch on Condition	47	RX	M1,D2 (X2,B2)
BCR	Branch on Condition	07	RR	M1,R2
BCT	Branch on Count	46	RX	R1,D2 (X2,B2)
BCTR	Branch on Count	06	RR	R1,R2
BXH	Branch on Index High	86	RS	R1,R3,D2 (B2)
BXLE	Branch on Index Low or Equal	87	RS	R1,R3,D2 (B2)
C	Compare Algebraic	59	RX	R1,D2 (X2,B2)
CD	Compare, Long	69	RX	R1,D2 (X2,B2)
CDR	Compare, Long	29	RR	R1,R2
CE	Compare, Short	79	RX	R1,D2 (X2,B2)
CER	Compare, Short	39	RR	R1,R2
CH	Compare Half Word	49	RX	R1,D2 (X2,B2)
CL	Compare Logical	55	RX	R1,D2 (X2,B2)
CLC	Compare Logical	D5	SS	D1 (L, B1) ,D2 (B2)
CLI	Compare Logical Immediate	95	SI	D1 (B1) ,I2
CLR	Compare Logical	15	RR	R1,R2
CP	Compare Decimal	F9	SS	D1 (L1, B1) ,D2 (L2,B2)
CR	Compare Algebraic	19	RR	R1,R2
CVB	Convert to Binary	4F	RX	R1,D2 (X2,B2)
CVD	Convert to Decimal	4E	RX	R1,D2 (X2,B2)
D	Divide	5D	RX	R1,D2 (X2,B2)
DD	Divide, Long	6D	RX	R1,D2 (X2,B2)
DDR	Divide, Long	2D	RR	R1,R2
DE	Divide, Short	7D	RX	R1,D2 (X2,B2)

Appendix B: Machine-Instruction Mnemonic Codes (Continued)

<u>Mnemonic Code</u>	<u>Instruction</u>	<u>Operation Code</u>	<u>Basic Machine Format</u>	<u>Operand Field Format</u>
DER	Divide, Short	3D	RR	R1, R2
DP	Divide Decimal	FD	SS	D1 (L1, B1) , D2 (L2, B2)
DR	Divide	1D	RR	R1, R2
ED	Edit	DE	SS	D1 (L, B1) , D2 (B2)
EDMK	Edit and Mark	DF	SS	D1 (L, B1) , D2 (B2)
EX	Execute	44	RX	R1, D2 (X2, B2)
HDR	Halve, Long	24	RR	R1, R2
HER	Halve, Short	34	RR	R1, R2
HIO	Halt I/O	9E	SI	D1 (B1)
IC	Insert Character	43	RX	R1, D2 (X2, B2)
ISK	Insert Storage Key	09	RR	R1, R2
L	Load	58	RX	R1, D2 (X2, B2)
LA	Load Address	41	RX	R1, D2 (X2, B2)
LCDR	Load Complement, Long	23	RR	R1, R2
LCER	Load Complement, Short	33	RR	R1, R2
LCR	Load Complement	13	RR	R1, R2
LD	Load, Long	68	RX	R1, D2 (X2, B2)
LDR	Load, Long	28	RR	R1, R2
LE	Load, Short	78	RX	R1, D2 (X2, B2)
LER	Load, Short	38	RR	R1, R2
LH	Load Half Word	48	RX	R1, D2 (X2, B2)
LM	Load Multiple	98	RS	R1, R3, D2 (B2)
LNDR	Load Negative, Long	21	RR	R1, R2
LNER	Load Negative, Short	31	RR	R1, R2
LNR	Load Negative	11	RR	R1, R2
LPDR	Load Positive, Long	20	RR	R1, R2
LPER	Load Positive, Short	30	RR	R1, R2
LPR	Load Positive	10	RR	R1, R2
LPSW	Load PSW	82	SI	D1 (B1)
LR	Load	18	RR	R1, R2
LTDR	Load and Test, Long	22	RR	R1, R2
LTER	Load and Test, Short	32	RR	R1, R2
LTR	Load and Test	12	RR	R1, R2
M	Multiply	5C	RX	R1, D2 (X2, B2)
MD	Multiply, Long	6C	RX	R1, D2 (X2, B2)
MDR	Multiply, Long	2C	RR	R1, R2
ME	Multiply, Short	7C	RX	R1, D2 (X2, B2)
MER	Multiply, Short	3C	RR	R1, R2
MH	Multiply Half Word	4C	RX	R1, D2 (X2, B2)
MP	Multiply Decimal	FC	SS	D1 (L1, B1) , D2 (L2, B2)
MR	Multiply	1C	RR	R1, R2
MVC	Move Characters	D2	SS	D1 (L, B1) , D2 (B2)
MVI	Move Immediate	92	SI	D1 (B1) , I2
MVN	Move Numerics	D1	SS	D1 (L, B1) , D2 (B2)
MVO	Move with Offset	F1	SS	D1 (L1, B1) , D2 (L2, B2)
MVZ	Move Zones	D3	SS	D1 (L, B1) , D2 (B2)
N	AND Logical	54	RX	R1, D2 (X2, B2)
NC	AND Logical	D4	SS	D1 (L, B1) , D2 (B2)
NI	AND Logical Immediate	94	SI	D1 (B1) , I2
NR	AND Logical	14	RR	R1, R2
O	OR Logical	56	RX	R1, D2 (X2, B2)
OC	OR Logical	D6	SS	D1 (L, B1) , D2 (B2)
OI	OR Logical Immediate	96	SI	D1 (B1) , I2
OR	OR Logical	16	RR	R1, R2
PACK	Pack	F2	SS	D1 (L1, B1) , D2 (L2, B2)
RDD	Read Direct	85	SI	D1 (B1) , I2
S	Subtract	5B	RX	R1, D2 (X2, B2)
SD	Subtract Normalized, Long	6B	RX	R1, D2 (X2, B2)

Appendix B: Machine-Instruction Mnemonic Codes (Continued)

<u>Mnemonic Code</u>	<u>Instruction</u>	<u>Operation Code</u>	<u>Basic Machine Format</u>	<u>Operand Field Format</u>
SDR	Subtract Normalized, Long	2B	RR	R1,R2
SE	Subtract Normalized, Short	7B	RX	R1,D2 (X2,B2)
SER	Subtract Normalized, Short	3B	RR	R1,R2
SH	Subtract Half Word	4B	RX	R1,D2 (X2,B2)
SIO	Start I/O	9C	SI	D1 (B1)
SL	Subtract Logical	5F	RX	R1,D2 (X2,B2)
SLA	Shift Left Single Algebraic	8B	RS	R1,D2 (B2)
SLDA	Shift Left Double Algebraic	8F	RS	R1,D2 (B2)
SLDL	Shift Left Double Logical	8D	RS	R1,D2 (B2)
SLL	Shift Left Single Logical	89	RS	R1,D2 (B2)
SLR	Subtract Logical	1F	RR	R1,R2
SP	Subtract Decimal	FB	SS	D1 (L1, B1) ,D2 (L2,B2)
SPM	Set Program Mask	04	RR	R1
SR	Subtract	1B	RR	R1,R2
SRA	Shift Right Single Algebraic	8A	RS	R1,D2 (B2)
SRDA	Shift Right Double Algebraic	8E	RS	R1,D2 (B2)
SRDL	Shift Right Double Logical	8C	RS	R1,D2 (B2)
SRL	Shift Right Single Logical	88	RS	R1,D2 (B2)
SSK	Set System Key	08	RR	R1,R2
SSM	Set System Mask	80	SI	D1 (B1)
ST	Store	50	RX	R1,D2 (X2,B2)
STC	Store Character	42	RX	R1,D2 (X2,B2)
STD	Store Long	60	RX	R1,D2 (X2,B2)
STE	Store Short	70	RX	R1,D2 (X2,B2)
STH	Store Half Word	40	RX	R1,D2 (X2,B2)
STM	Store Multiple	90	RS	R1,R3,D2 (B2)
SU	Subtract Unnormalized, Short	7F	RX	R1,D2 (X2,B2)
SUR	Subtract Unnormalized, Short	3F	RR	R1,R2
SVC	Supervisor Call	0A	RR	I
SW	Subtract Unnormalized, Long	6F	RX	R1,D2 (X2,B2)
SWR	Subtract Unnormalized, Long	2F	RR	R1,R2
TCH	Test Channel	9F	SI	D1 (B1)
TIO	Test I/O	9D	SI	D1 (B1)
TM	Test Under Mask	91	SI	D1 (B1) ,I2
TR	Translate	DC	SS	D1 (L, B1) ,D2 (B2)
TRT	Translate and Test	DD	SS	D1 (L, B1) ,D2 (B2)
TS	Test and Set	93	SI	D1 (B1)
UNPK	Unpack	F3	SS	D1 (L1, B1) ,D2 (L2, B2)
WRD	Write Direct	84	SI	D1 (B1) ,I2
X	Exclusive OR	57	RX	R1,D2 (X2,B2)
XC	Exclusive OR	D7	SS	D1 (L, B1) ,D2 (B2)
XI	Exclusive OR, Immediate	97	SI	D1 (B1) ,I2
XR	Exclusive OR	17	RR	R1,R2
ZAP	Zero and Add Decimal	F8	SS	D1 (L1, B1) ,D2 (L2, B2)

Extended Mnemonic Instruction Codes

<u>Extended Code</u>	<u>Meaning</u>	<u>Machine Instruction</u>
B D2 (X2,B2)	Branch Unconditional	BC 15,D2 (X2,B2)
BR R2	Branch Unconditional (RR format)	BCR 15,R2
NOP D2 (X2,B2)	No Operation	BC 0,D2 (X2,B2)
NOPR R2	No Operation (RR format)	BCR 0,R2
Used After Compare Instructions		
BH D2 (X2,B2)	Branch on High	BC 2,D2 (X2,B2)
BL D2 (X2,B2)	Branch on Low	BC 4,D2 (X2,B2)
BE D2 (X2,B2)	Branch on Equal	BC 8,D2 (X2,B2)
BNH D2 (X2,B2)	Branch on Not High	BC 13,D2 (X2,B2)
BNL D2 (X2,B2)	Branch on Not Low	BC 11,D2 (X2,B2)
BNE D2 (X2,B2)	Branch on Not Equal	BC 7,D2 (X2,B2)
Used After Arithmetic Instructions		
BO D2 (X2,B2)	Branch on Overflow	BC 1,D2 (X2,B2)
BP D2 (X2,B2)	Branch on Plus	BC 2,D2 (X2,B2)
BM D2 (X2,B2)	Branch on Minus	BC 4,D2 (X2,B2)
BZ D2 (X2,B2)	Branch on Zero	BC 8,D2 (X2,B2)
Used After Test Under Mask Instructions		
BO D2 (X2,B2)	Branch if Ones	BC 1,D2 (X2,B2)
BM D2 (X2,B2)	Branch if Mixed	BC 4,D2 (X2,B2)
BZ D2 (X2,B2)	Branch if Zeros	BC 8,D2 (X2,B2)

RR Format		Class			
		Fixed-Point Fullword and Logical	Floating-Point Long	Floating-Point Short	
		-0x-	-1x-	-2x-	-3x-
X					
0		Load Positive.....LPR	Load Positive.....LPDR	Load Positive.....LPER	
1		Load Negative.....LNR	Load Negative.....LNDR	Load Negative.....LNER	
2		Load and Test.....LTR	Load and Test.....LTRD	Load and Test.....LTER	
3		Load Complement...LCR	Load Complement...LCDR	Load Complement...LCER	
4	Set Program Mask...SPM	AND.....NR	Halve.....HDR	Halve.....HER	
5	Branch and Link....BALR	Compare Logical...CLR			
6	Branch on Count....BCTR	OR.....OR			
7	Branch/Condition...BCR	Exclusive OR....XOR			
8	Set Key.....SKK	Load.....LR	Load.....LDR	Load.....LER	
9	Insert Key.....ISK	Compare.....CR	Compare.....CDR	Compare.....CER	
A	Supervisor Call...SVC	Add.....AR	Add N.....ADR	Add N.....AER	
B		Subtract.....SR	Subtract N....SDR	Subtract.....SER	
C		Multiply.....MR	Multiply.....MDR	Multiply.....MER	
D		Divide.....DR	Divide.....DDR	Divide.....DER	
E		Add Logical...ALR	Add U.....AWR	Add U.....AUR	
F		Subtract Logical...SLR	Subtract U....SWR	Subtract.....SUR	

RS,SI Format		Class			
		Fixed-Point Logical and Input-Output			
		-8x-	-9x-	-Ax-	-Bx-
X					
0	Set System Mask...SSM	Store Multiple...STM			
1		Test under Mask...TM			
2	Load PSW.....LPSW	Move.....MVI			
3	Diagnose	Test and Set....TS			
4	Write Direct.....WRD	AND.....NI			
5	Read Direct.....RDD	Compare Logical...CLI			
6	Branch/High.....BXH	OR.....OI			
7	Branch/Low-Equal...BXLE	Exclusive OR....XI			
8	Shift Right SL....SRL	Load Multiple...LM			
9	Shift Left SL....SLL				
A	Shift Right S.....SRA				
B	Shift Left S.....SLA				
C	Shift Right DL....SRDL	Start I-O.....SIO			
D	Shift Left DL....SLDL	Test I-O.....TIO			
E	Shift Right D.....SRDA	Halt I-O.....HIO			
F	Shift Left D.....SLDA	Test Channel...TCH			

Operation Code Notes
 U = Unnormalized
 S = Single
 D = Double
 N = Normalized
 SL = Single Logical
 DL = Double Logical

RX Format		Class			
		Fixed-Point Halfword and Branching	Fixed-Point Fullword and Logical	Floating-Point Long	Floating-Point Short
		-4x-	-5x-	-6x-	-7x-
X					
0	Store.....STH	Store.....ST	Store.....STD	Store.....STE	
1	Load Address.....LA				
2	Store Character...STC				
3	Insert Character...IC				
4	Execute.....EX	AND.....N			
5	Branch and Link...BAL	Compare Logical...CL			
6	Branch on Count...BCT	OR.....O			
7	Branch/Condition...BC	Exclusive OR....X			
8	Load.....LH	Load.....L	Load.....LD	Load.....LE	
9	Compare.....CH	Compare.....C	Compare.....CD	Compare.....CE	
A	Add.....AH	Add.....A	Add N.....AD	Add N.....AE	
B	Subtract.....SH	Subtract.....S	Subtract N....SD	Subtract N....SE	
C	Multiply.....MH	Multiply.....M	Multiply.....MD	Multiply.....ME	
D		Divide.....D	Divide.....DD	Divide.....DE	
E	Convert-Decimal...CVD	Add Logical...AL	Add U.....AW	Add U.....AU	
F	Convert-Binary...CVB	Subtract Logical...SL	Subtract U.....SW	Subtract U.....SU	

SS Format		Class			
		Logical	Decimal		
		-Cx-	-Dx-	-Ex-	-Fx-
X					
0					
1		Move Numeric.....MVN			Move with Offset...MVO
2		Move Characters...MVC			Pack.....PACK
3		Move Zone.....MVZ			Unpack.....UNPK
4		AND.....NC			
5		Compare Logical...CLC			
6		OR.....OC			
7		Exclusive OR....XC			
8					Zero and Add.....ZAP
9					Compare.....CP
A					Add.....AP
B					Subtract.....SP
C		Translate.....TR			Multiply.....MP
D		Translate and Test...TRT			Divide.....DP
E		Edit.....ED			
F		Edit and Mark....EDMK			

<u>Mnemonic</u>	<u>Name Field</u>	<u>Operand Field</u>
CCW	An optional symbol	Four operands, separated by commas
CNOP	blank	Two absolute expressions, separated by a comma
COM	blank	Not used; should be blank
COPY	blank	One symbol
CSECT	An optional symbol	Not used; should be blank
DC	An optional symbol	One or more operands, separated by commas
DROP	blank	One to sixteen absolute expressions, separated by commas
DS	An optional symbol	One or more operands, separated by commas
DSECT	A required symbol	Not used; should be blank
EJECT	blank	Not used; should be blank
END	blank	A relocatable expression or blank
ENTRY	blank	One or more relocatable symbols, separated by commas
EQU	A required symbol	An absolute or relocatable expression
EXTRN	blank	One or more relocatable symbols, separated by commas
ICTL	blank	One to three decimal values, separated by commas
ISEQ	blank	Two decimal values, separated by a comma
LTOrg	an optional symbol	Not used; should be blank
ORG	blank	A relocatable expression or blank
PRINT	blank	One to three operands
PUNCH	blank	1 to 80 characters enclosed in single quotation marks
REPRO	blank	Not used; should be blank
SPACE	blank	A decimal term or blank
START	An optional symbol	A self-defining term or blank
TITLE	An optional symbol 0-4 charact	A sequence of characters, enclosed in single quotation marks
USING	blank	An absolute or relocatable expression followed by 1 to 16 absolute expressions, separated by commas

APPENDIX D: MACHINE-INSTRUCTION FORMAT

Basic Machine Format		Assembler Operand Field Format	Applicable Instructions														
RR	<table border="1"> <tr> <td>8</td> <td>4</td> <td>4</td> </tr> <tr> <td>Operation Code</td> <td>R1</td> <td>R2</td> </tr> </table>	8	4	4	Operation Code	R1	R2	R1,R2	All RR instructions except SPM and SVC								
	8	4	4														
	Operation Code	R1	R2														
<table border="1"> <tr> <td>8</td> <td>4</td> <td>4</td> </tr> <tr> <td>Operation Code</td> <td>R1</td> <td>R2</td> </tr> </table>	8	4	4	Operation Code	R1	R2	R1	SPM									
8	4	4															
Operation Code	R1	R2															
<table border="1"> <tr> <td>8</td> <td>8</td> </tr> <tr> <td>Operation Code</td> <td>I</td> </tr> </table>	8	8	Operation Code	I	I	SVC											
8	8																
Operation Code	I																
(See notes 1,6, and 8)																	
RX	<table border="1"> <tr> <td>8</td> <td>4</td> <td>4</td> <td>4</td> <td>12</td> </tr> <tr> <td>Operation Code</td> <td>R1</td> <td>X2</td> <td>B2</td> <td>D2</td> </tr> </table>	8	4	4	4	12	Operation Code	R1	X2	B2	D2	R1,D2 (X2,B2) R1,S2 (X2)	All RX instructions				
8	4	4	4	12													
Operation Code	R1	X2	B2	D2													
(See notes 1-4, and 7)																	
RS	<table border="1"> <tr> <td>8</td> <td>4</td> <td>4</td> <td>4</td> <td>12</td> </tr> <tr> <td>Operation Code</td> <td>R1</td> <td>R3</td> <td>B2</td> <td>D2</td> </tr> </table>	8	4	4	4	12	Operation Code	R1	R3	B2	D2	R1,R3,D2 (B2) R1,R3,S2	BXH,BXLE,LM,STM				
	8	4	4	4	12												
Operation Code	R1	R3	B2	D2													
<table border="1"> <tr> <td>8</td> <td>4</td> <td>4</td> <td>4</td> <td>12</td> </tr> <tr> <td>Operation Code</td> <td>R1</td> <td>R3</td> <td>B2</td> <td>D2</td> </tr> </table>	8	4	4	4	12	Operation Code	R1	R3	B2	D2	R1,D2 (B2) R1,S2	All shift instructions					
8	4	4	4	12													
Operation Code	R1	R3	B2	D2													
(See notes 1-3, 7, and 8)																	
SI	<table border="1"> <tr> <td>8</td> <td>8</td> <td>4</td> <td>12</td> </tr> <tr> <td>Operation Code</td> <td>I2</td> <td>B1</td> <td>D1</td> </tr> </table>	8	8	4	12	Operation Code	I2	B1	D1	D1 (B1) ,I2 S1,I2	All SI instructions except LPSW,SSM,HIO,SIO,TIO,TCH,TS						
	8	8	4	12													
Operation Code	I2	B1	D1														
<table border="1"> <tr> <td>8</td> <td>8</td> <td>4</td> <td>4</td> <td>12</td> </tr> <tr> <td>Operation Code</td> <td>I2</td> <td>B1</td> <td>D1</td> </tr> </table>	8	8	4	4	12	Operation Code	I2	B1	D1	D1 (B1) S1	LPSW,SSM,HIO,SIO,TIO,TCH,TS						
8	8	4	4	12													
Operation Code	I2	B1	D1														
(See notes 2, 3, and 6-8)																	
SS	<table border="1"> <tr> <td>8</td> <td>4</td> <td>4</td> <td>4</td> <td>12</td> <td>4</td> <td>12</td> </tr> <tr> <td>Operation Code</td> <td>L1</td> <td>L2</td> <td>B1</td> <td>D1</td> <td>B2</td> <td>D2</td> </tr> </table>	8	4	4	4	12	4	12	Operation Code	L1	L2	B1	D1	B2	D2	D1 (L1,B1) ,D2 (L2,B2) S1 (L1) ,S2 (L2)	PACK,UNPK,MVO,AP,CP,DP,MP,SP,ZAP
	8	4	4	4	12	4	12										
Operation Code	L1	L2	B1	D1	B2	D2											
<table border="1"> <tr> <td>8</td> <td>8</td> <td>4</td> <td>12</td> <td>4</td> <td>12</td> </tr> <tr> <td>Operation Code</td> <td>L</td> <td>B1</td> <td>D1</td> <td>B2</td> <td>D2</td> </tr> </table>	8	8	4	12	4	12	Operation Code	L	B1	D1	B2	D2	D1 (L,B1) ,D2 (B2) S1 (L) ,S2	NC,OC,XC,CLC,MVC,MVN,MVZ,TR,TRT,ED,EDMK			
8	8	4	12	4	12												
Operation Code	L	B1	D1	B2	D2												
(See notes 2, 3, 5, and 7)																	

Notes for Appendix D:

1. R1, R2, and R3 are absolute expressions that specify general or floating-point registers. The general register numbers are 0 through 15; floating-point register numbers are 0, 2, 4, and 6.
2. D1 and D2 are absolute expressions that specify displacements. A value of 0 - 4095 may be specified.
3. B1 and B2 are absolute expressions that specify base registers. Register numbers are 0 - 15.
4. X2 is an absolute expression that specifies an index register. Register numbers are 0 - 15. If B2 is specified, X2 must not be omitted; and when indexing is not desired, X2 must be specified as 0.
5. L, L1, and L2 are absolute expressions that specify field lengths. An L expression can specify a value of 1 - 256. L1 and L2 expressions can specify a value of 1 - 16. In all cases, the assembled value will be one less than the specified value.
6. I and I2 are absolute expressions that provide immediate data. The value of the expression may be 0 - 255.
7. S1 and S2 are absolute or relocatable expressions that specify an address.
8. RR, RS, and SI instruction fields that are crossed out in the machine formats are not examined during instruction execution. The fields are not written in the symbolic operand, but are assembled as binary zeros.

APPENDIX E: HEXADECIMAL-DECIMAL NUMBER CONVERSION TABLE

The table in this appendix provides for direct conversion of decimal and hexadecimal numbers in these ranges:

Hexadecimal Decimal
 000 to FFF 0000 to 4095

For numbers outside the range of the table, add the following values to the table figures:

Hexadecimal Decimal
 1000 4096
 2000 8192
 3000 12288
 4000 16384
 5000 20480
 6000 24576
 7000 28672
 8000 32768
 9000 36864
 A000 40960
 B000 45056
 C000 49152
 D000 53248
 E000 57344
 F000 61440

x =	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
00x	0000	0001	0002	0003	0004	0005	0006	0007	0008	0009	0010	0011	0012	0013	0014	0015
01x	0016	0017	0018	0019	0020	0021	0022	0023	0024	0025	0026	0027	0028	0029	0030	0031
02x	0032	0033	0034	0035	0036	0037	0038	0039	0040	0041	0042	0043	0044	0045	0046	0047
03x	0048	0049	0050	0051	0052	0053	0054	0055	0056	0057	0058	0059	0060	0061	0062	0063
04x	0064	0065	0066	0067	0068	0069	0070	0071	0072	0073	0074	0075	0076	0077	0078	0079
05x	0080	0081	0082	0083	0084	0085	0086	0087	0088	0089	0090	0091	0092	0093	0094	0095
06x	0096	0097	0098	0099	0100	0101	0102	0103	0104	0105	0106	0107	0108	0109	0110	0111
07x	0112	0113	0114	0115	0116	0117	0118	0119	0120	0121	0122	0123	0124	0125	0126	0127
08x	0128	0129	0130	0131	0132	0133	0134	0135	0136	0137	0138	0139	0140	0141	0142	0143
09x	0144	0145	0146	0147	0148	0149	0150	0151	0152	0153	0154	0155	0156	0157	0158	0159
0Ax	0160	0161	0162	0163	0164	0165	0166	0167	0168	0169	0170	0171	0172	0173	0174	0175
0Bx	0176	0177	0178	0179	0180	0181	0182	0183	0184	0185	0186	0187	0188	0189	0190	0191
0Cx	0192	0193	0194	0195	0196	0197	0198	0199	0200	0201	0202	0203	0204	0205	0206	0207
0Dx	0208	0209	0210	0211	0212	0213	0214	0215	0216	0217	0218	0219	0220	0221	0222	0223
0Ex	0224	0225	0226	0227	0228	0229	0230	0231	0232	0233	0234	0235	0236	0237	0238	0239
0Fx	0240	0241	0242	0243	0244	0245	0246	0247	0248	0249	0250	0251	0252	0253	0254	0255
10x	0256	0257	0258	0259	0260	0261	0262	0263	0264	0265	0266	0267	0268	0269	0270	0271
11x	0272	0273	0274	0275	0276	0277	0278	0279	0280	0281	0282	0283	0284	0285	0286	0287
12x	0288	0289	0290	0291	0292	0293	0294	0295	0296	0297	0298	0299	0300	0301	0302	0303
13x	0304	0305	0306	0307	0308	0309	0310	0311	0312	0313	0314	0315	0316	0317	0318	0319
14x	0320	0321	0322	0323	0324	0325	0326	0327	0328	0329	0330	0331	0332	0333	0334	0335
15x	0336	0337	0338	0339	0340	0341	0342	0343	0344	0345	0346	0347	0348	0349	0350	0351
16x	0352	0353	0354	0355	0356	0357	0358	0359	0360	0361	0362	0363	0364	0365	0366	0367
17x	0368	0369	0370	0371	0372	0373	0374	0375	0376	0377	0378	0379	0380	0381	0382	0383
18x	0384	0385	0386	0387	0388	0389	0390	0391	0392	0393	0394	0395	0396	0397	0398	0399
19x	0400	0401	0402	0403	0404	0405	0406	0407	0408	0409	0410	0411	0412	0413	0414	0415
1Ax	0416	0417	0418	0419	0420	0421	0422	0423	0424	0425	0426	0427	0428	0429	0430	0431
1Bx	0432	0433	0434	0435	0436	0437	0438	0439	0440	0441	0442	0443	0444	0445	0446	0447
1Cx	0448	0449	0450	0451	0452	0453	0454	0455	0456	0457	0458	0459	0460	0461	0462	0463
1Dx	0464	0465	0466	0467	0468	0469	0470	0471	0472	0473	0474	0475	0476	0477	0478	0479
1Ex	0480	0481	0482	0483	0484	0485	0486	0487	0488	0489	0490	0491	0492	0493	0494	0495
1Fx	0496	0497	0498	0499	0500	0501	0502	0503	0504	0505	0506	0507	0508	0509	0510	0511

x =	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
20x	0512	0513	0514	0515	0516	0517	0518	0519	0520	0521	0522	0523	0524	0525	0526	0527
21x	0528	0529	0530	0531	0532	0533	0534	0535	0536	0537	0538	0539	0540	0541	0542	0543
22x	0544	0545	0546	0547	0548	0549	0550	0551	0552	0553	0554	0555	0556	0557	0558	0559
23x	0560	0561	0562	0563	0564	0565	0566	0567	0568	0569	0570	0571	0572	0573	0574	0575
24x	0576	0577	0578	0579	0580	0581	0582	0583	0584	0585	0586	0587	0588	0589	0590	0591
25x	0592	0593	0594	0595	0596	0597	0598	0599	0600	0601	0602	0603	0604	0605	0606	0607
26x	0608	0609	0610	0611	0612	0613	0614	0615	0616	0617	0618	0619	0620	0621	0622	0623
27x	0624	0625	0626	0627	0628	0629	0630	0631	0632	0633	0634	0635	0636	0637	0638	0639
28x	0640	0641	0642	0643	0644	0645	0646	0647	0648	0649	0650	0651	0652	0653	0654	0655
29x	0656	0657	0658	0659	0660	0661	0662	0663	0664	0665	0666	0667	0668	0669	0670	0671
2Ax	0672	0673	0674	0675	0676	0677	0678	0679	0680	0681	0682	0683	0684	0685	0686	0687
2Bx	0688	0689	0690	0691	0692	0693	0694	0695	0696	0697	0698	0699	0700	0701	0702	0703
2Cx	0704	0705	0706	0707	0708	0709	0710	0711	0712	0713	0714	0715	0716	0717	0718	0719
2Dx	0720	0721	0722	0723	0724	0725	0726	0727	0728	0729	0730	0731	0732	0733	0734	0735
2Ex	0736	0737	0738	0739	0740	0741	0742	0743	0744	0745	0746	0747	0748	0749	0750	0751
2Fx	0752	0753	0754	0755	0756	0757	0758	0759	0760	0761	0762	0763	0764	0765	0766	0767
30x	0768	0769	0770	0771	0772	0773	0774	0775	0776	0777	0778	0779	0780	0781	0782	0783
31x	0784	0785	0786	0787	0788	0789	0790	0791	0792	0793	0794	0795	0796	0797	0798	0799
32x	0800	0801	0802	0803	0804	0805	0806	0807	0808	0809	0810	0811	0812	0813	0814	0815
33x	0816	0817	0818	0819	0820	0821	0822	0823	0824	0825	0826	0827	0828	0829	0830	0831
34x	0832	0833	0834	0835	0836	0837	0838	0839	0840	0841	0842	0843	0844	0845	0846	0847
35x	0848	0849	0850	0851	0852	0853	0854	0855	0856	0857	0858	0859	0860	0861	0862	0863
36x	0864	0865	0866	0867	0868	0869	0870	0871	0872	0873	0874	0875	0876	0877	0878	0879
37x	0880	0881	0882	0883	0884	0885	0886	0887	0888	0889	0890	0891	0892	0893	0894	0895
38x	0896	0897	0898	0899	0900	0901	0902	0903	0904	0905	0906	0907	0908	0909	0910	0911
39x	0912	0913	0914	0915	0916	0917	0918	0919	0920	0921	0922	0923	0924	0925	0926	0927
3Ax	0928	0929	0930	0931	0932	0933	0934	0935	0936	0937	0938	0939	0940	0941	0942	0943
3Bx	0944	0945	0946	0947	0948	0949	0950	0951	0952	0953	0954	0955	0956	0957	0958	0959
3Cx	0960	0961	0962	0963	0964	0965	0966	0967	0968	0969	0970	0971	0972	0973	0974	0975
3Dx	0976	0977	0978	0979	0980	0981	0982	0983	0984	0985	0986	0987	0988	0989	0990	0991
3Ex	0992	0993	0994	0995	0996	0997	0998	0999	1000	1001	1002	1003	1004	1005	1006	1007
3Fx	1008	1009	1010	1011	1012	1013	1014	1015	1016	1017	1018	1019	1020	1021	1022	1023
40x	1024	1025	1026	1027	1028	1029	1030	1031	1032	1033	1034	1035	1036	1037	1038	1039
41x	1040	1041	1042	1043	1044	1045	1046	1047	1048	1049	1050	1051	1052	1053	1054	1055
42x	1056	1057	1058	1059	1060	1061	1062	1063	1064	1065	1066	1067	1068	1069	1070	1071
43x	1072	1073	1074	1075	1076	1077	1078	1079	1080	1081	1082	1083	1084	1085	1086	1087
44x	1088	1089	1090	1091	1092	1093	1094	1095	1096	1097	1098	1099	1100	1101	1102	1103
45x	1104	1105	1106	1107	1108	1109	1110	1111	1112	1113	1114	1115	1116	1117	1118	1119
46x	1120	1121	1122	1123	1124	1125	1126	1127	1128	1129	1130	1131	1132	1133	1134	1135
47x	1136	1137	1138	1139	1140	1141	1142	1143	1144	1145	1146	1147	1148	1149	1150	1151
48x	1152	1153	1154	1155	1156	1157	1158	1159	1160	1161	1162	1163	1164	1165	1166	1167
49x	1168	1169	1170	1171	1172	1173	1174	1175	1176	1177	1178	1179	1180	1181	1182	1183
4Ax	1184	1185	1186	1187	1188	1189	1190	1191	1192	1193	1194	1195	1196	1197	1198	1199
4Bx	1200	1201	1202	1203	1204	1205	1206	1207	1208	1209	1210	1211	1212	1213	1214	1215
4Cx	1216	1217	1218	1219	1220	1221	1222	1223	1224	1225	1226	1227	1228	1229	1230	1231
4Dx	1232	1233	1234	1235	1236	1237	1238	1239	1240	1241	1242	1243	1244	1245	1246	1247
4Ex	1248	1249	1250	1251	1252	1253	1254	1255	1256	1257	1258	1259	1260	1261	1262	1263
4Fx	1264	1265	1266	1267	1268	1269	1270	1271	1272	1273	1274	1275	1276	1277	1278	1279
50x	1280	1281	1282	1283	1284	1285	1286	1287	1288	1289	1290	1291	1292	1293	1294	1295
51x	1296	1297	1298	1299	1300	1301	1302	1303	1304	1305	1306	1307	1308	1309	1310	1311
52x	1312	1313	1314	1315	1316	1317	1318	1319	1320	1321	1322	1323	1324	1325	1326	1327
53x	1328	1329	1330	1331	1332	1333	1334	1335	1336	1337	1338	1339	1340	1341	1342	1343
54x	1344	1345	1346	1347	1348	1349	1350	1351	1352	1353	1354	1355	1356	1357	1358	1359
55x	1360	1361	1362	1363	1364	1365	1366	1367	1368	1369	1370	1371	1372	1373	1374	1375
56x	1376	1377	1378	1379	1380	1381	1382	1383	1384	1385	1386	1387	1388	1389	1390	1391
57x	1392	1393	1394	1395	1396	1397	1398	1399	1400	1401	1402	1403	1404	1405	1406	1407
58x	1408	1409	1410	1411	1412	1413	1414	1415	1416	1417	1418	1419	1420	1421	1422	1423
59x	1424	1425	1426	1427	1428	1429	1430	1431	1432	1433	1434	1435	1436	1437	1438	1439
5Ax	1440	1441	1442	1443	1444	1445	1446	1447	1448	1449	1450	1451	1452	1453	1454	1455
5Bx	1456	1457	1458	1459	1460	1461	1462	1463	1464	1465	1466	1467	1468	1469	1470	1471
5Cx	1472	1473	1474	1475	1476	1477	1478	1479	1480	1481	1482	1483	1484	1485	1486	1487
5Dx	1488	1489	1490	1491	1492	1493	1494	1495	1496	1497	1498	1499	1500	1501	1502	1503
5Ex	1504	1505	1506	1507	1508	1509	1510	1511	1512	1513	1514	1515	1516	1517	1518	1519
5Fx	1520	1521	1522	1523	1524	1525	1526	1527	1528	1529	1530	1531	1532	1533	1534	1535

x =	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
60x	1536	1537	1538	1539	1540	1541	1542	1543	1544	1545	1546	1547	1548	1549	1550	1551
61x	1552	1553	1554	1555	1556	1557	1558	1559	1560	1561	1562	1563	1564	1565	1566	1567
62x	1568	1569	1570	1571	1572	1573	1574	1575	1576	1577	1578	1579	1580	1581	1582	1583
63x	1584	1585	1586	1587	1588	1589	1590	1591	1592	1593	1594	1595	1596	1597	1598	1599
64x	1600	1601	1602	1603	1604	1605	1606	1607	1608	1609	1610	1611	1612	1613	1614	1615
65x	1616	1617	1618	1619	1620	1621	1622	1623	1624	1625	1626	1627	1628	1629	1630	1631
66x	1632	1633	1634	1635	1636	1637	1638	1639	1640	1641	1642	1643	1644	1645	1646	1647
67x	1648	1649	1650	1651	1652	1653	1654	1655	1656	1657	1658	1659	1660	1661	1662	1663
68x	1664	1665	1666	1667	1668	1669	1670	1671	1672	1673	1674	1675	1676	1677	1678	1679
69x	1680	1681	1682	1683	1684	1685	1686	1687	1688	1689	1690	1691	1692	1693	1694	1695
6Ax	1696	1697	1698	1699	1700	1701	1702	1703	1704	1705	1706	1707	1708	1709	1710	1711
6Bx	1712	1713	1714	1715	1716	1717	1718	1719	1720	1721	1722	1723	1724	1725	1726	1727
6Cx	1728	1729	1730	1731	1732	1733	1734	1735	1736	1737	1738	1739	1740	1741	1742	1743
6Dx	1744	1745	1746	1747	1748	1749	1750	1751	1752	1753	1754	1755	1756	1757	1758	1759
6Ex	1760	1761	1762	1763	1764	1765	1766	1767	1768	1769	1770	1771	1772	1773	1774	1775
6Fx	1776	1777	1778	1779	1780	1781	1782	1783	1784	1785	1786	1787	1788	1789	1790	1791
70x	1792	1793	1794	1795	1796	1797	1798	1799	1800	1801	1802	1803	1804	1805	1806	1807
71x	1808	1809	1810	1811	1812	1813	1814	1815	1816	1817	1818	1819	1820	1821	1822	1823
72x	1824	1825	1826	1827	1828	1829	1830	1831	1832	1833	1834	1835	1836	1837	1838	1839
73x	1840	1841	1842	1843	1844	1845	1846	1847	1848	1849	1850	1851	1852	1853	1854	1855
74x	1856	1857	1858	1859	1860	1861	1862	1863	1864	1865	1866	1867	1868	1869	1870	1871
75x	1872	1873	1874	1875	1876	1877	1878	1879	1880	1881	1882	1883	1884	1885	1886	1887
76x	1888	1889	1890	1891	1892	1893	1894	1895	1896	1897	1898	1899	1900	1901	1902	1903
77x	1904	1905	1906	1907	1908	1909	1910	1911	1912	1913	1914	1915	1916	1917	1918	1919
78x	1920	1921	1922	1923	1924	1925	1926	1927	1928	1929	1930	1931	1932	1933	1934	1935
79x	1936	1937	1938	1939	1940	1941	1942	1943	1944	1945	1946	1947	1948	1949	1950	1951
7Ax	1952	1953	1954	1955	1956	1957	1958	1959	1960	1961	1962	1963	1964	1965	1966	1967
7Bx	1968	1969	1970	1971	1972	1973	1974	1975	1976	1977	1978	1979	1980	1981	1982	1983
7Cx	1984	1985	1986	1987	1988	1989	1990	1991	1992	1993	1994	1995	1996	1997	1998	1999
7Dx	2000	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015
7Ex	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
7Fx	2032	2033	2034	2035	2036	2037	2038	2039	2040	2041	2042	2043	2044	2045	2046	2047
80x	2048	2049	2050	2051	2052	2053	2054	2055	2056	2057	2058	2059	2060	2061	2062	2063
81x	2064	2065	2066	2067	2068	2069	2070	2071	2072	2073	2074	2075	2076	2077	2078	2079
82x	2080	2081	2082	2083	2084	2085	2086	2087	2088	2089	2090	2091	2092	2093	2094	2095
83x	2096	2097	2098	2099	2100	2101	2102	2103	2104	2105	2106	2107	2108	2109	2110	2111
84x	2112	2113	2114	2115	2116	2117	2118	2119	2120	2121	2122	2123	2124	2125	2126	2127
85x	2128	2129	2130	2131	2132	2133	2134	2135	2136	2137	2138	2139	2140	2141	2142	2143
86x	2144	2145	2146	2147	2148	2149	2150	2151	2152	2153	2154	2155	2156	2157	2158	2159
87x	2160	2161	2162	2163	2164	2165	2166	2167	2168	2169	2170	2171	2172	2173	2174	2175
88x	2176	2177	2178	2179	2180	2181	2182	2183	2184	2185	2186	2187	2188	2189	2190	2191
89x	2192	2193	2194	2195	2196	2197	2198	2199	2200	2201	2202	2203	2204	2205	2206	2207
8Ax	2208	2209	2210	2211	2212	2213	2214	2215	2216	2217	2218	2219	2220	2221	2222	2223
8Bx	2224	2225	2226	2227	2228	2229	2230	2231	2232	2233	2234	2235	2236	2237	2238	2239
8Cx	2240	2241	2242	2243	2244	2245	2246	2247	2248	2249	2250	2251	2252	2253	2254	2255
8Dx	2256	2257	2258	2259	2260	2261	2262	2263	2264	2265	2266	2267	2268	2269	2270	2271
8Ex	2272	2273	2274	2275	2276	2277	2278	2279	2280	2281	2282	2283	2284	2285	2286	2287
8Fx	2288	2289	2290	2291	2292	2293	2294	2295	2296	2297	2298	2299	2300	2301	2302	2303
90x	2304	2305	2306	2307	2308	2309	2310	2311	2312	2313	2314	2315	2316	2317	2318	2319
91x	2320	2321	2322	2323	2324	2325	2326	2327	2328	2329	2330	2331	2332	2333	2334	2335
92x	2336	2337	2338	2339	2340	2341	2342	2343	2344	2345	2346	2347	2348	2349	2350	2351
93x	2352	2353	2354	2355	2356	2357	2358	2359	2360	2361	2362	2363	2364	2365	2366	2367
94x	2368	2369	2370	2371	2372	2373	2374	2375	2376	2377	2378	2379	2380	2381	2382	2383
95x	2384	2385	2386	2387	2388	2389	2390	2391	2392	2393	2394	2395	2396	2397	2398	2399
96x	2400	2401	2402	2403	2404	2405	2406	2407	2408	2409	2410	2411	2412	2413	2414	2415
97x	2416	2417	2418	2419	2420	2421	2422	2423	2424	2425	2426	2427	2428	2429	2430	2431
98x	2432	2433	2434	2435	2436	2437	2438	2439	2440	2441	2442	2443	2444	2445	2446	2447
99x	2448	2449	2450	2451	2452	2453	2454	2455	2456	2457	2458	2459	2460	2461	2462	2463
9Ax	2464	2465	2466	2467	2468	2469	2470	2471	2472	2473	2474	2475	2476	2477	2478	2479
9Bx	2480	2481	2482	2483	2484	2485	2486	2487	2488	2489	2490	2491	2492	2493	2494	2495
9Cx	2496	2497	2498	2499	2500	2501	2502	2503	2504	2505	2506	2507	2508	2509	2510	2511
9Dx	2512	2513	2514	2515	2516	2517	2518	2519	2520	2521	2522	2523	2524	2525	2526	2527
9Ex	2528	2529	2530	2531	2532	2533	2534	2535	2536	2537	2538	2539	2540	2541	2542	2543
9Fx	2544	2545	2546	2547	2548	2549	2550	2551	2552	2553	2554	2555	2556	2557	2558	2559

x =	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
A0x	2560	2561	2562	2563	2564	2565	2566	2567	2568	2569	2570	2571	2572	2573	2574	2575
A1x	2576	2577	2578	2579	2580	2581	2582	2583	2584	2585	2586	2587	2588	2589	2590	2591
A2x	2592	2593	2594	2595	2596	2597	2598	2599	2600	2601	2602	2603	2604	2605	2606	2607
A3x	2608	2609	2610	2611	2612	2613	2614	2615	2616	2617	2618	2619	2620	2621	2622	2623
A4x	2624	2625	2626	2627	2628	2629	2630	2631	2632	2633	2634	2635	2636	2637	2638	2639
A5x	2640	2641	2642	2643	2644	2645	2646	2647	2648	2649	2650	2651	2652	2653	2654	2655
A6x	2656	2657	2658	2659	2660	2661	2662	2663	2664	2665	2666	2667	2668	2669	2670	2671
A7x	2672	2673	2674	2675	2676	2677	2678	2679	2680	2681	2682	2683	2684	2685	2686	2687
A8x	2688	2689	2690	2691	2692	2693	2694	2695	2696	2697	2698	2699	2700	2701	2702	2703
A9x	2704	2705	2706	2707	2708	2709	2710	2711	2712	2713	2714	2715	2716	2717	2718	2719
AAx	2720	2721	2722	2723	2724	2725	2726	2727	2728	2729	2730	2731	2732	2733	2734	2735
ABx	2736	2737	2738	2739	2740	2741	2742	2743	2744	2745	2746	2747	2748	2749	2750	2751
ACx	2752	2753	2754	2755	2756	2757	2758	2759	2760	2761	2762	2763	2764	2765	2766	2767
ADx	2768	2769	2770	2771	2772	2773	2774	2775	2776	2777	2778	2779	2780	2781	2782	2783
AEx	2784	2785	2786	2787	2788	2789	2790	2791	2792	2793	2794	2795	2796	2797	2798	2799
AFx	2800	2801	2802	2803	2804	2805	2806	2807	2808	2809	2810	2811	2812	2813	2814	2815
B0x	2816	2817	2818	2819	2820	2821	2822	2823	2824	2825	2826	2827	2828	2829	2830	2831
B1x	2832	2833	2834	2835	2836	2837	2838	2839	2840	2841	2842	2843	2844	2845	2846	2847
B2x	2848	2849	2850	2851	2852	2853	2854	2855	2856	2857	2858	2859	2860	2861	2862	2863
B3x	2864	2865	2866	2867	2868	2869	2870	2871	2872	2873	2874	2875	2876	2877	2878	2879
B4x	2880	2881	2882	2883	2884	2885	2886	2887	2888	2889	2890	2891	2892	2893	2894	2895
B5x	2896	2897	2898	2899	2900	2901	2902	2903	2904	2905	2906	2907	2908	2909	2910	2911
B6x	2912	2913	2914	2915	2916	2917	2918	2919	2920	2921	2922	2923	2924	2925	2926	2927
B7x	2928	2929	2930	2931	2932	2933	2934	2935	2936	2937	2938	2939	2940	2941	2942	2943
B8x	2944	2945	2946	2947	2948	2949	2950	2951	2952	2953	2954	2955	2956	2957	2958	2959
B9x	2960	2961	2962	2963	2964	2965	2966	2967	2968	2969	2970	2971	2972	2973	2974	2975
BAx	2976	2977	2978	2979	2980	2981	2982	2983	2984	2985	2986	2987	2988	2989	2990	2991
BBx	2992	2993	2994	2995	2996	2997	2998	2999	3000	3001	3002	3003	3004	3005	3006	3007
BCx	3008	3009	3010	3011	3012	3013	3014	3015	3016	3017	3018	3019	3020	3021	3022	3023
BDx	3024	3025	3026	3027	3028	3029	3030	3031	3032	3033	3034	3035	3036	3037	3038	3039
BEx	3040	3041	3042	3043	3044	3045	3046	3047	3048	3049	3050	3051	3052	3053	3054	3055
BFx	3056	3057	3058	3059	3060	3061	3062	3063	3064	3065	3066	3067	3068	3069	3070	3071
C0x	3072	3073	3074	3075	3076	3077	3078	3079	3080	3081	3082	3083	3084	3085	3086	3087
C1x	3088	3089	3090	3091	3092	3093	3094	3095	3096	3097	3098	3099	3100	3101	3102	3103
C2x	3104	3105	3106	3107	3108	3109	3110	3111	3112	3113	3114	3115	3116	3117	3118	3119
C3x	3120	3121	3122	3123	3124	3125	3126	3127	3128	3129	3130	3131	3132	3133	3134	3135
C4x	3136	3137	3138	3139	3140	3141	3142	3143	3144	3145	3146	3147	3148	3149	3150	3151
C5x	3152	3153	3154	3155	3156	3157	3158	3159	3160	3161	3162	3163	3164	3165	3166	3167
C6x	3168	3169	3170	3171	3172	3173	3174	3175	3176	3177	3178	3179	3180	3181	3182	3183
C7x	3184	3185	3186	3187	3188	3189	3190	3191	3192	3193	3194	3195	3196	3197	3198	3199
C8x	3200	3201	3202	3203	3204	3205	3206	3207	3208	3209	3210	3211	3212	3213	3214	3215
C9x	3216	3217	3218	3219	3220	3221	3222	3223	3224	3225	3226	3227	3228	3229	3230	3231
CAx	3232	3233	3234	3235	3236	3237	3238	3239	3240	3241	3242	3243	3244	3245	3246	3247
CBx	3248	3249	3250	3251	3252	3253	3254	3255	3256	3257	3258	3259	3260	3261	3262	3263
CCx	3264	3265	3266	3267	3268	3269	3270	3271	3272	3273	3274	3275	3276	3277	3278	3279
CDx	3280	3281	3282	3283	3284	3285	3286	3287	3288	3289	3290	3291	3292	3293	3294	3295
CEx	3296	3297	3298	3299	3300	3301	3302	3303	3304	3305	3306	3307	3308	3309	3310	3311
CFx	3312	3313	3314	3315	3316	3317	3318	3319	3320	3321	3322	3323	3324	3325	3326	3327
D0x	3328	3329	3330	3331	3332	3333	3334	3335	3336	3337	3338	3339	3340	3341	3342	3343
D1x	3344	3345	3346	3347	3348	3349	3350	3351	3352	3353	3354	3355	3356	3357	3358	3359
D2x	3360	3361	3362	3363	3364	3365	3366	3367	3368	3369	3370	3371	3372	3373	3374	3375
D3x	3376	3377	3378	3379	3380	3381	3382	3383	3384	3385	3386	3387	3388	3389	3390	3391
D4x	3392	3393	3394	3395	3396	3397	3398	3399	3400	3401	3402	3403	3404	3405	3406	3407
D5x	3408	3409	3410	3411	3412	3413	3414	3415	3416	3417	3418	3419	3420	3421	3422	3423
D6x	3424	3425	3426	3427	3428	3429	3430	3431	3432	3433	3434	3435	3436	3437	3438	3439
D7x	3440	3441	3442	3443	3444	3445	3446	3447	3448	3449	3450	3451	3452	3453	3454	3455
D8x	3456	3457	3458	3459	3460	3461	3462	3463	3464	3465	3466	3467	3468	3469	3470	3471
D9x	3472	3473	3474	3475	3476	3477	3478	3479	3480	3481	3482	3483	3484	3485	3486	3487
DAx	3488	3489	3490	3491	3492	3493	3494	3495	3496	3497	3498	3499	3500	3501	3502	3503
DBx	3504	3505	3506	3507	3508	3509	3510	3511	3512	3513	3514	3515	3516	3517	3518	3519
DCx	3520	3521	3522	3523	3524	3525	3526	3527	3528	3529	3530	3531	3532	3533	3534	3535
DDx	3536	3537	3538	3539	3540	3541	3542	3543	3544	3545	3546	3547	3548	3549	3550	3551
DEx	3552	3553	3554	3555	3556	3557	3558	3559	3560	3561	3562	3563	3564	3565	3566	3567
DFx	3568	3569	3570	3571	3572	3573	3574	3575	3576	3577	3578	3579	3580	3581	3582	3583

x =	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
E0x	3584	3585	3586	3587	3588	3589	3590	3591	3592	3593	3594	3595	3596	3597	3598	3599
E1x	3600	3601	3602	3603	3604	3605	3606	3607	3608	3609	3610	3611	3612	3613	3614	3615
E2x	3616	3617	3618	3619	3620	3621	3622	3623	3624	3625	3626	3627	3628	3629	3630	3631
E3x	3632	3633	3634	3635	3636	3637	3638	3639	3640	3641	3642	3643	3644	3645	3646	3647
E4x	3648	3649	3650	3651	3652	3653	3654	3655	3656	3657	3658	3659	3660	3661	3662	3663
E5x	3664	3665	3666	3667	3668	3669	3670	3671	3672	3673	3674	3675	3676	3677	3678	3679
E6x	3680	3681	3682	3683	3684	3685	3686	3687	3688	3689	3690	3691	3692	3693	3694	3695
E7x	3696	3697	3698	3699	3700	3701	3702	3703	3704	3705	3706	3707	3708	3709	3710	3711
E8x	3712	3713	3714	3715	3716	3717	3718	3719	3720	3721	3722	3723	3724	3725	3726	3727
E9x	3728	3729	3730	3731	3732	3733	3734	3735	3736	3737	3738	3739	3740	3741	3742	3743
EAx	3744	3745	3746	3747	3748	3749	3750	3751	3752	3753	3754	3755	3756	3757	3758	3759
EBx	3760	3761	3762	3763	3764	3765	3766	3767	3768	3769	3770	3771	3772	3773	3774	3775
ECx	3776	3777	3778	3779	3780	3781	3782	3783	3784	3785	3786	3787	3788	3789	3790	3791
EDx	3792	3793	3794	3795	3796	3797	3798	3799	3800	3801	3802	3803	3804	3805	3806	3807
EEx	3808	3809	3810	3811	3812	3813	3814	3815	3816	3817	3818	3819	3820	3821	3822	3823
EFx	3824	3825	3826	3827	3828	3829	3830	3831	3832	3833	3834	3835	3836	3837	3838	3839
F0x	3840	3841	3842	3843	3844	3845	3846	3847	3848	3849	3850	3851	3852	3853	3854	3855
F1x	3856	3857	3858	3859	3860	3861	3862	3863	3864	3865	3866	3867	3868	3869	3870	3871
F2x	3872	3873	3874	3875	3876	3877	3878	3879	3880	3881	3882	3883	3884	3885	3886	3887
F3x	3888	3889	3890	3891	3892	3893	3894	3895	3896	3897	3898	3899	3900	3901	3902	3903
F4x	3904	3905	3906	3907	3908	3909	3910	3911	3912	3913	3914	3915	3916	3917	3918	3919
F5x	3920	3921	3922	3923	3924	3925	3926	3927	3928	3929	3930	3931	3932	3933	3934	3935
F6x	3936	3937	3938	3939	3940	3941	3942	3943	3944	3945	3946	3947	3948	3949	3950	3951
F7x	3952	3953	3954	3955	3956	3957	3958	3959	3960	3961	3962	3963	3964	3965	3966	3967
F8x	3968	3969	3970	3971	3972	3973	3974	3975	3976	3977	3978	3979	3980	3981	3982	3983
F9x	3984	3985	3986	3987	3988	3989	3990	3991	3992	3993	3994	3995	3996	3997	3998	3999
FAx	4000	4001	4002	4003	4004	4005	4006	4007	4008	4009	4010	4011	4012	4013	4014	4015
FBx	4016	4017	4018	4019	4020	4021	4022	4023	4024	4025	4026	4027	4028	4029	4030	4031
FCx	4032	4033	4034	4035	4036	4037	4038	4039	4040	4041	4042	4043	4044	4045	4046	4047
FDx	4048	4049	4050	4051	4052	4053	4054	4055	4056	4057	4058	4059	4060	4061	4062	4063
FEx	4064	4065	4066	4067	4068	4069	4070	4071	4072	4073	4074	4075	4076	4077	4078	4079
FFx	4080	4081	4082	4083	4084	4085	4086	4087	4088	4089	4090	4091	4092	4093	4094	4095

TYPE	IMPLIED LENGTH (BYTES)	ALIGNMENT	LENGTH MODIFIER RANGE	SPECIFIED BY	NUMBER OF CONSTANTS PER OPERAND	RANGE FOR EXPONENTS	RANGE FOR SCALE	TRUNCATION/PADDING SIDE
C	as needed	byte	.1 to 256 (1)	characters	one			right
X	as needed	byte	.1 to 256 (1)	hexadecimal digits	one			left
B	as needed	byte	.1 to 256	binary digits	one			left
F	4	word	.1 to 8	decimal digits	multiple	-85 to +75	-187 to +346	left
H	2	half word	.1 to 8	decimal digits	multiple	-85 to +75	-187 to +346	left
E	4	word	.1 to 8	decimal digits	multiple	-85 to +75	0 to 2L-2 (2)	right
D	8	double word	.1 to 8	decimal digits	multiple	-85 to +75	0 to 2L-2 (2)	right
P	as needed	byte	.1 to 16	decimal digits	multiple			left
Z	as needed	byte	.1 to 16	decimal digits	multiple			left
A	4	word	.1 to 4 (3)	any expression	multiple			left
V	4	word	3 or 4	relocatable symbol	multiple			left
S	2	half word	2 only	one absolute or relocatable expression or two absolute expressions: exp (exp)	multiple			
Y	2	half word	.1 to 2 (3)	any expression	multiple			left

(1) In a DS assembler instruction C and X type constants may have length specification to 65535.

(2) L is length of constant. Negative scaling is not permitted.

(3) Bit length specification permitted with absolute expressions only. Relocatable A-type constants, 3 or 4 bytes only; relocatable Y-type constants, 2 bytes only.

APPENDIX G: MACRO LANGUAGE SUMMARY

The five charts in this appendix summarize the macro language described in Part II of this publication. The charts are:

1. Chart 1 describes the name and operand fields of each statement.
2. Chart 2 indicates which macro language elements may be used in the name and operand fields of each statement.
3. Chart 3 is a summary of the expressions that may be used in macro language statements.
4. Chart 4 is a summary of the attributes that may be used in each expression.
5. Chart 5 is a summary of the variable symbols that may be used in each expression.

Chart 1. Statements

Instruction	Name Field	Operand Field
AGO	A sequence symbol or blank	A sequence symbol
AIF	A sequence symbol or blank	A logical expression enclosed in parentheses, immediately followed by a sequence symbol
ANOP	A sequence symbol	Blank
COPY	A sequence symbol or blank	A symbol
GBLA,GBLB,GBLC	Blank	One or more variable symbols that are to be used as SET symbols, separated by commas ³
LCLA,LCLB,LCLC	Blank	One or more variable symbols that are to be used as SET symbols, separated by commas ³
MACRO ¹	Blank	Blank
MEND ¹	A sequence symbol or blank	Blank
MEXIT ¹	A sequence symbol or blank	Blank
MNOTE ¹	A sequence symbol or blank	A severity code, followed by a comma, followed by any combination of characters enclosed in quotation marks
SETA	A SETA symbol	An arithmetic expression
SETB	A SETB symbol	A 0 or a 1, or a logical expression enclosed in parentheses
SETC	A SETC symbol	A type attribute, a character expression, a substring notation, or a concatenation of character expressions and substring notations
Model Statement (any assembler language mnemonic operation code, except COPY, END, ICTL, ISEQ, PRINT, and START) ⁵	A symbol, a variable symbol, a sequence symbol, or blank, or a concatenation of variable symbols and other characters that is equivalent to a symbol	Any combination of characters (including variable symbols)
Prototype Statement	A symbolic parameter or blank	Zero or more operands that are symbolic parameters, separated by commas, followed by zero or more operands (separated by commas) of the form symbolic parameter, equal sign, optional standard value

(Continued)

Chart 1. Statements (Continued)

Instruction	Name Field	Operand Field
Macro-Instruction Statement	A symbol, a variable symbol, a sequence symbol, or blank, or a concatenation of variable symbols and other characters that is equivalent to a symbol ²	Zero or more positional operands separated by commas, followed by zero or more keyword operands (separated by commas) of the form keyword, equal sign, value ²
Assembler Language Statement ^{4,5}	A symbol, a variable symbol, a sequence symbol, or blank, or a concatenation of variable symbols and other characters that is equivalent to a symbol	Any combination of characters (including variable symbols)

¹ May only be used as part of a macro-definition.
² Variable symbols appearing in a macro-instruction are replaced by their values before the macro-instruction is processed.
³ SET symbols may be defined as subscripted SET symbols.
⁴ Variable symbols may not be used to generate the following mnemonic operation codes, nor may variable symbols be used in the name and operand fields of these instructions: COPY, END, ICTL, ISEQ, PRINT, REPRO, and START. Variable symbols may not be used to generate a macro-instruction mnemonic operation code.
⁵ The line following a REPRO statement may not contain variable symbols.

Chart 2. Macro Language Elements

Statement	Variable Symbols							System Variable Symbols			Attributes					Sequence Symbol		
	Symbolic Parameter	Global SET Symbols			Local SET Symbols			§SYSNDX	§SYSECT	§SYSLIST	Type	Length	Scaling	Integer	Count		Number	
MACRO																		
Prototype Statement	Name Operand																	
GBLA		Operand																
GBLB			Operand															
GBLC				Operand														
LCLA					Operand													
LCLB						Operand												
LCLC							Operand											
Model Statement	Name Operation Operand	Name Operation Operand	Name Operation Operand	Name Operation Operand	Name Operation Operand	Name Operation Operand	Name Operation Operand	Name Operation Operand	Name Operation Operand	Name Operation Operand							Name	
COPY																		Name
Inner Macro ¹	Name Operand	Name Operand	Name Operand	Name Operand	Name Operand	Name Operand	Name Operand	Name Operand	Name Operand	Name Operand								Name
SETA	Operand ²	Operand	Operand ³	Operand ³	Operand	Operand ³	Operand ²	Operand		Operand ²		Operand	Operand	Operand	Operand	Operand		
SETB	Operand ⁴	Operand ⁵	Name Operand	Operand ⁶	Operand ⁶	Name Operand	Operand ⁶	Operand ⁶	Operand ⁶	Operand ⁶	Operand ⁴	Operand ⁵	Operand ⁵	Operand ⁵	Operand ⁵	Operand ⁵	Operand ⁵	
SETC	Operand	Operand ⁷	Operand ⁶	Name Operand	Operand ⁷	Operand ⁶	Name Operand	Operand	Operand	Operand	Operand							
AIF	Operand ⁸	Operand ⁸	Operand	Operand ⁸	Operand ⁸	Operand	Operand ⁸	Operand ⁸	Operand ⁸	Operand ⁸	Operand ⁸	Operand ⁸	Operand ⁸	Operand ⁸	Operand ⁸	Operand ⁸	Operand ⁸	Name Operand
AGO																		Name Operand
ANOP																		Name
MEXIT																		Name
MNOTE	Operand	Operand	Operand	Operand	Operand	Operand	Operand	Operand	Operand	Operand								Name
MEND																		Name
Outer Macro ¹		Name Operand	Name Operand	Name Operand	Name Operand	Name Operand	Name Operand	Name Operand	Name Operand	Name Operand								Name
Assembler Language Statement		Name Operation Operand	Name Operation Operand	Name Operation Operand	Name Operation Operand	Name Operation Operand	Name Operation Operand	Name Operation Operand	Name Operation Operand	Name Operation Operand								Name

¹ Variable symbols in macro-instructions are replaced by their values before processing.
² Only if value is self-defining term.
³ Converted to arithmetic +1 or +0.
⁴ Only in character relations.
⁵ Only in arithmetic relations.
⁶ Only in arithmetic or character relations.
⁷ Converted to unsigned number.
⁸ Converted to character 1 or 0.

Chart 3. Expressions

Expression	Arithmetic Expressions	Character Expressions	Logical Expressions
May contain	<ol style="list-style-type: none"> 1. Self-defining terms 2. Length, scaling, integer, count, and number attributes 3. SETA and SETB symbols 4. SETC symbols whose value is 1-8 decimal digits 5. Symbolic parameters if the corresponding operand is a self-defining term 6. &SYSLIST (n) if the corresponding operand is a self-defining term 7. &SYSLIST (n,m) if the corresponding operand is a self-defining term 8. &SYSNDX 	<ol style="list-style-type: none"> 1. Any combination of characters enclosed in quotation marks 2. Any variable symbol enclosed in quotation marks 3. A concatenation of variable symbols and other characters enclosed in quotation marks 4. A request for a type attribute. 	<ol style="list-style-type: none"> 1. SETB symbols 2. Arithmetic relations¹ 3. Character relations²
Operators are	+, -, *, and / parentheses permitted	concatenation, with a period (.)	AND, OR, and NOT parentheses permitted
Range of values	-2 ³¹ to +2 ³¹ -1	0 through 255 characters	0 (false) or 1 (true)
May be used in	<ol style="list-style-type: none"> 1. SETA operands 2. Arithmetic relations 3. Subscripted SET symbols 4. &SYSLIST 5. Substring notation 6. Sublist notation 	<ol style="list-style-type: none"> 1. SETC operands³ 2. Character relations² 	<ol style="list-style-type: none"> 1. SETB operands 2. AIF operands
<p>¹ An arithmetic relation consists of two arithmetic expressions related by the operators GT, LT, EQ, NE, GE, or LE.</p> <p>² A character relation consists of two character expressions related by the operator GT, LT, EQ, NE, GE, or LE. The type attribute notation and the substring notation may also be used in character relations. The maximum size of the character expressions that can be compared is 255 characters. If the two character expressions are of unequal size, then the smaller one will always compare less than the larger.</p> <p>³ Maximum of eight characters will be assigned.</p>			

Chart 4. Attributes

Attribute	Notation	May be used with:	May be used only if type attribute is:	May be used in
Type	T'	Symbols outside macro-definitions; symbolic parameters, &SYSLIST (n) , and &SYSLIST (n,m) inside macro-definitions	(May always be used)	1. SETC operand fields 2. Character relations
Length	L'	Symbols outside macro-definitions; symbolic parameters, &SYSLIST (n) , and &SYSLIST (n,m) inside macro-definitions	Any letter except M,N,O,T, and U	Arithmetic expressions
Scaling	S'	Symbols outside macro-definitions; symbolic parameters, &SYSLIST (n) , and &SYSLIST (n,m) inside macro-definitions	H,F,G,D,E,K,P, and Z	Arithmetic expressions
Integer	I'	Symbols outside macro-definitions; symbolic parameters, &SYSLIST (n) , and &SYSLIST (n,m) inside macro-definitions	H,F,G,D,E,K,P, and Z	Arithmetic expressions
Count	K'	Symbolic parameters corresponding to macro-instruction operands, &SYSLIST (n) , and &SYSLIST (n,m) inside macro-definitions	Any letter	Arithmetic expressions
Number	N'	Symbolic parameters, &SYSLIST, and &SYSLIST (n) inside macro-definitions	Any letter	Arithmetic expressions

Chart 5. Variable Symbols

Variable symbol	Defined by:	Initialized, or set to:	Value changed by:	May be used in:
Symbolic ¹ parameter	Prototype statement	Corresponding macro-instruction operand	(Constant throughout definition)	1. Arithmetic expressions if operand is self-defining term 2. Character expressions
SETA	LCLA or GBLA instruction	0	SETA instruction	1. Arithmetic expressions 2. Character expressions
SETB	LCLB or GBLB instruction	0	SETB instruction	1. Arithmetic expressions 2. Character expressions 3. Logical expressions
SETC	LCLC or GBLC instruction	Null character value	SETC instruction	1. Arithmetic expressions if value is self-defining term 2. Character expressions
&SYSNDX ¹	The assembler	Macro-instruction index	(Constant throughout definition; unique for each macro-instruction)	1. Arithmetic expressions 2. Character expressions
&SYSECT ¹	The assembler	Control section in which macro-instruction appears	(Constant throughout definition; set by CSECT, DSECT, and START)	Character expressions
&SYSLIST ¹	The assembler	Not applicable	Not applicable	N*&SYSLIST in arithmetic expressions
&SYSLIST (n) ¹ &SYSLIST (n,m) ¹	The assembler	Corresponding macro-instruction operand	(Constant throughout definition)	1. Arithmetic expressions if operand is self-defining term 2. Character expressions
¹ May only be used in macro-definitions.				

Operating System/360 assemblers are designed to enhance the language features available to the user, to maximize the size of programs that may be processed, and to minimize assembly time for typical programs. To properly accomplish this, overall processing limitations have been set. These are dependent upon the size of main storage available to the assembler. The following discussions, concerning significant tables of the assembler, are presented here so that the user may be aware of the generality, flexibility, and sizeable capacity of Operating System/360 assemblers.

THE SYMBOL TABLE

The assembler maintains one table of not more than 64,100 bytes to hold all symbols, variable symbols, and macro operation codes used by the program except sequence symbols, local SET symbols used within macro definitions, and symbolic parameters. For each unique symbol, an entry is made in this table, consisting of the symbol itself and control information. The type of entries that are made in this table and the size of each entry are listed below.

<u>Type</u>	<u>Size of entry</u>
Symbols	6 bytes average plus the symbol* (9 bytes maximum plus the symbol*)
Macro instruction operation code (outer and inner used in the program)	10 bytes plus the operation code*
Sequence Symbols (outside macro definitions)	6 bytes plus the symbol*
Local Set Symbols (outside macro definitions)	3 bytes nonsubscripted, 4 bytes subscripted, plus the symbol*
Global Set Symbols (inside and outside macro definitions)	6 bytes plus the symbol*

* one byte for each character

Assuming an average of six characters per symbol and a reasonable mix of the above types, the number of entries that may be made in the symbol table before secondary storage is used is summarized in Table H-1.

Table H-1. Symbol Table Capacity

Main Storage Available To Assembler	Main Storage Symbol Table Size	Total Symbol Table Size	Number of Unique Macro Instructions (Inside and Outside) (1)	Number of Unique Global Symbols (2)	Number of Symbols In Main Storage Table (2)	Total Number of Symbols In Symbol Table
12000	2100	64100	25	25	118	>5000
44000	30000	64100	100	100	2200	>5000
80000	64100	64100	100	100	>5000	>5000

- (1) Assuming average of five characters per macro operation code
- (2) Assuming average of six characters per symbol

The values shown in columns 4,5, and 6 are examples only. There can be a larger number of entries for a given type if the number of entries of another type is smaller.

Symbol Table Permanent Area

The assembler speeds processing by ensuring that certain entries in the symbol table remain in main storage, even though other entries are placed in secondary storage. If the entries exceed the capacity of the permanent area in main storage, the assembly is terminated. If this occurs, the program can be assembled by specifying the alternate mode of assembly, which handles the permanent area in a different manner.

The entries in the permanent area are macro instruction operation codes and global SET symbols. The size of the entries are 10 bytes plus the operation code, and six bytes plus the symbol, respectively. Assuming six-character symbols and an equal number of macro instruction operation codes and global SET symbols, the capacity of the permanent area is shown in Table H-2.

Table H-2. Symbol Table Permanent Area Capacity

Main Storage Available to Assembler	Permanent Area of Symbol Table	Number of Unique Macro-Instructions	Number of Unique Global Symbols
12000	1600	59	59
44000	29000	1050	1050

The values shown in columns 3 and 4 are examples only. There can be a larger number of macro-instructions, if the number of global SET symbols is smaller.

SET SYMBOL VALUE TABLE

This value table is used to store the values associated with all global SET symbols and certain local SET symbols. The entries are:

1. The values for all global SET symbols defined both inside and outside of macro definitions.
2. The values of all local SET symbols defined outside of macro definitions.

Subscripted SET symbols require as many entries as the defined dimension of the subscripted SET symbol. SETA values require four bytes; SETB, one bit (one-eighth of a byte), and SETC, nine bytes. For each subscripted SET symbol, one byte to contain the dimension is also required.

The SET Symbol Value Table (see Table H-3) allows a substantially larger number of global SET symbol entries than the permanent area of the symbol table. Subscripting global SET symbols allows the programmer to take advantage of this.

Table H-3 shows an assumed number of symbols and the implied average dimension permitted by the value table. For the purpose of the value tables, the number of SET values is equivalent to the number of unsubscripted SET symbols plus the sum of the dimensions of subscripted SET symbols. Global SET symbols and local SET symbols that occur outside macro definitions are treated alike.

Table H-3. SET Symbol Value Table Capacity

Main Storage Available to Assembler	Main Storage Available to SET Symbol Value Table	Number of Values Permitted (1)	Number of Global Symbols Assumed	Implied Maximum Average For Dimension of Subscripted Symbols
12000	1500	500	50	10
44000	5000	1666	100	16
80000	10000	3333	100	33

(1) Assuming two SETA and three SETB values for each SETC value, or an average of three bytes per value.

The figures shown in column 3 are an average. The figures in columns 4 and 5 are examples only. There can be a large number of entries for a given type if the number of entries of another type is smaller.

MACRO DEFINITION VALUE TABLE

This table is used to hold the values of local SET symbols within macro definitions, the position of sequence symbols, the attribute values of any symbols referenced in an outer macro instruction, and a pointer-value for each symbolic parameter. This table is unique in that entries are removed from the table when they are no longer needed. As processing of an inner or outer macro definition is completed, space assigned to that definition is made available for processing of a later inner or outer macro definition.

When an inner macro-instruction occurs, the space required is the space for the macro definition for both the inner and outer macro definitions. Subsequent levels of inner macro-instructions may continue to occur until the space available for this table is exhausted.

The sizes of the entries for the SET symbols are described with the preceding table. In addition, each sequence symbol requires five bytes; the attribute values for each symbol require four bytes, and each symbolic parameter requires four bytes in this table.

Table H-4. Macro Definition Value Table Capacity

Main Storage Available To Assembler	Storage Available To Macro-Definition Value Table	Average Number of Entries (1)	Assumed Number of Symbols In Outer Macro Instruction Operands	Assumed Number of Symbolic Parameters (2)	Implied Number of Sequence Symbols And Local SET Symbols (2)
12000	3000	750	200	100	450
44000	30000	7500	500	1000	6000
80000	60000	15000			
200000	160000	40000			

(1) Assuming two SETAs, three SETBs, and one SETC for six sequence symbols.
 (2) One nest of active macro definition only.

The values in columns 3, 4, and 5 are examples only. There can be a larger number of entries for a given type, if the number of entries of another type is smaller.

If one assumes a macro definition contains an average of 30 symbolic parameters and a combination of sequence symbols and local SET values not exceeding 150 (see assumption (1) in Table H-4), then three levels of inner macro-instructions may be accommodated with 12,000 available bytes of storage, and more than 33 levels with 44,000 bytes of available storage.

If the average macro definition contains ten symbolic parameters and a combination of sequence symbols and local SET values not exceeding 45 (see assumption (1) in Table H-4), then ten levels of macro-instructions may be accommodated with 12,000 available bytes of storage.

While the capacity of this value table sets a practical limit of around 100 symbolic parameters to one macro definition, there is also a limit of 200 operands (including suboperands in a sublist) in any one macro-instruction.

Given:

1. A TABLE with 15 entries, each 16 bytes long, having the following format:

NUMBER of items	SWITCHes	ADDRESS	NAME
3 bytes	1 byte	4 bytes	8 bytes

2. A LIST of items, each 16 bytes long, having the following format:

NAME	SWITCHes	NUMBER of items	ADDRESS
8 bytes	1 byte	3 bytes	4 bytes

Find: Any of the items in the LIST which occur in the TABLE and put the SWITCHes, NUMBER of items, and ADDRESS from that LIST entry into the corresponding TABLE entry. If the LIST item does not occur in the TABLE, turn on the first bit in the SWITCHes byte of the LIST entry.

The TABLE entries have been sorted by their NAME.

```

EXAM    TITLE 'SAMPLE PROGRAM'
*
*      THIS IS THE MACRO-DEFINITION
*
      MACRO
      MOVE &TO,&FROM
.*
.*      DEFINE SETC SYMBOL
.*
      LCLC &TYPE
.*
.*      CHECK NUMBER OF OPERANDS
.*
      AIF (N'&SYSLIST NE 2) .ERROR1
.*
.*      CHECK TYPE ATTRIBUTES OF OPERANDS
.*
      AIF (T'&TO NE T'&FROM) .ERROR2
      AIF (T'&TO EQ 'C' OR T'&TO EQ 'G' OR T'&TO EQ 'K') .TYPECGK
      AIF (T'&TO EQ 'D' OR T'&TO EQ 'E' OR T'&TO EQ 'H') .TYPEDEH
      AIF (T'&TO EQ 'F') .MOVE
      AGO .ERROR3
.*TYPEDEH ANOP
.*
.*      ASSIGN TYPE ATTRIBUTE TO SETC SYMBOL
.*
&TYPE  SETC T'&TO
.*MOVE  ANOP
*      NEXT TWO STATEMENTS GENERATED FOR MOVE MACRO
      L&TYPE  2,&FROM
      ST&TYPE 2,&TO
      MEXIT
.*
.*      CHECK LENGTH ATTRIBUTES OF OPERANDS
.*
TYPECGK AIF (L'&TO NE L'&FROM OR L'&TO GT 256) .ERROR4
*      NEXT STATEMENT GENERATED FOR MOVE MACRO
      MVC &TO,&FROM
      MEXIT

```

```

.*
.*      ERROR MESSAGES FOR INVALID MOVE MACRO INSTRUCTIONS
.*
.ERROR1 MNOTE 1,'IMPROPER NUMBER OF OPERANDS, NO STATEMENTS GENERATED'
MEXIT
.ERROR2 MNOTE 1,'OPERAND TYPES DIFFERENT, NO STATEMENTS GENERATED'
MEXIT
.ERROR3 MNOTE 1,'IMPROPER OPERAND TYPES, NO STATEMENTS GENERATED'
MEXIT
.ERROR4 MNOTE 1,'IMPROPER OPERAND LENGTHS, NO STATEMENTS GENERATED'
MEND

*
*      MAIN ROUTINE
*
CSECT
BEGIN   BALR  R13,0           ESTABLISH ADDRESSABILITY OF PROGRAM
        USING *,R13          AND TELL THE ASSEMBLER
        LM   R5,R7,=A(LISTAREA,16,LISTEND)    LOAD LIST AREA PARAMETERS
        USING LIST,R5        REGISTER 5 POINTS TO THE LIST
MORE    BAL  R14,SEARCH       FIND LIST ENTRY IN TABLE
        TM   SWITCH,NONE     CHECK TO SEE IF NAME WAS FOUND
        BO   NOTHERE        BRANCH IF NOT
        USING TABLE,R1      REGISTER 1 NOW POINTS TO TABLE ENTRY
        MOVE TSWITCH,LSWITCH MOVE FUNCTIONS
*      NEXT STATEMENT GENERATED FOR MOVE MACRO
        MVC  TSWITCH,LSWITCH
        MOVE TNUMBER,LNUMBER FROM LIST ENTRY
*      NEXT STATEMENT GENERATED FOR MOVE MACRO
        MVC  TNUMBER,LNUMBER
        MOVE ADDRESS,LADDRESS TO TABLE ENTRY
*      NEXT TWO STATEMENTS GENERATED FOR MOVE MACRO
        L    2,LADDRESS
        ST   2,ADDRESS
        BXLE R5,R6,MORE      LOOP THROUGH THE LIST
        STOP                                END OF PROGRAM
NOTHERE OI   LSWITCH,NONE    TURN ON SWITCH IN LIST ENTRY
        BXLE R5,R6,MORE      LOOP THROUGH THE LIST
        STOP                                END OF PROGRAM
SWITCH  DS   X
NONE    EQU  X'80'
*
*      BINARY SEARCH ROUTINE
*
SEARCH  NI   SWITCH,255-NONE  TURN OFF NOT FOUND SWITCH
        LM   R1,R3,=F'128,4,128'    LOAD TABLE PARAMETERS
        LA   R1,TABLAREA-16(R1) GET ADDRESS OF MIDDLE ENTRY
LOOP    SRL  R3,1             DIVIDE INCREMENT BY 2
        CLC  LNAME,TNAME      COMPARE LIST ENTRY WITH TABLE ENTRY
        BH   HIGHER           BRANCH IF SHOULD BE HIGHER IN TABLE
        BCR  8,R14            EXIT IF FOUND
        SR   R1,R3            OTHERWISE IT IS LOWER IN THE TABLE X
                                SO SUBTRACT INCREMENT
        BCT  R2,LOOP          LOOP 4 TIMES
        B    NOTFOUND        ARGUMENT IS NOT IN THE TABLE
HIGHER  AR   R1,R3           ADD INCREMENT
        BCT  R2,LOOP          LOOP 4 TIMES
NOTFOUND OI  SWITCH,NONE     TURN ON NOT FOUND SWITCH
        BR   R14            EXIT
*
*      THIS IS THE TABLE
*
TABLAREA DS   0D
        DC   XL8'0',CL8'ALPHA'
        DC   XL8'0',CL8'BETA'
        DC   XL8'0',CL8'DELTA'
        DC   XL8'0',CL8'EPSILON'
        DC   XL8'0',CL8'ETA'
        DC   XL8'0',CL8'GAMMA'

```

```

DC      XL8'0',CL8'IOTA'
DC      XL8'0',CL8'KAPPA'
DC      XL8'0',CL8'LAMBDA'
DC      XL8'0',CL8'MU'
DC      XL8'0',CL8'NU'
DC      XL8'0',CL8'OMICRON'
DC      XL8'0',CL8'PHI'
DC      XL8'0',CL8'SIGMA'
DC      XL8'0',CL8'ZETA'
*
*      THIS IS THE LIST
*
LISTAREA DC      CL8'LAMBDA',X'0A',FL3'29',A (BEGIN)
DC      CL8'ZETA',X'05',FL3'5',A (LOOP)
DC      CL8'THETA',X'02',FL3'45',A (BEGIN)
DC      CL8'TAU',X'00',FL3'0',A (1)
DC      CL8'LIST',X'1F',FL3'456',A (0)
LISTEND  DC      CL8'ALPHA',X'00',FL3'1',A (123)
*
*      THESE ARE THE SYMBOLIC REGISTERS
*
R1      EQU      1
R2      EQU      2
R3      EQU      3
R5      EQU      5
R6      EQU      6
R7      EQU      7
R13     EQU      13
R14     EQU      14
*
*      THIS IS THE FORMAT DEFINITION OF LIST ENTRYS
*
LIST     DSECT
LNAME   DS      CL8
LSWITCH DS      C
LNUMBER DS      FL3
LADDRESS DS     F
*
*      THIS IS THE FORMAT DEFINITION OF TABLE ENTRYS
*
TABLE   DSECT
TNUMBER DS      FL3
TSWITCH DS      C
TADDRESS DS     F
TNAME   DS      CL8
END     BEGIN

```

APPENDIX J: ASSEMBLER LANGUAGES--FEATURES COMPARISON CHART

Features not shown below are common to all assemblers. In the chart:

Dash = Not allowed.

X = as defined in Operating System/360 Assembler Language Manual.

Feature	Basic Programming Support/360: Basic Assembler	7090/7094 Support Package Assembler	Other System/360 Assemblers ¹	OS/360 Assembler
No. of Continuation Cards/Statement (exclusive of macro-instructions)	0	0	1	2
Input Character Code	EBCDIC	BCD & EBCDIC	EBCDIC	EBCDIC
ELEMENTS:				
Maximum Characters per symbol	6	6	8	8
Character self-defining terms	1 Char. only	X	X	X
Binary self-defining terms	--	--	X	X
Length attribute reference	--	--	X	X
Literals	--	--	X	X
Extended mnemonics	--	X	X	X
Maximum Location Counter value	2 ¹⁶ -1	2 ²⁴ -1	2 ²⁴ -1	2 ²⁴ -1
Multiple Control Sections per assembly	--	--	X	X
EXPRESSIONS:				
Operators	+-*	+*/	+*/	+*/
Number of terms	3	16	3	16
Number of parentheses	--	--	1 Level	5 Levels
Complex relocatability	--	--	X	X
ASSEMBLER INSTRUCTIONS:				
DC and DS				
Expressions allowed as modifiers	--	--	--	X
Multiple operands	--	--	--	X
Multiple constants in an operand	--	--	Except Address Consts.	X

(Continued)

Appendix J: Assembler Languages--Features Comparison Chart (Continued)

Feature	Basic Programming Support/360: Basic Assembler	7090/7094 Support Package Assembler	Other System/360 Assemblers ¹	OS/360 Assembler
Bit length specifications	--	--	--	X
Scale modifier	--	--	X	X
Exponent Modifier	--	--	X	X
DC types	Except B, P, Z, V, Y, S	Except B, V	X	X
DC duplication factor	Except A	X	Except S	X
DC duplication factor of zero	--	--	Except S	X
DC length modifier	Except H, E, D	X	X	X
DS types	Only C, H, F, D	Only C, H, F, D	X	X
DS length modifier	Only C	Only C	X	X
DS maximum length modifier	256	256	256	65,535
DS constant subfield permitted	--	--	X	X
COPY	--	--	--	X
CSECT	--	--	X	X
DSECT	--	--	X	X
ISEQ	--	--	X	X
LTORG	--	--	X	X
PRINT	--	--	X	X
TITLE	--	X	X	X
COM	--	--	--	X
ICTL	1 oprnd 1 or 25 only	1 oprnd	X	X
USING	2 oprnds oprnd 1 reloc only	2-17oprnds oprnd 1 reloc only	6 oprnds	X
DROP	1 oprnd only	X	5 oprnds	X

(Continued)

Appendix J: Assembler Languages--Features Comparison Chart (Continued)

Feature	Basic Programming Support/360: Basic Assembler	7090/7094 Support Package Assembler	Other System/360 Assemblers ¹	OS/360 Assembler
CCW	oprnd 2 reloc only	X	X	X
ORG	no blank oprnd	no blank oprnd	X	X
ENTRY	1 oprnd only	1 oprnd only	1 oprnd only	X
EXTRN	max 14 1 oprnd only	1 oprnd only	1 oprnd only	X
CNOP	2 dec digits	2 dec digits	2 dec digits	X
PUNCH	--	--	--	X
REPRO	--	--	X	X
Macro Instructions	--	--	X	X

(Continued)

Appendix J: Assembler Languages--Features Comparison Chart (Continued)

Macro Language Features	Other System/360 Assemblers ¹	OS/360 Assembler
Operand Sublists	--	X
Attributes of macro-instruction operands inside macro definitions and symbols used in conditional assembly instructions outside macro definitions.	--	X
Subscripted SET symbols	--	X
Maximum number of operands	49	200
Conditional assembly instructions outside macro definitions.	--	X
Maximum number of SET symbols		
global SETA	16	²
global SETB	128	²
global SETC	16	²
local SETA	16	²
local SETB	128	²
local SETC	0	²
¹ Not including Model 20. ² The number of SET symbols permitted by the Operating System/360 Assembler is variable, dependent upon available main storage.		

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