NOTE 1: FORCES DIRECTION OF DATA BUS DRIVERS ON MPU TO "LOOK THE OTHER WAY" WHILE DATA FROM THE CPA IS IMPOSED ON THE MPU'S LOCAL DATA BUS.

A '0' FROM SSW DSB, DIRECTION FORCED TO PREVENT BUS CONTENTION

WHILE NOT IN RUN MODE (RUN=0)
A '1' FROM ANY OF THE FOLLOWING WILL FORCE DATA TO S-100 BUS
DEPOSIT TO RAM, DIRECTION FORCED TO ROUTE DATA SWITCHES TO RAM
EXEC NOP, DIRECTION FORCED TO PREVENT BUS CONTENTION
EXEC JUMP, DIRECTION FORCED TO PREVENT BUS CONTENTION

R1 - 270KΩ ¼W
R2,6,7,10,11,13-15,60 - 1KΩ ¼W
R3-5,8,9,12 - 47KΩ ¼W
R16-59 - 220Ω ¼W
C1,2,5,6,7,11-22 - 0.1µF
C3,8 - 0.001µF
C4 - 0.01µF
C9,10 - 33µF
CR1 - 1N914

U1,3,4,6,7 - 7405
U2 - DATA HEADER SOCKET
U5 - 8212
U8,15 - 74LS04
U9 - 74LS30
U10 - 7404
U11 - 74LS10
U12 - 7410
U13,16 - 7402
U14,25 - 7400
U15.5 - 7427
U17,20,23 - 74123
U18,19,22 - 74107
U21 - 7430
U24 - 8T97

IMSAI 8080

FRONT CONTROL PANEL

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