

COMMON EIGHT LINE INTERRUPT MODULE TEST

CONSISTS OF:

PROGRAM DESCRIPTION
TEST PROGRAM PAPER TAPE
PROGRAM LISTING

B06-134M95R04A15
06-134M17R04
06-134R04M96A13


INTERDATA®
A DIVISION OF
THE PERKIN ELMER CORPORATION
Oceanport, New Jersey 07757, U.S.A.

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Printed in U.S.A.
November 1977

PAGE REVISION STATUS SHEET

PUBLICATION NUMBER E06-134

TITLE Common Eight Line Interrupt Module Test

REVISION R05 DATE 11/77

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COMMON EIGHT-LINE INTERRUPT MODULE TEST PROGRAM
DESCRIPTION

COMMON EIGHT-LINE INTERRUPT MODULE TEST

Related Documents

Test Program Listing 06-134M96R04A13
Test Program Paper Tape 06-134M17R04

M48-001 Eight-Line Interrupt
Module Instruction Manual 29-268 Consisting of:

Installation Spec. 02-237A20
Maintenance Spec. 02-237A21
Programming Spec. 02-237A22
Application Spec. 02-237A24
Schematic 02-237D08

Test Programs to be run prior to loading this test:

For 16 Bit Processors

Memory Test 06-003
Series 16 Processor Test 06-106
5/16 Processor Test Part 1 06-215
5/16 Processor Test Part 2 06-216
8/16 Processor Test Part 1 06-209
8/16 Processor Test Part 2 06-210
8/16E Processor Test Part 1 06-211
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For 32 Bit Processors

Series 32 Basic Test 06-158
Series 32 Processor Test
Part 1 06-154
Part 2 06-155
Part 3 06-178
Series 32 Memory Test 06-156

Other Test Programs

Teletype Basic Confidence Test 06-004
CRT Test 06-146
Carousel 300 Test 06-183
Current Loop Interface Test 06-184

PURPOSE OF TEST

The Common Eight-Line Interrupt Module Test verifies the operation of the Eight-Line Interrupt Module (M48-001), and assists maintenance personnel in testing and troubleshooting.

The program tests the clear command; disable interrupt function; operation of the mask function; operation of the reset function; external interrupt inputs; and operation of the set interrupt function. Options are available for flexibility in testing.

Test Sequence

1. Test 0

This test verifies that no interrupts occur after a CLEAR command.

2. Test 1

This test verifies that interrupts do not occur while disabled. It also verifies that interrupts may be prevented by masking.

3. Test 2

This test verifies that a single interrupt occurs for each line set, when all interrupts are masked and enabled, and each line is set individually.

4. Test 3

This test verifies that when all interrupts are enabled and set, and each interrupt is masked individually, only the masked line generates an interrupt.

5. Test 4

This test verifies that when all interrupts are set and masked, and each interrupt is reset individually, all lines, except the one reset, cause an interrupt.

6. Test 5

This test verifies the function of the External Interrupt Lines attached to the Eight-Line Interrupt Module.

MINIMUM HARDWARE

The following is a list of hardware required, as a minimum, to perform this test.

1. Processor - Model 7/16 Basic or equivalent, or Model 7/32 or equivalent
2. Minimum Memory - 16K Bytes
3. Console Input Device (see Appendix 1)
Teletype or CRT/Carousel on PASLA
4. List Device (see Appendix 1)
Teletype, CRT/Carousel on PASLA or Line Printer
5. Paper Tape Reader
Teletype or High Speed Paper Tape Reader
6. Eight Line Interrupt Module (M48-001)

REQUIREMENTS OF MACHINE UNDER TEST

This program assumes that the programs listed in the 'Test Programs' section have been run without the detection of an error.

Device Addresses

The Eight-Line Interrupt Module should be strapped for device addresses X'20' - X'27'. If the addresses are different, the DEVADR option must be entered. Refer to Appendices 2 and 3.

Hardware Changes

Before loading the test program ensure that cable 17-170 is removed from Connector 'A' at the front of the 35-397 7" board (Eight Line Interrupt Module).

LOADING PROCEDURE

Test Tape Format

Absolute, non-zoned object tape (M17) with front end bootloader. The test program occupies memory from X'A00' through X'2269'.

Normal Loading Procedure

1. Manually enter the X'50' sequence shown below, into memory.

	LOCATION	CONTENTS
	X'30'	X'0000'
	X'32'	X'0000'
	X'34'	X'0000'
	X'36'	X'0050'
	X'50'	X'D500'
	X'52'	X'00CF'
	X'54'	X'4300'
	X'56'	X'0080'
For TTY	X'78'	X'0294'
For HSPTR	X'78'	X'0399'
For HSPTR/P	X'78'	X'1399'

2. Place the program tape in the paper tape reader.
3. Execute at address X'30'.
4. When the processor halts, observe the CHKSUM byte, displayed on the Console Display Register D1. If it is zero, loading is complete; otherwise, repeat the loading procedure.
5. Refer to Appendix 1 and set up the addresses for the console input device and the list device.
6. Address memory location X'A00' in the case of a 32 bit processor. Address memory location X'A04' in the case of a 16 bit processor.
7. Start program execution. Observe the following title is output to the list device.

COMMON EIGHT LINE INTERRUPT MODULE TEST 06-134R04

OPERATING PROCEDURES

Normal Testing

1. When the title is printed, enter the appropriate TIMVAL option (see Appendix 3).
2. Enter the 'RUN' command. All default tests will be executed, and control returned to the user. (See Appendices 4 and 5 for explanation of printout; see the listing for a description of each test).

3. If no error is detected, select and run Test 5 (see Figure 1). When all tests (0-5) have been run successfully, normal testing is complete.

Optional Testing

Certain test options may be modified for further testing. See Appendix 3 for available options.

Error Procedures

Recoverable Errors

When a recoverable error is detected, an error message is printed if possible, and testing proceeds according to the options selected. See Appendix 5 for error messages printed.

Irrecoverable Errors

If the Machine Malfunction Interrupt is taken, the Processor is halted. When the RUN ('EXECUTE') switch is depressed, the following message is displayed:

```
ERROR TTF2
PSW PPPP LOC LLLL
```

where TT is the number of the test in which the error was detected.

F2 is the code for Machine Malfunction.

PPPP is the least significant 16 bits of the PSW status when the error was detected.

LLLL is the least significant 16 bits of the PSW location counter when the error was detected.

Control is then returned to the Command Processor, and the program waits for console input.

In the case of Irrecoverable Errors other than Machine Malfunction Interrupt, the following message is immediately printed, and control then returned to the Command Processor:

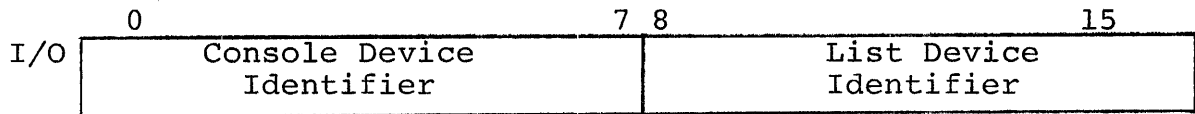
```
ERROR TTFN
PSW PPPP LOC LLLL
```

where FN is the code for the Irrecoverable Error detected, and other printout is as described above (see Appendix 5).

APPENDIX 1

CONSOLE DEVICE DEFINITION

1. The halfword labeled I/O (see the listing) has the default value for Teletype (address X'02') as the console device. If the configuration is different, the test program must be changed as follows:



Console/List Device Identifier	Explanation
X'01'	GDT/CRT on PASLA/PALM interface, strapped for FDX and the highest baud rate.
X'02'	TTY on TTY interface. GDT/CRT on Current Loop Interface
X'03'	Line Printer (Data Printer or Centronics on LP interface.
X'04'	Carousel 300 on PASLA/PALM interface, strapped for FDX and the highest baud rate.
X'05'	Micro I/O Bus Interface.
0,X'06'-X'FF'	Reserved. The program defaults it to 2.

2. The Teletype or Current Loop interface, if used, should be strapped for the device address of X'02'. If it is different, the halfword labeled TTYADR (see the listing) must be changed accordingly.
3. The Carousel, GDT (Graphic Display Terminal) or CRT; if used on PASLA interface should be strapped for the device address of X'10' and X'11' for receiving and transmitting sides respectively. If it is different, the halfword labeled CRTADR (in case of CRT) or C300ADR (in case of carousel) must be changed accordingly (see the listing).
4. The Micro I/O Bus if used should be strapped for device address X'C0'. If the address is different, the halfword labeled MICROBUS (see the listing) must be changed accordingly.

5. The Line Printer, if used, should be strapped for the device address of X'62'. If it is different, the halfword labeled LPADR (see the listing) must be changed accordingly.

APPENDIX 2

OPTION/COMMAND INPUT STRUCTURE

An asterisk (*) is output to the list device to indicate that the program is awaiting an option input. Any option may be typed in from the Console Input Device, followed by a space and the desired hexadecimal value; an exception is the TEST option which accepts argument separated by commas. A carriage return (CR) is issued to terminate every option/command input. An invalid option/command or value causes a (?) followed by a carriage return (CR), line feed (LF), and an asterisk (*) to occur.

APPENDIX 3

OPTIONS TABLE

OPTION	DEFAULT VALUE	DESCRIPTION
TEST	1,2,3,4	Selects the test(s) to be executed.
DEVADR	020	The hexadecimal operand specifies the physical device address of the highest priority Interrupt Line (Line 0) within the module.
INTLEV	0	Specifies Interrupt priority level of the eight-line Interrupt Module. In the case of Model 8/32, can be 0,1,2, or 3.
NOMSG	0	Determines whether all messages will be printed or only error messages will be printed. 0 = All Messages 1 = Error Messages Only
CONTIN	0	Enables the user to run all tests selected continuously, until the Break Key returns the program to the Command Mode. 0 = Normal Execution 1 = Continuous Execution
LOOP	0	Determines the number of times each test is to be executed.

APPENDIX 3 (Continued)

OPTION	DEFAULT VALUE	DESCRIPTION																										
TIMVAL		<p>Controls the length of software time-outs used in program. If this value is increased, the time-out delay is increased. The TIMVAL operand should be selected in accordance with the following table:</p>																										
		<table border="1"> <thead> <tr> <th data-bbox="795 787 1079 819"><u>Processor Model</u></th> <th data-bbox="1266 787 1380 819"><u>TIMVAL</u></th> </tr> </thead> <tbody> <tr> <td data-bbox="795 850 876 882">5/16</td> <td data-bbox="1282 850 1347 882">134</td> </tr> <tr> <td data-bbox="795 913 950 945">6/16 MOS</td> <td data-bbox="1282 913 1347 945">14A</td> </tr> <tr> <td data-bbox="795 976 1209 1008">6/16 (750 nsec memory)</td> <td data-bbox="1282 976 1347 1008">14D</td> </tr> <tr> <td data-bbox="795 1039 1226 1071">6/16 (1000 nsec memory)</td> <td data-bbox="1282 1039 1347 1071">134</td> </tr> <tr> <td data-bbox="795 1102 982 1134">7/16 Basic</td> <td data-bbox="1299 1102 1347 1134">D2</td> </tr> <tr> <td data-bbox="795 1165 1023 1197">7/16 HSALU -</td> <td data-bbox="1282 1165 1347 1197"></td> </tr> <tr> <td data-bbox="795 1228 1112 1260">(750 nsec memory)</td> <td data-bbox="1282 1228 1347 1260">14D</td> </tr> <tr> <td data-bbox="795 1291 1128 1323">(1000 nsec memory)</td> <td data-bbox="1282 1291 1347 1323">134</td> </tr> <tr> <td data-bbox="795 1354 909 1386">7/32 -</td> <td data-bbox="1282 1354 1347 1386"></td> </tr> <tr> <td data-bbox="795 1417 1112 1449">(750 nsec memory)</td> <td data-bbox="1299 1417 1347 1449">EB</td> </tr> <tr> <td data-bbox="795 1480 1128 1512">(1000 nsec memory)</td> <td data-bbox="1299 1480 1347 1512">D2</td> </tr> <tr> <td data-bbox="795 1543 909 1575">8/32 -</td> <td data-bbox="1299 1543 1347 1575">DA</td> </tr> </tbody> </table>	<u>Processor Model</u>	<u>TIMVAL</u>	5/16	134	6/16 MOS	14A	6/16 (750 nsec memory)	14D	6/16 (1000 nsec memory)	134	7/16 Basic	D2	7/16 HSALU -		(750 nsec memory)	14D	(1000 nsec memory)	134	7/32 -		(750 nsec memory)	EB	(1000 nsec memory)	D2	8/32 -	DA
<u>Processor Model</u>	<u>TIMVAL</u>																											
5/16	134																											
6/16 MOS	14A																											
6/16 (750 nsec memory)	14D																											
6/16 (1000 nsec memory)	134																											
7/16 Basic	D2																											
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(1000 nsec memory)	134																											
7/32 -																												
(750 nsec memory)	EB																											
(1000 nsec memory)	D2																											
8/32 -	DA																											
OPTION		<p>Enter this command to display all options with their current values on the console device.</p>																										
RUN		<p>Enter this command to execute the test.</p>																										

APPENDIX 4

SAMPLE PRINTOUT

COMMON EIGHT LINE INTERRUPT MODULE TEST 06-134R04
*TIMVAL DA
*RUN

TEST 0
NO ERROR

TEST 1
NO ERROR

TEST 2
NO ERROR

TEST 3
NO ERROR

TEST 4
NO ERROR
END OF TEST

*TEST 5
*RUN
GENERATE INTERRUPT
DEV 020 STA 00
NO MORE INTERRUPTS
GENERATE INTERRUPT
DEV 021 STA 00

-
-
-
DEV 026 STA 00
NO MORE INTERRUPTS
GENERATE INTERRUPT
DEV 024 STA 00
DEV 026 STA 00
DEV 027 STA 00
NO MORE INTERRUPTS
NO ERROR
END OF TEST
*

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APPENDIX 5 - ERROR TABLE

TEST NO.	ERROR CONDITION	EXPLANATION	SUGGESTED ACTION
Any	ERROR TT01 DEV DDD CMD ADR CCC	Interrupt Module generated an interrupt after a CLEAR command was issued.	
Any	ERROR TT02 DEV DDD CMD ADR CCC	Interrupt Module generated an interrupt while disabled.	
Any	ERROR TT03 DEV DDD CMD ADR CCC	Interrupt Module generated an interrupt while line was unmasked.	
Any	ERROR TT04 DEV DDD EXP DDD CMD ADR CCC	Interrupt Module did not generate an interrupt when line was set, enabled, and masked (Software Time-out).	

NOTE 1. TT = Test Number, 00-05

DDD = Device Address

CCC = Device Address to which all commands are sent.

A5-1

APPENDIX 5 (Continued)

TEST NO.	ERROR CONDITION	EXPLANATION	SUGGESTED ACTION
Any	ERROR TT05 DEV DDD EXP DDD CMD ADR CCC	Interrupt Module generated an interrupt when expected, but with a wrong line address	Determine whether the correct line interrupted, but the wrong line address was returned. If the correct line address was returned, the line interrupted before a set, masked, and enabled line lower address.
Any	ERROR TT06 DEV DDD RESET DDD CMD ADR CCC	Interrupt Module generated an interrupt on a line just reset.	
05	ERROR 0507 CMD ADR CCC	The External Interrupt Line on the Interrupt Module did not generate an interrupt when manually grounded.	<ol style="list-style-type: none"> 1. Check Fig. 1, "procedure for Manually Generating an Interrupt". 2. Repeat the test. 3. Ensure that proper contact is made between the appropriate pins on Connector A.

APPENDIX 5 (Continued)

TEST NO.	ERROR CONDITION	EXPLANATION	SUGGESTED ACTION
Any	ERROR TT08 DEV DDD CMD ADR CCC	The Interrupt Module generated multiple interrupts returning the same line address.	1. Ensure that cable 17-170 is removed from Connector A on the 35-397 7" board.
0	ERROR TT09 DEV DDD CMD ADR CCC	False Sync. from a device address of the eight line interrupt module.	1. Ensure that the DEVADR option is properly entered for the eight line addresses in the system.

NOTE 1: TT = Test Number, 00-05
 DDD = Device Address
 CCC = Device Address to which all commands are sent.

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APPENDIX 5 (Continued)

IRRECOVERABLE ERRORS COMMON TO ALL TESTS

ERROR NO.	TYPE OF FAILURE
TTF1	Arithmetic Fault Interrupt
TTF2	Illegal Instruction Interrupt
TTF3	Machine Malfunction Interrupt (See Note 2)
TTF4	Unsolicited Immediate Interrupt
TTF5	Memory Access Controller/HW Floating Point Interrupt
TTF6	Interrupt into wrong register set.

NOTE 1. TT = Test Number from 00 to 05.

NOTE 2. The PSW resulting from the Machine Malfunction Interrupt is displayed. The last 4 bits of the PSW status define the type of failure, as described below:

X100	Parity Error on Data Fetch
0010	Parity Error on Instruction Fetch
X001	Power Fail
0000	Power Restore
1X0X	Parity Error or Power Fail during an Auto Driver Channel Operation (32-bit Processors only).

PROG= CELINT ASSEMBLED BY CAL 03-066R05-00 (32-BIT)

1	**0613403	ELI00010
2	CELINT PROG COMMON EIGHT-LINE INTERRUPT MODULE TEST 06-134R04M96A13	ELI00020
3	CROSS	ELI00030
4	WIDTH 120	ELI00040
5	TARGT 16	ELI00050
6	SGCHK	ELI00060
7	* *	ELI00070
8	*	ELI00080
9	* COMMON EIGHT-LINE INTERRUPT MODULE TEST 06-134R04	ELI00090
10	* COPYRIGHT INTERDATA, INC. NOVEMBER 1977	ELI00100
11	*	ELI00110
12	* PROGRAM USES THE COMMON INSTRUCTION SET	ELI00120
13	*	ELI00130
14	* THIS PROGRAM TESTS THE EIGHT-LINE INTERRUPT MODULE.	ELI00140
15	* THE PROGRAM CONSISTS OF SIX TESTS, WITH TEST 5 BEING THE MANUAL	ELI00150
16	* INTERRUPT TEST.	ELI00160
17	* THERE ARE 9 OPTIONS AVAILABLE TO THE USER AND EIGHT ERROR MESSAGES	ELI00170
18	* TO ENABLE ISOLATION OF A MALFUNCTION TO THE HARDWARE LEVEL. EACH	ELI00180
19	* TEST EXCEPT SUBTEST 5 EXERCISES ALL COMMAND ADDRESSES ASSOCIATED	ELI00190
20	* WITH THE EIGHT-LINE INTERRUPT MODULE.	ELI00200
21	*	ELI00210
22	* THE PROGRAM REQUIRES EITHER 7/16 BASIC OR EQUIVALENT PROCESSOR,	ELI00220
23	* OR 7/32 OR EQUIVALENT PROCESSOR, WITH 16K BYTES OF MEMORY. OPTIONS	ELI00230
24	* AND RUN COMMAND ARE TO BE ENTERED VIA A CONSOLE DEVICE. A SINGLE	ELI00240
25	* INTERRUPT MODULE MAY BE TESTED AT A TIME.	ELI00250
26	*	ELI00260
27	* THE 06-134R04M17 TAPE IS AN ABSOLUTE TAPE WITH FRONT-END BOOT	ELI00270
28	* LOADER.	ELI00280
29	*	ELI00290
30	* TEST 0	ELI00300
31	* VERIFIES THAT A CLEAR COMMAND (X'08') DOES CLEAR ALL	ELI00310
32	* PENDING INTERRUPTS WITHIN THE EIGHT-LINE INTERRUPT MODULE.	ELI00320
33	*	ELI00330
34	* TEST 1	ELI00340
35	* VERIFIES THAT ALL DEVICE INTERRUPTS ARE DISABLED WHEN A 'DISABLE'	ELI00350
36	* COMMAND IS ISSUED. THE ABILITY OF THE MASK TO PREVENT INTERRUPTS IS	ELI00360
37	* ALSO TESTED.	ELI00370
38	*	ELI00380
39	* TEST 2	ELI00390
40	* VERIFIES THAT INTERRUPT LINES WITHIN THE MODULE MAY BE SET	ELI00400
41	* INDIVIDUALLY UNDER PROGRAM CONTROL.	ELI00410
42	*	ELI00420
43	* TEST 3	ELI00430
44	* VERIFIES THAT INTERRUPT LINES WITHIN THE MODULE MAY BE MASKED	ELI00440
45	* INDIVIDUALLY UNDER PROGRAM CONTROL.	ELI00450
46	*	ELI00460
47	* TEST 4	ELI00470
48	* VERIFIES THAT INTERRUPT LINES WITHIN THE MODULE MAY BE RESET	ELI00480
49	* INDIVIDUALLY UNDER PROGRAM CONTROL.	ELI00490
50	*	ELI00500
51	* TEST 5	ELI00510
52	* VERIFIES THAT INTERRUPTS MAY BE GENERATED BY MANUALLY CONNECTING	ELI00520
53	* CERTAIN PINS ON CONNECTOR 'A' OF THE INTERRUPT MODULE BOARD.	ELI00530

	54	*			ELI00540
	55	*	* ANY COMBINATION OF THESE TESTS MAY BE SELECTED AS A STRING AND		ELI00550
	56	*	* CAN BE LOOPED OR RUN CONTINUOUSLY.		ELI00560
	57	*			ELI00570
	58	**ETPE			ELI00580
	59	*			ELI00590
	60	R0	EQU	0	ELI00600
	61	R1	EQU	1	ELI00610
	62	R2	EQU	2	ELI00620
	63	R3	EQU	3	ELI00630
	64	R4	EQU	4	ELI00640
	65	R5	EQU	5	ELI00650
	66	R6	EQU	6	ELI00660
	67	R7	EQU	7	ELI00670
	68	R8	EQU	8	ELI00680
	69	R9	EQU	9	ELI00690
	70	R10	EQU	10	ELI00700
	71	R11	EQU	11	ELI00710
	72	R12	EQU	12	ELI00720
	73	R13	EQU	13	ELI00730
	74	R14	EQU	14	ELI00740
	75	RET	EQU	14	ELI00750
	76	R15	EQU	15	ELI00760
	77	LINK	EQU	15	ELI00770
	78	*			ELI00780
	79	*			ELI00790
	80	*	BOOTLOADER WITH CHKSUM		ELI00800
	81	*			ELI00810
0000R	82		ORG	X'R0'	ELI00820
0080	83	2421	LIS	R2,1	ELI00830
0082	84	2303	BS	BOOT	ELI00840
0084	85	2028	DC	Z(PSWSAVE)	ELI00850
0086	86	2030	DC	Z(RSAVE)	ELI00860
0088	87	C810 0A00	BOOT	LHI R1,ORIGIN1	ELI00870
008C	88	C830 2018		LHI R3,LNZ8+1	ELI00880
0090	89	4030 0022		STH R3,X'22'	ELI00890
0094	90	2731		SIS R3,1	ELI00900
0096	91	C860 0000	MN	LHI R6,0	ELI00910
009A	92	D340 0078		LB R4,X'78'	ELI00920
009E	93	DE40 0079		OC R4,X'79'	ELI00930
00A2	94	9D45	LEADER	SSR R4,R5	ELI00940
00A4	95	2091		BTBS 9,1	ELI00950
00A6	96	9B45		RDR R4,R5	ELI00960
00A8	97	0855		LDAR R5,R5	ELI00970
00AA	98	2234		BZS LEADER	ELI00980
00AC	99	D251 0000	LOAD	STB R5,0(R1)	ELI00990
00B0	100	D351 0000		LB R5,0(R1)	ELI01000
00B4	101	0765		XAR R6,R5	ELI01010
00B6	102	9481		EXBR R8,R1	ELI01020
00B8	103	9828		WHR R2,R8	ELI01030
00BA	104	9D45		SSR R4,R5	ELI01040
00BC	105	2091		BTBS 9,1	ELI01050
00BE	106	9B45		RDR R4,R5	ELI01060
00C0	107	C110 00AC		9XLE R1,LOAD	ELI01070
00C4	108	9486		EXBR R8,R6	ELI01080

CURRENT PSW SAVE POINTER(32-BIT M/C)
REGISTER SAVE POINTER(32-BIT M/C)
R1 = ADR(FIRST BYTE OF TEST PROG)
R3 = ADR(LAST NON-ZERO BYTE)
REGISTER SAVE POINTER(16-BIT M/C)
R6 = CHKSUM BYTE = X'MN'
INPUT DEV ADR
DU,BSY
DU,BSY
IGNORE LEADER
STORE 1ST NON-ZERO & SUBSEQUENT BYTE
RELOAD DATA BYTE TO
GENERATE CHKSUM
DISPLAY MEMORY ADDRESS
DU,BSY
LOAD TILL LAST BYTE

00C6 9828
00C8 2478
00CA 917C
00CC 9557
00CE 2203

109
110 LDWT
111
112
113

WHR R2,R8
LIS R7,8
SLLS R7,12
EPSR R5,R7
RS LDWT

FINAL CHKSUM
R7 = X*8000*
HALT PROCESSOR.

ELI01090
ELI01100
ELI01110
ELI01120
ELI01130

EXEC - ETPE RU3P4 (W/CONDITIONAL ASSEMBLY)

00D0		115	ORG	X'A00'		ELI01150
0A00	4300 0A32	116	ORIGIN1	B START1	START HERE FOR 32-BIT PROCESSOR	ELI01160
0A04		117		IFZ ADC-2		ELI01170
0A04	4300 0A48	118	ORIGIN2	B START2	START HERE FOR 16-BIT PROCESSOR	ELI01180
0A08	4300 0A62	119	ORIGIN3	B START3	SPECIAL 32-BIT PROCESSOR START	ELI01190
0A0C	4300 0A66	120	ORIGIN4	B START4	SPECIAL 16-BIT PROCESSOR START	ELI01200
		121		ELSE		ELI01210
		122	ORIGIN2	B START3	SPECIAL START(S) - 32 BIT PROCESSOR	ELI01220
		123		B START3		ELI01230
		124		B START3		ELI01240
		125		ENDC		ELI01250
		126	*			ELI01260
		127	*-----*			ELI01270
		128	* TEST CONSTANTS	*		ELI01280
		129	*			ELI01290
0A10	0202	130	IO	DC X'0202'	I/O DEVICE(S) IDENTIFIER	ELI01300
0A12	1011	131	PASLADR	DC X'1011'	PASLA/PALM READ/WRITE ADDRESSES	ELI01310
0A14	0202	132	CLIFADR	DC X'0202'	CURRENT LOOP INTERFACE R/W ADDRESSES	ELI01320
0A16	6262	133	LPADR	DC X'6262'	LINE PRINTER ADDRESS	ELI01330
0A18	1011	134	C300ADR	DC X'1011'	CAROUSEL 300/PASLA ADDRESSES	ELI01340
0A1A	C0C0	135	MICROBUS	DC X'C0C0'	MICROBUS ADDRESS	ELI01350
0A1C	0000	136		DCX 0	PROVISION FOR SPECIAL DEVICE	ELI01360
		137	*			ELI01370
		138	* IO =	0101 FOR CRT ON PASLA		ELI01380
		139	*	0202 FOR TELETYPE, CAROUSEL 15/30		ELI01390
		140	*	XX03 FOR LINE PRINTER		ELI01400
		141	*	0404 FOR CAROUSEL 300		ELI01410
		142	*	0505 FOR MICROBUS		ELI01420
		143	*			ELI01430
0A1E	0140	144	TIME	DC X'140'	CONSTANT FOR 1 MS DELAY(X'C8'-MOD70)	ELI01440
0A20	0000	145		DCX 0	RESERVED	ELI01450
0A22	70F0	146	PSW	DCX 70F0	PSW USED IN PROGRAM	ELI01460
0A24	30F0	147	PSW2	DCX 30F0	PSW USED IN EXEC	ELI01470
0A26	70F0	148	PSW3	DCX 70F0		ELI01480
0A28	0000	149		DCX 0	RESERVED	ELI01490
0A2A	0000	150		DCX 0	RESERVED	ELI01500
0A2C	0000	151		DCX 0	RESERVED	ELI01510
0A2E	0000	152		DCX 0	RESERVED	ELI01520
0A30	0000	153		DCX 0	RESERVED	ELI01530
		154	*-----*			ELI01540
		155	*			ELI01550
0A32	2410	156	START1	LIS R1,0		ELI01560
0A34	4010 0030	157		STH R1,X'30'	DISABLE INT AT PROCESSOR LEVEL	ELI01570
0A38	4820 0A24	158		LH R2,PSW2		ELI01580
0A3C	4020 0032	159		STH R2,X'32'	SELECT REG SET 15	ELI01590
0A40		160		IFZ ADC-2		ELI01600
0A40	2521	161		LCS R2,1		ELI01610
0A42	4020 1712	162		STH R2,MOD32	SET MODEL 32 PROCESSOR FLAG	ELI01620
0A46	2306	163		BS ST		ELI01630
0A48	2410	164	START2	LIS R1,0		ELI01640
0A4A	4010 1712	165		STH R1,MOD32	RESET MOD 32 PROCESSOR FLAG	ELI01650
0A4E	4810 0A24	166		LH R1,PSW2		ELI01660
		167		ENDC		ELI01670

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0A52	C820	0A6A	168	ST	LHI	R2,START		ELI01680
0A56	4010	0034	169		STH	R1,X'34'		ELI01690
0A5A	4020	0036	170		STH	R2,X'36'	II INT NEW PSW LOC	ELI01700
0A5E	0000		171		DCX	0	TAKE AN ILLEGAL INSTRUCTION INT	ELI01710
0A60	2200		172		BS	*	HALT IF II NOT TAKEN	ELI01720
			173	*				ELI01730
0A62	4300	0A32	174	START3	B	START1	INSERT SPECIAL ROUTINE HERE	ELI01740
0A66			175		IFZ	ADC-2		ELI01750
0A66	4300	0A48	176	START4	B	START2	INSERT SPECIAL ROUTINE HERE	ELI01760
			177		ENDC			ELI01770
			178	*				ELI01780
0A6A	D310	0A10	179	START	LB	R1,I0	GET I/O IDENTIFIERS	ELI01790
0A6E	D320	0A11	180		LB	R2,I0+1		ELI01800
0A72	2436		181		LIS	R3,6	IDENTIFIER CAN BE 1,2,3,4,5	ELI01810
0A74	0513		182		CLAR	R1,R3		ELI01820
0A76	2182		183		BLS	I0.OK1	BRANCH IF K3 IDENTIFIER OK	ELI01830
0A78	2412		184		LIS	R1,2	OTHERWISE FORCE IT TO BE TTY	ELI01840
0A7A	0523		185	I0.OK1	CLAR	R2,R3		ELI01850
0A7C	2182		186		BLS	I0.OK2	SAME TEST FOR LIST DEVICE	ELI01860
0A7E	2422		187		LIS	R2,2		ELI01870
0A80	D210	0A10	188	I0.OK2	STR	R1,I0	REESTABLISH VALUES	ELI01880
0A84	D220	0A11	189		STB	R2,I0+1		ELI01890
0A88	D362	1744	190		LR	R6,CONRQ2S(R2)		ELI01900
0A8C	4060	1728	191		STH	R6,PASFLG2	SET PASLA FLAG (LIST DEVICE)	ELI01910
0A90	0866		192		LDAR	R6,R6		ELI01920
0A92	2336		193		BZS	I0.OK3	SKIP IF NOT PASLA	ELI01930
0A94	9121		194		SLHLS	R2,1		ELI01940
0A96	D302	0A11	195		LB	R0,I0+1(R2)		ELI01950
0A9A	DE02	1738	196		OC	R0,CON2ND(R2)	ISSUE 2ND COMMAND (TO LIST DEVICE)	ELI01960
			197	*				ELI01970
0A9E	41F0	13FC	198	I0.OK3	BAL	LINK,SETKB	ESTABLISH KEYBOARD DEVICE (& IOSAVE)	ELI01980
0AA2	9310		199		LBR	R1,R0	(R1) = 1,2,4,5 ; (R0 = KBIDENT)	ELI01990
0AA4	9111		200		SLHLS	R1,1	(R1) = 2,4,6,A	ELI02000
0AA6	4831	0A10	201		LH	R3,I0(R1)		ELI02010
0AAA	4030	172A	202		STH	R3,CONADR	SET UP CONSOLE DEVICE ADDRESS	ELI02020
0AAE	4821	172C	203		LH	R2,CONRD(R1)		ELI02030
0AB2	4020	172C	204		STH	R2,CONRD	SET UP R/W COMMANDS	ELI02040
0AB6	4821	1738	205		LH	R2,CON2ND(R1)		ELI02050
0ABA	4020	1738	206		STH	R2,CON2ND	2ND CMD: ENABLE READ CMD	ELI02060
0ABE	9011		207		SRHLS	R1,1		ELI02070
0AC0	D341	1744	208		LB	R4,CONRQ2S(R1)		ELI02080
0AC4	D240	1744	209		STR	R4,CONRQ2S	CONSOLE REQUEST TO SEND	ELI02090
0AC8	4040	1726	210		STH	R4,PASFLG	SET PASLA FLAG (CONSOLE)	ELI02100
0ACC	9333		211		LBR	R3,R3	MASK CONSOLE ADDRESS TO 8 BITS	ELI02110
0ACE	0844		212		LDAR	R4,R4		ELI02120
0AD0	2333		213		BZS	I0.OK4	SKIP 2ND OC IF NOT PASLA DEVICE	ELI02130
0AD2	9422		214		EXBR	R2,R2		ELI02140
0AD4	9E32		215		OCR	R3,R2	ISSUE 2ND COMMAND (TO CONSOLE)	ELI02150
0AD6	DE30	172C	216	I0.OK4	OC	R3,CONRD	PUT CONSOLE IN READ MODE	ELI02160
0ADA	9B3F		217		RDR	R3,R15	READ A DUMMY CHARACTER (SET BUSY)	ELI02170
			218	*				ELI02180
0ADC	41F0	145A	219		BAL	LINK,LCORE	SET UP LOW CORE	ELI02190
0AE0	2400		220		LIS	R0,0		ELI02200

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0AE2	4000	1754	221	STH	R0,WASDU	RESET 'DEVICE UNAVAILABLE' FLAGS	ELI02210
0AE6	4000	1756	222	STH	R0,WASDU1		ELI02220
0AEA	41F0	1274	223	BAL	LINK,CRLF		ELI02230
0AEE	C850	1890	224	LHI	R5,TITLE		ELI02240
0AF2	41F0	11F0	225	BAL	R15,PRINT	PRINT TEST PROGRAM TITLE	ELI02250
			226	*-----*			ELI02260
			227	* KEYBOARD INPUT ROUTINE			ELI02270
			228	*			ELI02280
0AF6	41F0	1274	229	OPTIN	BAL LINK,CRLF	CR,LF TO LIST DEVICE	ELI02290
			230	*			ELI02300
0AFA	4820	0A24	231	OPTIN1	LH R2,PSW2		ELI02310
0AFE	9512		232	EPSR	R1,R2	NO INT. REG SET 15	ELI02320
0B00	41F0	13FC	233	BAL	LINK,SETKR	ESTABLISH CONSOLE	ELI02330
0B04	0340	180E	234	LB	R4,AMSG	OUTPUT AN * TO INDICATE	ELI02340
0B08	41F0	1282	235	BAL	LINK,OUTCHR	COMMAND MODE ESTABLISHED	ELI02350
0B0C	2541		236	LCS	R4,1	X'FF'	ELI02360
0B0E	41F0	1282	237	BAL	LINK,OUTCHR		ELI02370
0B12	C8C0	133E	238	LHI	R12,QUESTN	SET UP R12 FOR ERR ROUTINE	ELI02380
0B16	C800	2020	239	LHI	R0,X'2020'	BLANK OUT COMMAND BUFFER	ELI02390
0B1A	4000	201A	240	STH	R0,OPTBUF	WHICH WILL CONTAIN OPTION	ELI02400
0B1E	4000	201C	241	STH	R0,OPTBUF+2	NAME	ELI02410
0B22	4000	201E	242	STH	R0,OPTBUF+4		ELI02420
0B26	2410		243	LIS	R1,0	CLEAR OPTBUF INDEX	ELI02430
0B28	41F0	1310	244	RDCHR	BAL R15,GETCHR	GET A CHAR IN R4	ELI02440
0B2C	C540	0060	245	CLHI	R4,X'60'	UPPER CASE ALPHA ?	ELI02450
0B30	2183		246	BLS	RDCHAR0	BRANCH IF NO.	ELI02460
0B32	C840	0020	247	SHI	R4,X'20'	CONVERT TO LOWER CASE	ELI02470
0B36	C540	0023	248	RDCHAR0	CLHI R4,X'23'	IS IT # ?	ELI02480
0B3A	4330	0AF6	249	BE	OPTIN		ELI02490
0B3E	C540	005F	250	CLHI	R4,X'5F'	LEFT ARROW, UNDERLINE OR DELETE ?	ELI02500
0B42	2334		251	BES	RDCHAR1		ELI02510
0B44	C540	0008	252	CLHI	R4,X'08'	BACK SPACE ?	ELI02520
0B48	2139		253	BNES	RDCHR1	NO, BRANCH	ELI02530
0B4A	2711		254	RDCHAR1	SIS R1,1	YES, DECREMENT INDEX	ELI02540
0B4C	021C		255	BMR	R12	BUFFER UNDERFLOW; PRINT '?'	ELI02550
0B4E	C800	0020	256	LHI	R0,X'20'		ELI02560
0B52	D201	201A	257	STB	R0,OPTBUF(R1)		ELI02570
0B56	4300	0B28	258	B	RDCHR		ELI02580
0B5A	C540	0000	259	RDCHR1	CLHI R4,X'00'	IS IT CR ?	ELI02590
0B5E	233C		260	BES	LOOKUP	YES, TRY MATCH	ELI02600
0B60	C540	0020	261	CLHI	R4,X'20'	IS IT A BLANK?	ELI02610
0B64	2339		262	BES	LOOKUP	YES, TRY MATCH	ELI02620
0B66	C510	0006	263	CLHI	R1,6	7 CHARACTERS INPUT ?	ELI02630
0B6A	038C		264	BNLR	R12	IF YES, ERROR	ELI02640
0B6C	D241	201A	265	STB	R4,OPTBUF(R1)	STORE CURRENT BYTE	ELI02650
0B70	2611		266	AIS	R1,1	BUMP BUFFER INDEX	ELI02660
0B72	4300	0B28	267	B	RDCHR	READ NEXT CHARACTER	ELI02670
			268	*-----*			ELI02680
			269	* OPTION MATCH ROUTINE			ELI02690
			270	*			ELI02700
0B76	C810	1810	271	LOOKUP	LHI R1,OPT	LOAD ADDRESS OF OPTION TABLE	ELI02710
0B7A	2430		272	LOOK1	LIS R3,0	CLEAR BUFFER INDEX	ELI02720
0B7C	0861		273	LDAR	R6,R1	SET OPTION WORD INDEX	ELI02730

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0B7E	4856	0000	274	LOOK2	LH	R5,0(R6)		ELI02740
0B82	021C		275		BMR	R12	IF MINUS, THEN NO MATCH = ERROR	ELI02750
0B84	4553	201A	276		CLH	R5,OPTRUF(R3)	COMPARE TO OPTBUF HW	ELI02760
0B88	2333		277		BES	LOOK3		ELI02770
0B8A	261C		278		AIS	R1,12		ELI02780
0B8C	2209		279		BS	LOOK1		ELI02790
0B8E	2632		280	LOOK3	AIS	R3,2	TRY NEXT HW	ELI02800
0B90	2662		281		AIS	R6,2		ELI02810
0B92	C530	0006	282		CLHI	R3,6	3 MATCHING HW FOUND ?	ELI02820
0B96	208C		283		BLS	LOOK2		ELI02830
			284	*				ELI02840
0B98	C510	1870	285		CLHI	R1,RUN	RUN COMMAND ?	ELI02850
0B9C	4330	0D80	286		BE	RUNIT		ELI02860
0BA0	C510	1864	287		CLHI	R1,OPTION	OPTION CMD ?	ELI02870
0BA4	4230	0CB6	288		RNE	LOOK4	NO, LOOK FURTHER	ELI02880
			289	*-----*				ELI02890
			290	*	* TO PROCESS INPUT COMMAND 'OPTION'			ELI02900
			291	*				ELI02910
0BA8	C540	0000	292		CLHI	R4,X'0D'	CR ?	ELI02920
0BAC	233C		293		BES	OPTEXX	YES, BRANCH	ELI02930
0BAE	41E0	10C6	294		BAL	R14,OPTVAL	NO, GET OPTION DEV. PRINTOUT NUM.	ELI02940
0BB2	C560	0006	295		CLHI	R6,6	IS DEVICE NUMBER VALID ?	ELI02950
0BB6	2387		296		BNLS	OPTEXX	NO, BRANCH	ELI02960
0BB8	C840	000A	297		LHI	R4,X'0A'	YES, LOAD AN LF CHARACTER	ELI02970
0BBC	41F0	1282	298		BAL	LINK,OUTCHR	WRITE IT TO THE CONSOLE	ELI02980
0BC0	D260	2021	299		STB	R6,IOSAVE+1	CHANGE THE LIST DEVICE	ELI02990
0BC4	4820	186C	300	OPTEXX	LH	R2,OPTION+8	CHECK FOR SPECIAL ROUTINE	ELI03000
0BC8	0232		301		BNZR	R2	LINK TO ROUTINE	ELI03010
			302	*				ELI03020
0BCA	C830	1810	303	OPTRTN	LHI	R3,TEST	RETURN HERE	ELI03030
0BCE	C8E0	0C54	304		LHI	R14,OPTCMD8		ELI03040
0BD2	41F0	1274	305		BAL	LINK,CRLF		ELI03050
0BD6	2420		306	OPTCMD	LIS	R2,0	RESET COUNTER	ELI03060
0BD8	D342	1810	307	OPTCMD1	LB	R4,OPT(R2)	TO PRINT TEST	ELI03070
0BDC	41F0	1282	308		BAL	LINK,OUTCHR		ELI03080
0BE0	2621		309		AIS	R2,1		ELI03090
0BE2	C520	0006	310		CLHI	R2,6		ELI03100
0BE6	2087		311		BLS	OPTCMD1		ELI03110
0BE8	C840	0020	312		LHI	R4,C' '		ELI03120
0BEC	41F0	1282	313		BAL	LINK,OUTCHR	OUTPUT 1 SPACE	ELI03130
0BF0	2450		314		LIS	R5,0	TO PRINT SELECTED TEST NUMBERS	ELI03140
0BF2	4050	1710	315		STH	R5,FIRST		ELI03150
0BF6	4823	0006	316		LH	R2,6(R3)	FIRST TEST WORD	ELI03160
0BFA	2440		317	OPTCMD2	LIS	R4,0	START WITH TEST 0	ELI03170
0BFC	4040	2022	318		STH	R4,TEMP		ELI03180
0C00	9121		319	OPTCMD3	SLHLS	R2,1		ELI03190
0C02	4380	0C34	320		BNC	OPTCMD7		ELI03200
0C06	4040	2022	321	OPTCMD4	STH	R4,TEMP	OPTION VALUE FOUND.	ELI03210
0C0A	4800	1710	322		LH	R0,FIRST	IS IT FIRST ?	ELI03220
0C0E	2335		323		BZS	OPTCMD5		ELI03230
0C10	C840	002C	324		LHI	R4,C' '	NO, OUTPUT COMMA	ELI03240
0C14	41F0	1282	325		BAL	LINK,OUTCHR		ELI03250
0C18	40F0	1710	326	OPTCMD5	STH	LINK,FIRST		ELI03260

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0C1C	0855	327	LDAR	R5,R5	TEST VALUE FROM SECOND HW	ELI03270
0C1E	2335	328	BZS	OPTCMD6	NO	ELI03280
0C20	C840 0031	329	LHI	R4,C'1'	YES,OUTPUT '1'	ELI03290
0C24	41F0 1282	330	BAL	LINK,OUTCHR		ELI03300
0C28	4840 2022	331	OPTCMD6	LH R4,TEMP	RESTORE R4	ELI03310
0C2C	D344 1778	332	LB	R4,HEXTAB(R4)	CONVERT	ELI03320
0C30	41F0 1282	333	BAL	LINK,OUTCHR	OUTPUT 0-F	ELI03330
0C34	4840 2022	334	OPTCMD7	LH R4,TEMP	RESTORE	ELI03340
0C38	2641	335	AIS	R4,1	INCREMENT TEST #	ELI03350
0C3A	4040 2022	336	STH	R4,TEMP		ELI03360
0C3E	C540 0010	337	CLHI	R4,16		ELI03370
0C42	4280 0C00	338	BL	OPTCMD3		ELI03380
0C46	0855	339	OPTCMD71	LDAR R5,R5	DONE ?	ELI03390
0C48	023E	340	BNZR	R14		ELI03400
0C4A	4823 0008	341	LH	R2,8(R3)	SECOND TEST WORD	ELI03410
0C4E	2451	342	LIS	R5,1	R5 = 1 FOR SECOND TEST HW	ELI03420
0C50	4300 0BFA	343	B	OPTCMD2		ELI03430
		344	*-----*			ELI03440
		345	* TO OUTPUT OTHER OPTION NAMES & VALUES			ELI03450
		346	*			ELI03460
0C54	41F0 1274	347	OPTCMD8	BAL LINK,CRLF		ELI03470
0C58	2461	348	LIS	R6,1	SET LINE COUNTER	ELI03480
0C5A	C820 181C	349	LHI	R2,OPT+12	R2 POINTS TO THE NAME	ELI03490
0C5E	2436	350	OPTCMD9	LIS R3,6		ELI03500
0C60	D342 0000	351	OPTCMD10	LB R4,0(R2)		ELI03510
0C64	41F0 1282	352	BAL	LINK,OUTCHR	OUTPUT OPTION NAME CHAR	ELI03520
0C68	2621	353	AIS	R2,1		ELI03530
0C6A	2731	354	SIS	R3,1	6 CHARACTERS OUTPUT ?	ELI03540
0C6C	2026	355	BPS	OPTCMD10	NO,LOOP	ELI03550
0C6E	C840 0020	356	LHI	R4,C' '		ELI03560
0C72	41F0 1282	357	BAL	LINK,OUTCHR	OUTPUT ONE SPACE	ELI03570
0C76	4852 0000	358	LH	R5,0(R2)	R5 = OPTION VALUE	ELI03580
0C7A	2404	359	LIS	R0,4		ELI03590
0C7C	41F0 112A	360	BAL	LINK,RSHEX	WRITE OPTION VALUE IN HEX (4 DIGITS)	ELI03600
0C80	D300 0A10	361	LB	R0,10		ELI03610
0C84	2701	362	SIS	R0,1	CONSOLE = CRT ?	ELI03620
0C86	213D	363	BNZS	OPTCMD12	BRANCH: NO.	ELI03630
0C88	2661	364	AIS	R6,1	INCREMENT LINE COUNTER.	ELI03640
0C8A	C560 0014	365	CLHI	R6,20	PAGE FULL ?	ELI03650
0C8E	2189	366	BLS	OPTCMD12	NO	ELI03660
0C90	2460	367	LIS	R6,0	INITIALIZE LINE COUNT	ELI03670
0C92	41F0 1310	368	OPTCMD11	BAL LINK,GETCHR		ELI03680
0C96	274D	369	SIS	R4,13	CR ?	ELI03690
0C98	4330 0AF6	370	B4	OPTIN	TO ACCEPT NEXT COMMAND	ELI03700
0C9C	2643	371	AIS	R4,3	LF ?	ELI03710
0C9E	2036	372	BNZS	OPTCMD11	IF YES, PRINT NEXT PAGE	ELI03720
0CA0	41F0 1274	373	OPTCMD12	BAL LINK,CRLF		ELI03730
0CA4	41F0 1358	374	BAL	LINK,TSTBRK	EXIT IF 'BREAK' PRESSED.	ELI03740
0CA8	2626	375	AIS	R2,6		ELI03750
0CAA	C520 1864	376	CLHI	R2,OPTEND2	ALL PRINTING OPTIONS DONE ?	ELI03760
0CAE	4280 0C5E	377	BL	OPTCMD9	NO,LOOP FOR NEXT ONE	ELI03770
0CB2	4300 0AFA	378	B	OPTIN1	TO ACCEPT NEXT COMMAND	ELI03780
		379	*-----*			ELI03790

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OCB6	C510	1810	380	LOOK4	CLHI	R1,TEST	'TEST' OPTION ?	ELI03800
OCBA	4330	0D2E	381		BE	TESTOP		ELI03810
			382	*		TO PROCESS COMMANDS OTHER THAN 'TEST', 'OPTION'.		ELI03820
			383	*				ELI03830
OCBE	274D		384		SIS	R4,13	OPT FOLLOWED BY CR ?	ELI03840
OCC0	033C		385		BZR	R12	YES, ERROR	ELI03850
OCC2	41E0	10C6	386		BAL	R14,OPTVAL	GET OPTION VALUE IN R6	ELI03860
OCC6	274D		387		SIS	R4,13	TERMINATED BY CR ?	ELI03870
OCC8	023C		388		BNZR	R12	IF NO, BRANCH	ELI03880
OCCA	48E1	0008	389		LH	R14,8(R1)	GET OPTION CHECK ROUTINE ADDRESS	ELI03890
OCCE	2332		390		BZS	LOOK5		ELI03900
OCDO	01FE		391		BALR	R15,R14	LINK OPTION CHECK ROUTINE	ELI03910
			392	*			RETURN HERE	ELI03920
OCD2	4061	0006	393	LOOK5	STH	R6,6(R1)	STORE OPTION VALUE	ELI03930
OCD6	4300	0AF6	394		B	OPTIN	TO ACCEPT NEXT COMMAND	ELI03940
			395	*				ELI03950
OCDA	C360	FFFE	396	ZERONE	THI	R6,X'FFFE'	IGNORE LSB	ELI03960
OCDE	033F		397		BZR	R15	OKAY	ELI03970
OCE0	030C		398		BR	R12	ERROR RETURN	ELI03980
			399	*				ELI03990
OCE2	C560	0400	400	ADR	CLHI	R6,X'400'	(R6) = 10 BIT DEVICE ADDRESS	ELI04000
OCE6	028F		401		BLR	R15	RETURN TO LOOK5	ELI04010
OCE8	030C		402		BR	R12		ELI04020
			403	*				ELI04030
OCEA	C560	000F	404	LEVEL	CLHI	R6,15	(R6) = INTERRUPT LEVEL HEX DIGIT	ELI04040
OCCE	028F		405		BLR	R15	RETURN TO LOOK5	ELI04050
OCFO	030C		406		BR	R12		ELI04060
			407	*				ELI04070
			408	*		TO CHECK THAT OPTION ENTRY IN R6 IS IN DECIMAL DIGITS.		ELI04080
			409	*		TO CONVERT DECIMAL ENTRY IN R6 TO HEX VALUE AND		ELI04090
			410	*		STORE IT @ 0(R5).		ELI04100
			411	*				ELI04110
OCF2	D000	2030	412	DECHEX	STM	R0,RSAVE		ELI04120
OCF6	2400		413		LIS	R0,0	ACCUMULATOR	ELI04130
OCF8	2410		414		LIS	R1,0	TABLE INDEX	ELI04140
OCFA	2420		415		LIS	R2,0	SHIFT COUNTER	ELI04150
OCFC	0836		416	DECLP1	LDAR	R3,R6	COPY INPUT VALUE	ELI04160
OCFE	CC32	0000	417		SRAL	R3,0(R2)		ELI04170
OD02	4330	0D24	418		BZ	DECHEX1	TO RETURN	ELI04180
OD06	C430	000F	419		NHI	R3,15		ELI04190
OD0A	C530	000A	420		CLHI	R3,10	VALID DECIMAL DIGIT ?	ELI04200
OD0E	038C		421		BNLR	R12	IF NOT, ERROR.	ELI04210
OD10	4871	176E	422		LDA	R7,DECTAB(R1)	1,10,....,10000	ELI04220
OD14	2731		423	DECLP2	SIS	R3,1		ELI04230
OD16	2113		424		BMS	DECLP3		ELI04240
OD18	0A07		425		AAR	R0,R7	ADD IN CURRENT VALUE	ELI04250
OD1A	2203		426		BS	DECLP2		ELI04260
OD1C	2624		427	DECLP3	AIS	R2,4	INCREMENT SHIFTER	ELI04270
OD1E	2612		428		AIS	R1,ADC	INCREMENT POINTER	ELI04280
OD20	4300	0CFC	429		B	DECLP1		ELI04290
OD24	4005	0000	430	DECHEX1	STH	R0,0(R5)	STORE HEX OPTION VALUE	ELI04300
OD28	D100	2030	431		LM	R0,RSAVE		ELI04310
OD2C	030F		432		BR	LINK	RETURN	ELI04320

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		433	*-----			ELI04330
		434	* TEST OPTION PROCESS ROUTINE			ELI04340
		435	*			ELI04350
002E	274D	436	TSTOP	SIS R4,13	'TEST' FOLLOWED BY (CR) ?	ELI04360
0030	2138	437		BNZS TSTOP1		ELI04370
0032	4800 187E	438		LH R0,DEFTSTS	YES, SET TEST OPTION TO	ELI04380
0036	4000 1816	439		STH R0,TEST+6	FIRST TEST WORD	ELI04390
003A	4800 1880	440		LH R0,DEFTSTS+2	ALL DEFAULT TESTS IN PROGRAM	ELI04400
003E	4000 1818	441		STH R0,TEST+8	SECOND TEST WORD	ELI04410
0042	4300 0AF6	442		B OPTIN	TO ACCEPT NEXT COMMAND	ELI04420
		443	*			ELI04430
0046	4850 1882	444	TSTOP1	LH R5,MAXTST		ELI04440
004A	2470	445		LIS R7,0	TEST BIT ACCUMULATORS	ELI04450
004C	2480	446		LIS R8,0		ELI04460
004E	41E0 10C6	447	TSTOP2	BAL R14,OPTVAL	GET OPTION VALUE IN R6	ELI04470
0052	0556	448		CLAR R5,R6		ELI04480
0054	028C	449		BLR R12	ERROR: INVALID TEST NUMBER	ELI04490
0056	C560 0010	450		CLHI R6,16	R6 < 16 ?	ELI04500
005A	2385	451		BNLS TSTOP3	NO	ELI04510
005C	41E0 1102	452		BAL R14,UNARY	GET UNARY OPERAND IN R3	ELI04520
0060	0673	453		OAR R7,R3	SET CURRENT BIT	ELI04530
0062	2306	454		BS TSTOP4		ELI04540
0064	CB60 0010	455	TSTOP3	SHI R6,16	R6 = 0-F	ELI04550
0068	41E0 1102	456		BAL R14,UNARY		ELI04560
006C	0683	457		OAR R8,R3	SET CURRENT BIT	ELI04570
006E	274D	458	TSTOP4	SIS R4,13	TERMINATED BY CR ?	ELI04580
0070	4230 0D4E	459		BNZ TSTOP2		ELI04590
0074	4070 1816	460		STH R7,TEST+6	STORE VALID SELECTED TESTS	ELI04600
0078	4080 1818	461		STH R8,TEST+8		ELI04610
007C	4300 0AF6	462		B OPTIN	TO ACCEPT NEXT COMMAND	ELI04620
		463	*-----			ELI04630
		464	*			ELI04640
0080	41F0 1274	465	RUNIT	BAL LINK,CRLF		ELI04650
0084	24F0	466		LIS R15,0		ELI04660
0086	40F0 1754	467		STH R15,WASDU	RESET DU FLAGS	ELI04670
008A	40F0 1756	468		STH R15,WASDU1		ELI04680
008E	240F	469		LIS R0,15	TO FIND HIGHEST SELECTED TEST NO.	ELI04690
0090	4810 1818	470		LH R1,TEST+8	CHECK SECOND TEST HW	ELI04700
0094	9011	471	KEEP1	SRLS R1,1		ELI04710
0096	218B	472		BCS FOUND1	R0 = F-0	ELI04720
0098	2701	473		SIS R0,1		ELI04730
009A	2213	474		BNMS KEEP1	TRY NEXT DIGIT	ELI04740
009C	240F	475		LIS R0,15	INITIALIZE AGAIN	ELI04750
009E	4810 1816	476		LH R1,TEST+6	CHECK FIRST TEST HW	ELI04760
00A2	9011	477	KEEP2	SRLS R1,1		ELI04770
00A4	2186	478		BCS FOUND2	R0 = F-0 = TEST #	ELI04780
00A6	2701	479		SIS R0,1		ELI04790
00A8	2213	480		BNMS KEEP2	LOOP	ELI04800
00AA	030C	481		BR R12	TEST NOT SELECTED	ELI04810
00AC	CA00 0010	482	FOUND1	AHI R0,16	ADJUST TEST # FOR SECOND HW	ELI04820
00B0	4000 1752	483	FOUND2	STH R0,SELTST	HIGHEST SELECTED TEST NUMBER	ELI04830
00B4	4800 0A10	484		LH R0,I0		ELI04840
00B8	4000 2020	485		STH R0,I0SAVE	RESTORE USER'S I/O CHOICE	ELI04850

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0DB0	41F0 1274	486	BAL	LINK,CRLF		ELI04860
0DC0	41F0 1FBA	487	BAL	LINK,INIT	LINK USER INITIALIZATION ROUTINE	ELI04870
		488	*			ELI04880
		489	*			ELI04890
		490	*	RESET TEST PARAMETERS		ELI04900
		491	*			ELI04910
0DC4	2400	492	INITRET	LIS R0,0	RETURN HERE FROM USER'S INIT ROUTINE	ELI04920
0DC6	4000 174E	493	STH	R0,ISITERR	RESET ERROR FLAG	ELI04930
0DCA	4000 1758	494	STH	R0,TOTAL	RESET TOTAL	ELI04940
0DCE	4000 175A	495	STH	R0,TOTERR	RESET TOTERR	ELI04950
0DD2	4000 1754	496	STH	R0,WASDU	RESET WASDU	ELI04960
0DD6	C810 3030	497	LHI	R1,C'00'		ELI04970
0DDA	4010 178E	498	STH	R1,MTESTNO	RESET THESE FLAGS TO C'00'	ELI04980
0DDE	4010 1798	499	STH	R1,ETESTNO		ELI04990
0DE2	4010 179A	500	STH	R1,ERRNO		ELI05000
0DE6	41F0 145A	501	BAL	LINK,LCORE	SET UP LOW CORE	ELI05010
		502	*			ELI05020
		503	*	START SELECTION FROM TEST 0		ELI05030
		504	*			ELI05040
0DEA	2400	505	KEEP3	LIS R0,0		ELI05050
0DEC	4000 175C	506	STH	R0,BTESTNO	RESET BINARY TEST NUMBER	ELI05060
0DF0	4000 1760	507	STH	R0,NEXTST	RESET NEXT TEST #	ELI05070
		508	*			ELI05080
		509	*	TO FIND THE NEXT SELECTED TEST.		ELI05090
		510	*			ELI05100
0DF4	4820 1760	511	KEEP4	LH R2,NEXTST	GET NEXT TEST #	ELI05110
0DF8	2408	512	KEEP41	LIS R0,8		ELI05120
0DFA	910C	513		SLHLS R0,12	R0 = X'8000'	ELI05130
0DFC	CC02 0000	514		SRHL R0,0(R2)	R0 = NEXT TEST BIT	ELI05140
0E00	C520 0010	515		CLHI R2,X'10'	NEXT TEST < 16	ELI05150
0E04	2185	516		BLS KEEP42		ELI05160
0E06	4400 1818	517		NH R0,TEST+8	LOOK AT TEST HW 2	ELI05170
0E0A	2137	518		BNZS KEEP5		ELI05180
0E0C	2304	519		BS KEEP43		ELI05190
0E0E	4400 1816	520	KEEP42	NH R0,TEST+6	LOOK AT TEST HW 1	ELI05200
0E12	2133	521		BNZS KEEP5		ELI05210
0E14	2621	522	KEEP43	AIS R2,1		ELI05220
0E16	220F	523		BS KEEP41	LOOP FOR NEXT TEST #	ELI05230
0E18	4020 175C	524	KEEP5	STH R2,BTESTNO	CURRENT TEST #	ELI05240
0E1C	0812	525		LDAR R1,R2	R1 = TEST # IN BINARY	ELI05250
0E1E	2621	526		AIS R2,1		ELI05260
0E20	4020 1760	527		STH R2,NEXTST		ELI05270
0E24	2402	528		LIS R0,2	SET DIGITS TO PRINT = 2	ELI05280
0E26	C820 178E	529		LHI R2,MTESTNO	R2 = A(MTESTNO)	ELI05290
0E2A	41F0 1190	530		BAL LINK,HEXASC	STORE TEST # IN ASCII @ MTESTNO	ELI05300
0E2E	482C 178E	531		LH R2,MTESTNO		ELI05310
0E32	4020 1798	532		STH R2,ETESTNO	STORE TEST # IN ASCII @ ETESTNO	ELI05320
0E36	41F0 1358	533		BAL LINK,TSTBRK	TEST BREAK	ELI05330
0E3A	C850 1788	534		LHI R5,TSTMSG		ELI05340
0E3E	41F0 11F0	535		BAL LINK,PRINT	PRINT 'TEST NN'	ELI05350
0E42	2400	536		LIS R0,0		ELI05360
0E44	4000 1750	537		STH R0,NOERR	RESET ERROR FLAG	ELI05370
0E48	4000 175E	538		STH R0,COUNT	RESET COUNT	ELI05380

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0E4C 4810 0A24      539  KEEP6  LH    R1,PSW2      DISABLE INTERRUPTS      ELI05390
0E50 9501           540          EPSR  R0,R1          ELI05400
0E52 4820 175C      541          LH    R2,BTESTNO     R2 = TEST #            ELI05410
0E56 9121           542          SLLS  R2,LADC         ELI05420
0E58 4812 1884      543          LDA   R1,TESTS(R2)   ELI05430
0E5C 0301           544          BR    R1              GO TO TEST MODULE      ELI05440
545 *-----*
546 *
547 * TEST MODULE END ROUTINE
548 *
0E5E 4810 0A24      549  TSTENO  LH    R1,PSW2      DISABLE INT @ PROCESSOR LEVEL  ELI05480
0E62 9501           550          EPSR  R0,R1          ELI05490
0E64 4800 175E      551          LH    R0,COUNT       INCREMENT COUNT        ELI05510
0E68 2601           552          AIS  R0,1             ELI05520
0E6A 4000 175E      553          STH  R0,COUNT       ELI05530
0E6E 4500 1822      554          CLH  R0,LOOP+6       IF COUNT > LOOP,      ELI05540
0E72 2385           555          BNLS  KEEP7          GO TO NEXT TEST MODULE  ELI05550
0E74 41F0 1358      556          BAL  LINK,TSTBRK     IF BREAK GO TO OPTIN   ELI05560
0E78 4300 0E4C      557          B    KEEP6           OTHERWISE, REPEAT SAME TEST  ELI05570
0E7C 4800 1750      558  KEEP7  LH    R0,NOERR      LOOK @ ERROR FLAG      ELI05580
0E80 2135           559          BNZS  KEEP71         ELI05590
0E82 C850 17AE      560          LHI  R5,NOERMSG     ELI05600
0E86 41F0 11F0      561          BAL  LINK,PRINT      PRINT "NO ERROR"       ELI05610
0E8A 4810 175C      562  KEEP71 LH    R1,BTESTNO     GET TEST #             ELI05620
0E8E 4510 1752      563          CLH  R1,SELTST      IS THE LAST SELECTED TEST DONE ?  ELI05630
0E92 4280 0DF4      564          BL   KEEP4           NO, GO SELECT NEXT TEST  ELI05640
565 *
566 * ALL THE SELECTED TESTS HAVE NOW RUN
567 *
0E96 4200 0E96      568  ABORT  NOP    *           COME HERE TO ABORT TEST SEQUENCE.  ELI05680
0E9A 4810 0A24      569          LH    R1,PSW2       ELI05690
0E9E 9501           570          EPSR  R0,R1          PSW = 30F0             ELI05700
0EA0 41F0 13C6      571          BAL  LINK,TSTDU     RETURN WITH R1 = DU BIT  ELI05710
0EA4 4230 0ED4      572          BNZ  KEEP9          IF DU, DISPLAY TOTAL   ELI05720
0EA8 4810 1756      573          LH   R1,WASDU1     WAS IT EVER ?          ELI05730
0EAC 4230 0F0A      574          BNZ  KEEP10         YES, PRINT TOTAL, TOTERR  ELI05740
0EB0 41F0 1358      575          BAL  LINK,TSTBRK    ELI05750
0EB4 4810 182E      576          LH   R1,CONTIN+6   IF CONTIN = 1,         ELI05760
0EB8 4230 0ED8      577          BNZ  ABORT2         INCREMENT & GO TO TEST 0  ELI05770
0EBC 41F0 13FC      578          BAL  LINK,SETKB     KB DEVICE = LIST DEVICE  ELI05780
0EC0 C850 17FE      579          LHI  R5,EOTMSG     ELI05790
0EC4 41F0 11F0      580          BAL  LINK,PRINT     'END OF TEST'         ELI05800
0EC8 48F0 183A      581          LH   LINK,NOMSG+6  ELI05810
0ECC 4230 0F04      582          BNZ  KEEP92         ELI05820
0ED0 4300 0AF6      583          B    OPTIN          ELI05830
584 *
585 *-----*
586 * ROUTINE INCREMENTS,DISPLAYS & CHECKS 'TOTAL'
587 *
0ED4 4010 1754      588  KEEP9  STH  R1,WASDU     SET 'WASDU' FLAG      ELI05870
0ED8 4810 1758      589  ABORT2 LH    R1,TOTAL     INCREMENT TOTAL        ELI05880
0EDC 2611           590          AIS  R1,1           ELI05890
0EDE 4010 1758      591          STH  R1,TOTAL      ELI05910

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0EE2 4810 1758          592 KEEP91 LH R1,TOTAL          ELI05920
0EE6 C510 7FFF          593          CLHI R1,X'7FFF'          TOTAL < MAX RETAINABLE ? ELI05930
0EEA 2389              594          BNLS HALT9              ELI05940
0EEC 4800 175C          595          LH R0,BTESTNO          R0 = CURRENT TEST # ELI05950
0EF0 4500 1752          596          CLH R0,SELTST          IS IT LAST TEST ? ELI05960
0EF4 4280 0DF4          597          3L KEEP4              NO, GO TO NEXT TEST ELI05970
0EF8 4300 0DEA          598          B KEEP3              GO TO TEST 0 ELI05980
                          599          * ELI05990
0EFC C810 080F          600 HALT9 LHI R1,X'80F'          ELI06000
0F00 9114              601          SLHLS R1,4              (R1) = X'80F0' ELI06010
0F02 9521              602          EPSR R2,R1              HALT PROCESSOR ELI06020
                          603          * ELI06030
                          604 * WHEN EXE/RUN IS PRESSED, PRINT TOTAL & TOTERR ELI06040
                          605 * ELI06050
0F04 41F0 13C6          606 KEEP92 BAL LINK,TSTDU          SEE IF LIST DEV IS ON ELI06060
0F08 2036              607          BNZS HALT9              NO, HALT ELI06070
0F0A 2400              608 KEEP10 LIS R0,0              ELI06080
0F0C 4000 1754          609          STH R0,WASDU          RESET FLAG ELI06090
0F10 41F0 1274          610          BAL LINK,CRLF          ELI06100
0F14 C850 179E          611          LHI R5,TOTMSG          ELI06110
0F18 4050 174E          612          STH R5,ISITERR          ELI06120
0F1C 41F0 11F0          613          BAL LINK,PRINT          PRINT 'TOTAL TOTERR' ELI06130
0F20 2404              614          LIS R0,4              TO PRINT 4 HEX DIGITS ELI06140
0F22 4850 1758          615          LH R5,TOTAL              ELI06150
0F26 41F0 112A          616          BAL LINK,R5HEX          PRINT TOTAL IN HEX ELI06160
0F2A 2434              617          LIS R3,4              ELI06170
0F2C C840 0020          618          LHI R4,C' '              SPACE ELI06180
0F30 41F0 1282          619 KEEP101 BAL LINK,OUTCHR          OUTPUT IT ELI06190
0F34 2731              620          SIS R3,1              ELI06200
0F36 2023              621          BPS KEEP101              4 TIMES ELI06210
0F38 2404              622          LIS R0,4              TO PRINT 4 HEX DIGITS ELI06220
0F3A 4850 175A          623          LH R5,TOTERR          ELI06230
0F3E 41F0 112A          624          BAL LINK,R5HEX          PRINT TOTERR IN HEX ELI06240
0F42 4300 0AF6          625          B OPTIN              GO TO BEGINNING ELI06250
                          626 ***** ELI06260
                          627 * ELI06270
0F46 2401              628 DISPLAY LIS R0,1          DISPLAY PANEL ADDRESS ELI06280
0F48 DE00 1725          629          OC R0,INCR              INCREMENTAL MODE ELI06290
0F4C 481F 0002          630          LH R1,2(LINK)          GET 2ND PARAMETER ADDRESS ELI06300
0F50 4811 0000          631          LH R1,0(R1)              GET DATA ELI06310
0F54 9411              632          EXBR R1,R1              ELI06320
0F56 9801              633          WHR R0,R1              WRITE DATA ELI06330
0F58 481F 0000          634          LH R1,0(LINK)          GET 1ST PARAMETER ADDRESS ELI06340
0F5C 4811 0000          635          LH R1,0(R1)              GET DATA ELI06350
0F60 9411              636          EXBR R1,R1              ELI06360
0F62 9801              637          WHR R0,R1              WRITE DATA TO D1,D2 ELI06370
0F64 DE00 1724          638          OC R0,NORM              NORMAL MODE ELI06380
0F68 430F 0004          639          B 4(LINK)              RETURN ELI06390
                          640 * ELI06400
                          641 ***** ELI06410
                          642 * ELI06420
                          643 * ERROR ROUTINES (OVERRIDE NOMSG OPTION) ELI06430
                          644 * ELI06440

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0F6C	0000	20F0	645	ERR	STM	R0,ERRSAVE	STORE REGISTERS	ELI06450
0F70	4120	0FF6	646		BAL	R2,ERRCOM	RETURN IF LIST DEVICE IS ON	ELI06460
0F74	41E0	102A	647		BAL	RET,ERR1	PRINT 'ERROR TTNN'	ELI06470
0F78	2400		648	ERRCOM2	LIS	R0,0		ELI06480
0F7A	4000	174E	649		STH	R0,ISITERR	RESET ERROR FLAG	ELI06490
0F7E	4820	0A22	650		LH	R2,PSW		ELI06500
0F82	9502		651		EPSR	R0,R2		ELI06510
0F84	0100	20F0	652		LM	R0,ERRSAVE	RESTORE REGISTERS	ELI06520
0F88	030F		653		RR	LINK	RETURN TO TEST	ELI06530
0F8A	0000	20F0	654	ERRD	STM	R0,ERRSAVE	STORE REGISTERS	ELI06540
0F8E	4120	0FF6	655		BAL	R2,ERRCOM	RETURN IF LIST DEVICE IS ON	ELI06550
0F92	41E0	102A	656		BAL	RET,ERR1	PRINT 'ERROR TTNN'	ELI06560
0F96	41E0	1034	657		BAL	RET,ERRD1	PRINT 'DEV DDD'	ELI06570
0F9A	4300	0F78	658		B	ERRCOM2		ELI06580
0F9E	0000	20F0	659	ERRS	STM	R0,ERRSAVE	STORE REGISTERS	ELI06590
0FA2	4120	0FF6	660		BAL	R2,ERRCOM	RETURN IF LIST DEVICE IS ON	ELI06600
0FA6	41E0	102A	661		BAL	RET,ERR1	PRINT 'ERROR TTNN'	ELI06610
0FAA	41E0	104C	662		BAL	RET,ERRS1	PRINT 'STA SS'	ELI06620
0FAE	4300	0F78	663		B	ERRCOM2		ELI06630
0FB2	0000	20F0	664	ERRDS	STM	R0,ERRSAVE	STORE REGISTERS	ELI06640
0FB6	4120	0FF6	665		BAL	R2,ERRCOM	RETURN IF LIST DEVICE IS ON	ELI06650
0FBA	41E0	102A	666		BAL	RET,ERR1	PRINT 'ERROR TTNN'	ELI06660
0FBE	41E0	1064	667		BAL	RET,ERRDS1	PRINT 'DEV DDD STA SS'	ELI06670
0FC2	4300	0F78	668		B	ERRCOM2		ELI06680
0FC6	0000	20F0	669	ERRL	STM	R0,ERRSAVE	STORE REGISTERS	ELI06690
0FCA	40F0	171E	670		STH	R15,0LOC	STORE ERROR LOC TO PRINT	ELI06700
0FCE	4120	0FF6	671		BAL	R2,ERRCOM	RETURN IF LIST DEVICE IS ON	ELI06710
0FD2	41E0	102A	672		BAL	RET,ERR1	PRINT 'ERROR TTNN'	ELI06720
0FD6	41E0	108A	673		BAL	RET,ERRL1	PRINT 'LOC LLLL'	ELI06730
0FDA	4300	0F78	674		B	ERRCOM2		ELI06740
0FDE	0000	20F0	675	ERRALL	STM	R0,ERRSAVE	STORE REGISTERS	ELI06750
0FE2	4120	0FF6	676		BAL	R2,ERRCOM	RETURN IF LIST DEVICE IS ON	ELI06760
0FE6	41E0	102A	677		BAL	RET,ERR1	PRINT 'ERROR TTNN'	ELI06770
0FEA	41E0	1064	678		BAL	RET,ERRDS1	PRINT 'DEV DDD STA SS'	ELI06780
0FEE	41E0	10A2	679		BAL	RET,ERRPL1	PRINT 'PSW PPPP LOC LLLL'	ELI06790
0FF2	4300	0F78	680		B	ERRCOM2		ELI06800
			681	*				ELI06810
			682	*		COMMON ERROR ROUTINE		ELI06820
			683	*				ELI06830
OFF6	4020	176C	684	ERRCOM	STA	R2,COMRET	STORE RETURN ADDRESS	ELI06840
OFFA	4810	0A24	685		LH	R1,PSW2		ELI06850
OFFE	9501		686		EPSR	R0,R1	DISABLE INT. @ PROCESSOR LEVEL	ELI06860
1000	41F0	13C6	687		BAL	LINK,TSTDU	GET LIST DEVICE DU BIT IN R1	ELI06870
1004	2138		688		BNZS	ERRCOM1	BRANCH IF OFF-LINE	ELI06880
1006	4020	174E	689		STH	R2,ISITERR	SET ERROR FLAG	ELI06890
100A	4020	1750	690		STH	R2,NOERR		ELI06900
100E	4820	176C	691		LDA	R2,COMRET		ELI06910
1012	0302		692		BR	R2	GO, PRINT ERROR MESSAGE	ELI06920
			693	*				ELI06930
1014	4810	175A	694	ERRCOM1	LH	R1,TOTERR	LIST DEVICE IS OFF	ELI06940
1018	2611		695		AIS	R1,1		ELI06950
101A	4010	175A	696		STH	R1,TOTERR	INCREMENT TOTERR	ELI06960
101E	C510	7FFF	697		CLHI	R1,X'7FFF'	TOTERR < MAX RETAINABLE ?	ELI06970

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1022	4280	0EE2	698	BL	KEEP91	NO. ABORT CURRENT TEST & GOTO NEXT	ELI06980	
1026	4300	0EFC	699	B	HALT9	YES, HALT PROCESSOR	ELI06990	
			700	*-----*			ELI07000	
			701	* MESSAGE PRINT ROUTINES			(DO NOT OVERRIDE NOMSG OPTION)	ELI07010
			702	*			ELI07020	
			703	* TO PRINT 'ERROR TTNN'			ELI07030	
			704	*			ELI07040	
102A	C850	1792	705	ERR1	LHI R5,ERRMSG	PRINT 'ERROR TTNN'	ELI07050	
102E	41F0	11F0	706		BAL LINK,PRINT	TT = TEST #, NN = ERROR #	ELI07060	
			707	*			ELI07070	
1032	030E		708		BR RET	RETURN	ELI07080	
			709	*			ELI07090	
			710	* TO PRINT 'DEV DDD'			ELI07100	
			711	*			ELI07110	
1034	2403		712	ERRD1	LIS R0,3	SET UP DIGITS = 3	ELI07120	
1036	4810	1720	713		LH R1,ERRDEV	R1 = ERROR DEV # IN BINARY	ELI07130	
103A	C820	17CC	714		LHI R2,ASCIDEV2		ELI07140	
103E	41F0	1190	715		BAL LINK,HEXASC	CONVERT IT TO ASCII	ELI07150	
1042	C850	17C8	716		LHI R5,DEVMSG2		ELI07160	
1046	41F0	11F0	717		BAL LINK,PRINT	PRINT 'DEV DD'	ELI07170	
104A	030E		718		BR RET	RETURN	ELI07180	
			719	*			ELI07190	
			720	* TO PRINT 'STA SS'			ELI07200	
			721	*			ELI07210	
104C	2402		722	ERRS1	LIS R0,2	SET UP DIGITS = 2	ELI07220	
104E	D310	1722	723		LB R1,ERRSTA	R1 = ERROR STATUS	ELI07230	
1052	C820	17C4	724		LHI R2,ASCISTA		ELI07240	
1056	41F0	1190	725		BAL LINK,HEXASC	CONVERT IT TO ASCII	ELI07250	
105A	C850	17C0	726		LHI R5,STAMSG		ELI07260	
105E	41F0	11F0	727		BAL LINK,PRINT	PRINT 'STA SS'	ELI07270	
1062	030E		728		BR RET	RETURN	ELI07280	
			729	*			ELI07290	
			730	* TO PRINT 'DEV DDD STA SS'			ELI07300	
			731	*			ELI07310	
1064	2403		732	ERRDS1	LIS R0,3	SET UP DIGITS = 3	ELI07320	
1066	4810	1720	733		LH R1,ERRDEV	R1 = ERROR DEV #	ELI07330	
106A	C820	17BC	734		LHI R2,ASCIDEV		ELI07340	
106E	41F0	1190	735		BAL LINK,HEXASC	CONVERT IT TO ASCII	ELI07350	
1072	2402		736		LIS R0,2	SET UP DIGITS = 2	ELI07360	
1074	D310	1722	737		LB R1,ERRSTA	R1 = ERROR STATUS	ELI07370	
1078	C820	17C4	738		LHI R2,ASCISTA		ELI07380	
107C	41F0	1190	739		BAL LINK,HEXASC	CONVERT IT TO ASCII	ELI07390	
1080	C850	1788	740		LHI R5,DEVMSG		ELI07400	
1084	41F0	11F0	741		BAL LINK,PRINT	PRINT 'DEV DD STA SS'	ELI07410	
1088	030E		742		BR RET	RETURN	ELI07420	
			743	*			ELI07430	
			744	* TO PRINT 'LOC LLLL'			ELI07440	
			745	*			ELI07450	
108A	2404		746	ERRL1	LIS R0,4	SET UP DIGITS = 4	ELI07460	
108C	4810	171E	747		LH R1,OLC	R1 = OLD LOC	ELI07470	
1090	C820	17E0	748		LHI R2,ASCILC		ELI07480	
1094	41F0	1190	749		BAL LINK,HEXASC	CONVERT IT TO ASCII	ELI07490	
1098	C850	17DC	750		LHI R5,LOCMSG		ELI07500	

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109C	41F0 11F0	751	BAL	LINK,PRINT	PRINT 'LOC LLLL'	ELI07510
10A0	030E	752	BR	RET	RETURN	ELI07520
		753	*			ELI07530
		754	*	TO PRINT 'PSW PPPP LOC LLLL'		ELI07540
		755	*			ELI07550
10A2	2404	756	ERRPL1	LIS R0,4	SET UP DIGITS = 4	ELI07560
10A4	4810 171A	757	LH	R1,OPSW	R1 = OLD PSW	ELI07570
10A8	C820 17D6	758	LHI	R2,ASCIPSW		ELI07580
10AC	41F0 1190	759	BAL	LINK,HEXASC	CONVERT IT TO ASCII	ELI07590
10B0	4810 171E	760	LH	R1,LOLOC	R1= OLD LOC	ELI07600
10B4	C820 17E0	761	LHI	R2,ASCILOC		ELI07610
10B8	41F0 1190	762	BAL	LINK,HEXASC	CONVERT IT TO ASCII	ELI07620
10BC	C850 1702	763	LHI	R5,PSWMSG		ELI07630
10C0	41F0 11F0	764	BAL	LINK,PRINT	PRINT 'PSW PPPP LOC LLLL'	ELI07640
10C4	030E	765	BR	RET	RETURN	ELI07650
		766	*	*****		ELI07660
		767	*	TO OBTAIN OPTION VALUE IN R6	(16 BITS, TARGT 16)	ELI07670
		768	*			ELI07680
10C6	2460	769	OPTVAL	LIS R6,0	INITIALIZE ACCUMULATOR	ELI07690
10C8	41F0 1310	770	BAL	R15,GETCHR	GET A CHAR IN R4	ELI07700
10CC	24FF	771	OPTVAL0	LIS R15,15		ELI07710
10CE	044F 1778	772	OPTVAL1	CLB R4,HEXTAB(R15)	SCAN TABLE	ELI07720
10D2	2334	773	BES	OPTVAL2	MATCH	ELI07730
10D4	27F1	774	SIS	R15,1		ELI07740
10D6	2214	775	BNMS	OPTVAL1		ELI07750
10D8	030C	776	BR	R12	ERROR: VALUE NOT IN TABLE.	ELI07760
10DA	9164	777	OPTVAL2	SLLS R6,4	SHIFT LEFT 4	ELI07770
10DC	066F	778	OAR	R6,R15	OR IN CURRENT DIGIT	ELI07780
10DE	41F0 1310	779	OPTVAL3	BAL R15,GETCHR	GET NEXT CHAR	ELI07790
10E2	C540 005F	780	CLHI	R4,X'5F'	IS IT LEFT ARROW ?	ELI07800
10E6	2334	781	BES	OPTVAL5	YES, BRANCH	ELI07810
10E8	C540 0008	782	CLHI	R4,X'08'	BACK SPACE ?	ELI07820
10EC	2133	783	BNES	OPTVAL4	NO, BRANCH	ELI07830
10EE	9064	784	OPTVAL5	SRLS R6,4	THROW AWAY LAST HEX ENTRY	ELI07840
10F0	2209	785	BS	OPTVAL3		ELI07850
10F2	C540 000D	786	OPTVAL4	CLHI R4,13	EXIT IF CR	ELI07860
10F6	033E	787	BER	R14		ELI07870
10F8	C540 002C	788	CLHI	R4,X'2C'	OR COMMA	ELI07880
10FC	4230 10CC	789	BNE	OPTVAL0	LOOP TO PROCESS	ELI07890
1100	030E	790	BR	R14	RETURN	ELI07900
		791	*	-----		ELI07910
		792	*	TO CONVERT (R6) FROM BINARY TO UNARY PATTERN, IN R3		ELI07920
		793	*			ELI07930
1102	2431	794	UNARY	LIS R3,1	INITIALIZE	ELI07940
1104	C560 000F	795	UNARY1	CLHI R6,15	DONE ?	ELI07950
1108	033E	796	BER	R14	RETURN	ELI07960
110A	0A33	797	AAR	R3,R3	NO, SHIFT R3.	ELI07970
110C	2661	798	AIS	R6,1	INCREMENT COUNTER	ELI07980
110E	2205	799	BS	UNARY1		ELI07990
		800	*	-----		ELI08000
		801	*	TO PROVIDE # OF MILLISECONDS DELAY SPECIFIED BY R0		ELI08010
		802	*			ELI08020
1110	D000 2030	803	TIMER	STM R0,RSAVE	SAVE REGISTERS	ELI08030

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1114 2410          804 $TIMER1 LIS R1,0          ELI08040
1116 2421          805          LIS R2,1          ELI08050
1118 4830 0A1E     806          LH R3,TIME        R3 = TIME CONSTANT FOR 1 MS DELAY ELI08060
111C C110 111C     807          BXL R1,*          ELI08070
1120 2701          808          SIS R0,1          ELI08080
1122 2037          609          BNZS $TIMER1      LOOP TILL SPECIFIED DELAY ELI08090
1124 0100 2030     810          LM R0,RSRVE        RESTORE REGISTERS ELI08100
1128 030F          811 $TIMXT BR LINK          RETURN ELI08110
812 *-----*
813 * R5HEX PRINTS CONTENTS OF R5 IN HEX ELI08130
814 * PRINTS UPTO 4 DIGITS (8 DIGITS, TARGT 32) ELI08140
815 * ELI08150
112A 0000 2030     816 R5HEX STM R0,RSRVE        STORE REGISTERS ELI08160
112E 0820          817          LDAR R2,R0        R2 = # OF DIGITS TO BE PRINTED ELI08170
1130 2721          818          SIS R2,1          ELI08180
1132 4210 114E     819          BM R5XB          ELI08190
1136 9122          820          SLLS R2,2        R2 = 4(DIGITS-1) ELI08200
1138 0845          821 R5X LDAR R4,R5          ELI08210
113A CC42 0000     822          SRAL R4,0(R2)      ELI08220
113E C440 000F     823          NHI R4,15        R4 = HEX DIGIT ELI08230
1142 0344 1778     824          LB R4,HEXTAB(R4)      ELI08240
1146 41F0 1282     825 R5XA BAL R15,OUTCHR      ELI08250
114A 2724          826          SIS R2,4          ELI08260
114C 221A          827          BNMS R5X        LOOP TILL ALL DIGITS ELI08270
114E 0100 2030     828 R5XB LM R0,RSRVE        RESTORE REGISTERS ELI08280
1152 030F          829 BR LINK          RETURN ELI08290
830 *-----*
831 * R5BIN PRINTS CONTENTS OF R5 IN BINARY ELI08310
832 * PRINTS UPTO 16 DIGITS ELI08320
833 * ELI08330
1154 0000 2030     834 R5BIN STM R0,RSRVE        STORE REGISTERS ELI08340
1158 0830          835          LDAR R3,R0        R3 = # OF DIGITS TO BE PRINTED ELI08350
115A C810 0010     836          LHI R1,16        ELI08360
115E 0813          837          SAR R1,R3        ELI08370
1160 211C          838          SMS R5B2        EXIT ELI08380
1162 C051 0000     839          SLHL R5,0(R1)      R5 = DATA TO BE PRINTED ELI08390
1166 C840 0030     840 R5B LHI R4,C'0'        ELI08400
116A 9151          841          SLHLS R5,1        ELI08410
116C 2382          842          BNCS R5B1        ELI08420
116E 2641          843          AIS R4,1        IF CARRY, PRINT 1 ELI08430
1170 41F0 1282     844 R5B1 SAL LINK,OUTCHR      ELI08440
1174 2731          845          SIS R3,1        R3 = # OF REMAINING DIGITS ELI08450
1176 2124          846          BPS R5B3        ELI08460
1178 0100 2030     847 R5B2 LM R0,RSRVE        RESTORE REGISTERS ELI08470
117C 030F          848          BR LINK          RETURN ELI08480
117E C330 0003     849 R5B3 TH1 R3,3        4,8 OR 12 DIGITS LEFT ? ELI08490
1182 2135          850          BNZS R5B4        NO ELI08500
1184 C840 0020     851          LHI R4,C' '        YES, OUTPUT ONE SPACE ELI08510
1188 41F0 1282     852          BAL R15,OUTCHR      ELI08520
118C 4300 1166     853 R5B4 B R5B          LOOP FOR NEXT DIGIT ELI08530
854 *-----*
855 * TO CONVERT HEXADECIMAL DATA IN R1 TO ASCII CHAR & STORE @ 0(R2) ELI08550
856 * ELI08560

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1190	D000	2030	857	HEXASC	STM	R0,RSAVE	STORE REGISTERS	ELI08570
1194	0830		858		LDAR	R3,R0	R3 = DIGITS	ELI08580
1196	9132		859		SLLS	R3,2		ELI08590
1198	2734		860		SIS	R3,4	R3 = 4(DIGITS)-4	ELI08600
119A	0841		861	HEXASC1	LDAR	R4,R1	R4 = HEX DATA	ELI08610
119C	CC43	0000	862		SRAL	R4,0(R3)		ELI08620
11A0	C440	000F	863		NHI	R4,15	R4 = HEX DIGIT TO BE CONVERTED	ELI08630
11A4	D344	1778	864		LB	R4,HEXTAB(R4)		ELI08640
11A8	D242	0000	865		STB	R4,0(R2)	STORE ASCII CHAR	ELI08650
11AC	2621		866		AIS	R2,1		ELI08660
11AE	2734		867		SIS	R3,4		ELI08670
11B0	221B		868		BNMS	HEXASC1	LOOP TILL ALL DIGITS	ELI08680
11B2	D100	2030	869		LM	R0,RSAVE	RESTORE REGISTERS	ELI08690
11B6	030F		870		BR	LINK	RETURN	ELI08700
			871	*-----*				ELI08710
			872	* TO CONVERT BINARY DATA IN R1 INTO DECIMAL DIGITS				ELI08720
			873	* AND STORE THEM IN ASCII @ 0(R2)				ELI08730
			874	*				ELI08740
11B8	D000	2030	875	DECASC	STM	R0,RSAVE		ELI08750
11BC	0830		876		LDAR	R3,R0	COPY DIGIT COUNT	ELI08760
11BE	9131		877		SLLS	R3,LADC	ESTABLISH DECTAB INDEX.	ELI08770
11C0	2732		878		SIS	R3,ADC		ELI08780
11C2	2440		879	\$DEC1	LIS	R4,0	CLEAR MODULUS COUNTER	ELI08790
11C4	4853	176E	880		LDA	R5,DECTAB(R3)	LOAD LARGEST REQ. POWER OF 10.	ELI08800
11C8	0515		881	\$DEC2	CLAR	R1,R5	EXCEEDS TEST VALUE ?	ELI08810
11CA	2188		882		BLS	\$DEC3	BRANCH IF YES.	ELI08820
11CC	0B15		883		SAR	R1,R5	DECREMENT TEST VALUE	ELI08830
11CE	2641		884		AIS	R4,1	INCREMENT MODULUS COUNTER	ELI08840
11D0	C540	000A	885		CLHI	R4,10	VALID DECIMAL DIGIT ?	ELI08850
11D4	2086		886		BLS	\$DEC2	BRANCH IF YES; ELSE	ELI08860
11D6	274A		887		SIS	R4,10	FORCE VALID DIGIT.	ELI08870
11D8	2208		888		BS	\$DEC2	REPEAT DECREMENT.	ELI08880
11DA	D344	1778	889	\$DEC3	LB	R4,HEXTAB(R4)	CONVERT MODULUS COUNT TO ASCII	ELI08890
11DE	D242	0000	890		STB	R4,0(R2)	AND STORE AT DESTINATION MSB.	ELI08900
11E2	2621		891		AIS	R2,1	INCREMENT DESTINATION POINTER	ELI08910
11E4	2732		892		SIS	R3,ADC	DECREMENT DECTAB POINTER	ELI08920
11E6	4310	11C2	893		RNM	\$DEC1	FALL THROUGH ON DECTAB UNDERFLOW.	ELI08930
11EA	D100	2030	894		LM	R0,RSAVE	RESTORE USER'S REGISTERS	ELI08940
11EE	030F		895		BR	LINK	RETURN.	ELI08950
			896	*-----*				ELI08960
			897	* TO PRINT THE ASCII MESSAGE				ELI08970
			898	*				ELI08980
11F0	D000	2030	899	PRINT	STM	R0,RSAVE	STORE REGISTERS	ELI08990
11F4	41F0	13C6	900		BAL	LINK,TSTOU		ELI09000
11F8	2335		901		BZS	P1		ELI09010
11FA	4010	1754	902		STH	R1,WASDU	SET FLAG	ELI09020
11FE	4300	126A	903		B	PRINT5	EXIT	ELI09030
1202	4820	1754	904	P1	LM	R2,WASDU		ELI09040
1206	4330	1234	905		BZ	P3		ELI09050
120A	C810	0140	906		LHI	R1,X'140'	DELAY CONSTANT	ELI09060
120E	C800	1000	907		LHI	R0,X'1000'		ELI09070
1212	2701		908		SIS	R0,1		ELI09080
1214	2031		909		BTBS	3,1		ELI09090

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1216	2711	910	SIS	R1,1		ELI09100
1218	2035	911	BTBS	3,5	LOOP TILL TIMEOUT	ELI09110
		912	*		(20 SEC FOR CRT WARM-UP)	ELI09120
121A	2440	913	LIS	R4,0		ELI09130
121C	4040 1754	914	STH	R4,WASDU		ELI09140
1220	2541	915	LCS	R4,1	CHARACTER = X'FF'	ELI09150
1222	4040 1756	916	STH	R4,WASDU1		ELI09160
1226	2434	917	LIS	R3,4		ELI09170
1228	41F0 12A2	918	P2	BAL	LINK,OUTCHR	ELI09180
122C	2731	919	SIS	R3,1		ELI09190
122E	2023	920	BPS	P2		ELI09200
1230	4300 0F0A	921	B	KEEP10	PRINT TOTAL, TOTERR	ELI09210
1234	4800 183A	922	P3	LH	R0,NOMSG+6	ELI09220
1238	2335	923	BZS	PRINT2	NO, PRINT ALL MESSAGES	ELI09230
123A	4800 174E	924	LH	R0,ISITERR		ELI09240
123E	4330 126A	925	BZ	PRINT5	NOT AN ERROR MSG. EXIT	ELI09250
		926	*			ELI09260
1242	0345 0000	927	PRINT2	LB	R4,0(R5)	ELI09270
1246	41F0 1282	928	BAL	LINK,OUTCHR	GET A MESSAGE BYTE	ELI09280
124A	2740	929	SIS	R4,13	OUTPUT IT	ELI09290
124C	2333	930	BZS	PRINT3	CR ?	ELI09300
124E	2651	931	AIS	R5,1	MSG OVER	ELI09310
1250	2207	932	BS	PRINT2	LOOP FOR NEXT CHAR	ELI09320
1252	244A	933	PRINT3	LIS	R4,10	ELI09330
1254	0310 2021	934	LB	R1,IOSAVE+1	GET LIST DEV IDENTIFIER	ELI09340
1258	2713	935	SIS	R1,3	LINE PRINTER ?	ELI09350
125A	2335	936	BZS	PRINT3A	BRANCH IF YES.	ELI09360
125C	41F0 1282	937	BAL	LINK,OUTCHR	LF	ELI09370
1260	2541	938	LCS	R4,1	DEL	ELI09380
1262	2302	939	BS	PRINT3B		ELI09390
1264	2441	940	PRINT3A	LIS	R4,1	ELI09400
1266	41F0 1282	941	PRINT3B	BAL	LINK,OUTCHR	ELI09410
126A	41F0 1358	942	PRINT5	BAL	LINK,TSTBRK	ELI09420
126E	0100 2030	943	LM	R0,RSAVE	RESTORE REGISTERS	ELI09430
1272	030F	944	BR	LINK	RETURN	ELI09440
		945	*-----*			ELI09450
		946	* SMALL SUPPORT ROUTINES			ELI09460
		947	*			ELI09470
		948	* TO OUTPUT CR,LF TO LIST DEVICE			ELI09480
		949	*			ELI09490
1274	0000 2030	950	CRLF	STH	R0,RSAVE	ELI09500
1278	2440	951		LIS	R4,13	ELI09510
127A	41F0 1282	952		BAL	LINK,OUTCHR	ELI09520
127E	4300 1252	953		B	PRINT3	ELI09530
		954	*-----*			ELI09540
		955	* TO OUTPUT A CHARACTER TO THE LIST DEVICE			ELI09550
1282	40F0 1766	956	OUTCHR	STA	R15,OUT.SAV	ELI09560
1286	0300 2021	957		LB	R0,IOSAVE+1	ELI09570
128A	2704	958		SIS	R0,4	ELI09580
128C	4230 12CA	959		BNZ	OUTCHR2	ELI09590
1290	4000 1764	960		STH	R0,PAUSE	ELI09600
1294	41F0 1306	961	OTC.0	BAL	LINK,TSTDU	ELI09610
1298	4230 1306	962		BNZ	OUT0	ELI09620

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129C	9D01	963	SSR	RO,R1	GET CAROUSEL STATUS	ELI09630
129E	2386	964	BFFS	8,OTC.2	BRANCH IF CHAR. IS TO BE READ	ELI09640
12A0	4810 1764	965	OTC.1	LH R1,PAUSE	PAUSED NOW ?	ELI09650
12A4	2038	966	BNZS	OTC.0	YES, LOOP	ELI09660
12A6	4300 12CA	967	B	OUTCHR2	NO, GO OUTPUT CHARACTER	ELI09670
12AA	9B01	968	OTC.2	RDR RO,R1	GET CAROUSEL CHARACTER	ELI09680
12AC	C410 007F	969	NHI	R1,X'7F'		ELI09690
12B0	C810 0012	970	SHI	R1,X'12'	DC2 ?	ELI09700
12B4	2134	971	BNZS	OTC.3		ELI09710
12B6	4010 1764	972	STH	R1,PAUSE		ELI09720
12BA	2308	973	BS	OUTCHR2		ELI09730
12BC	2712	974	OTC.3	SIS R1,2	DC4 ?	ELI09740
12BE	4230 1294	975	BNZ	OTC.0	NO, GO WAIT FOR DC2	ELI09750
12C2	40F0 1764	976	STH	LINK,PAUSE		ELI09760
12C6	4300 1294	977	B	OTC.0		ELI09770
		978	*			ELI09780
12CA	4010 1764	979	OUTCHR2	STH R1,PAUSE	RESET FLAG	ELI09790
12CE	41F0 13C6	980	BAL	LINK,TSTDU	OFF-LINE ?	ELI09800
12D2	4230 1306	981	BNZ	OUT0	BRANCH IF OFF-LINE	ELI09810
12D6	4110 1442	982	BAL	R1,SETUP	SET UP FOR OUTPUT	ELI09820
12DA	9D01	983	OTC.4	SSR RO,R1	WAIT FOR NOT BUSY	ELI09830
12DC	4230 1306	984	BTC	3,OUT0	BRANCH IF OFF-LINE	ELI09840
12E0	C510 000C	985	CLHI	R1,12	PASLA OFFLINE ?	ELI09850
12E4	4330 1306	986	BE	OUT0	BRANCH: YES,	ELI09860
12E8	C310 0008	987	THI	R1,8	BUSY ?	ELI09870
12EC	2039	988	BNZS	OTC.4	WAIT FOR NOT BUSY.	ELI09880
12EE	9A04	989	WOR	RO,R4	OUTPUT DATA BYTE	ELI09890
12F0	41F0 13C6	990	OTC.5	BAL LINK,TSTDU		ELI09900
12F4	2139	991	BNZS	OUT0		ELI09910
12F6	D310 2021	992	LB	R1,IOSAVE+1		ELI09920
12FA	9111	993	SLHLS	R1,1		ELI09930
12FC	D301 0A11	994	LB	RO,IO+1(R1)	GET CONSOLE WRITE ADDRESS	ELI09940
1300	9D01	995	SSR	RO,R1		ELI09950
1302	2089	996	BTRS	8,OTC.5	WAIT FOR BUSY TO DROP	ELI09960
1304	2303	997	BS	OUT1		ELI09970
1306	4010 1754	998	OUT0	STH R1,WASDU	SET FLAG	ELI09980
130A	48F0 1766	999	OUT1	LDA R15,OUT.SAV		ELI09990
130E	030F	1000		BR R15	RETURN AS SET UP ABOVE	ELI10000
		1001	*-----*			ELI10010
		1002	* TO GET A CHAR FROM KEYBOARD (IN REG R4)			ELI10020
		1003	*			ELI10030
1310	4140 140A	1004	GETCHR	BAL R4,KBREAD	PUT KB DEVICE IN READ MODE	ELI10040
1314	0890	1005	LDAR	R9,R0	SAVE CONSOLE ADDRESS	ELI10050
1316	9D04	1006	SSR	RO,R4		ELI10060
1318	2081	1007	BTRS	8,1	IF BUSY, LOOP (POSSIBLE HANG)	ELI10070
131A	9B04	1008	RDR	RO,R4	READ A CHAR IN R4	ELI10080
		1009	* TO ECHO RECEIVED CHARACTERS TO CONSOLE DEVICE IN FDX MODE			ELI10090
131C	D400 0A1A	1010	ECHO	CLB RO,MICROBUS		ELI10100
1320	233B	1011	BS	ECHO1	IF MICROBUS, BRANCH	ELI10110
1322	D390 172C	1012	LB	R9,CONRD		ELI10120
1326	C590 00A9	1013	CLHI	R9,X'A9'	CAROUSEL ?	ELI10130
132A	2137	1014	BNES	ECHRTN	DO NOT ECHO	ELI10140
132C	D390 172B	1015	LB	R9,CONADR+1		ELI10150

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1330	0D90	1723	1016	SS	R9,SINK		ELI10160
1334	2082		1017	BTBS	8,2		ELI10170
1336	9A94		1018	ECHO1	WDR	R9,R4	ELI10180
1338	C440	007F	1019	ECHR TN	NHI	R4,X'7F'	ELI10190
133C	030F		1020		BR	LINK	ELI10200
			1021	*-----*			ELI10210
			1022	* TO OUTPUT '?' TO CONSOLE			ELI10220
			1023	*			ELI10230
133E	41F0	1274	1024	QUESTN	BAL	LINK,CRLF	ELI10240
1342	40F0	174E	1025		STH	LINK,ISITERR	ELI10250
1346	C850	180C	1026		LHI	R5,QMSG	ELI10260
134A	41F0	11F0	1027		BAL	LINK,PRINT	ELI10270
134E	2400		1028		LIS	R0,0	ELI10280
1350	4000	174E	1029		STH	R0,ISITERR	ELI10290
1354	4300	0AFA	1030		B	OPTIN1	ELI10300
			1031	*-----*			ELI10310
			1032	* IF BREAK KEY DEPRESSED, GO TO 'OPTIN' OR (BRKVECT); ELSE RETURN.			ELI10320
			1033	*			ELI10330
1358	D000	2070	1034	TSTBRK	STM	R0,RSAVE+64	ELI10340
135C	40F0	1768	1035		STA	LINK,BRK,SAV	ELI10350
1360	D300	172A	1036		LB	R0,CONADR	ELI10360
1364	9D01		1037		SSR	R0,R1	ELI10370
1366	C310	0020	1038		THI	R1,X'20'	ELI10380
136A	4330	1386	1039		BZ	TSTBRK3	ELI10390
136E	0320	0A10	1040		LB	R2,I0	ELI10400
1372	C520	0005	1041		CLHI	R2,5	ELI10410
1376	2139		1042		BNES	TSTBRK4	ELI10420
1378	9802		1043	TSTBRK5	RDR	R0,R2	ELI10430
137A	9001		1044		SSR	R0,R1	ELI10440
137C	C310	0020	1045		THI	R1,X'20'	ELI10450
1380	4230	1378	1046		BNZ	TSTBRK5	ELI10460
1384	4300	13AA	1047		B	TSTBRK2	ELI10470
1388	4820	1726	1048	TSTBRK4	LH	R2,PASFLG	ELI10480
138C	233E		1049		BZS	TSTBRK1	ELI10490
138E	C310	0008	1050		THI	R1,8	ELI10500
1392	4230	1386	1051		BNZ	TSTBRK3	ELI10510
1396	9802		1052		RDR	R0,R2	ELI10520
1398	9D01		1053		SSR	R0,R1	ELI10530
139A	2281		1054		BFBS	8,1	ELI10540
139C	0822		1055		LDAR	R2,R2	ELI10550
139E	213C		1056		BNZS	TSTBRK3	ELI10560
13A0	2305		1057		BS	TSTBRK2	ELI10570
13A2	9D01		1058	TSTBRK1	SSR	R0,R1	ELI10580
13A4	C310	0020	1059		THI	R1,X'20'	ELI10590
13A8	2033		1060		BNZS	TSTBRK1	ELI10600
13AA	48F0	174C	1061	TSTBRK2	LH	R15,BRKVECT	ELI10610
13AE	4330	0AF6	1062		BZ	OPTIN	ELI10620
13B2	40F0	1768	1063		STA	R15,BRK,SAV	ELI10630
13B6	2400		1064	TSTBRK3	LIS	R0,0	ELI10640
13B8	4000	174C	1065		STH	R0,BRKVECT	ELI10650
13BC	D100	2070	1066		LM	R0,RSAVE+64	ELI10660
13C0	48F0	1768	1067		LDA	LINK,BRK,SAV	ELI10670
13C4	030F		1068		BR	LINK	ELI10680

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1069 *-----
1070 * SEE IF CURRENT LIST DEVICE IS OFF-LINE (R1 & CC NON-ZERO IF OFF)
1071 *
1306 2401 1072 TSTDU LIS R0,1 SET CLI STATUS MASK
1308 4810 1728 1073 LH R1,PASFLG2 LIST DEVICE ON PASLA ?
130C 2333 1074 BZS $TSTDU0 BRANCH: NO.
130E C800 00FC 1075 LHI R0,X'FC' SET PASLA STATUS MASK
1302 0310 2021 1076 $TSTDU0 LB R1,IOSAVE+1 GET I/O POINTER FOR LIST DEVICE
1306 9111 1077 SLHLS R1,1
1308 0311 0A10 1078 LB R1,IO(R1) GET DEVICE ADDRESS
130C 0210 1723 1079 STR R1,SINK AND SAVE IT
13E0 9011 1080 SSR R1,R1 GET LIST DEVICE STATUS
13E2 0410 1081 NAR R1,R0 MASK OFF UNWANTED BITS
13E4 C310 0001 1082 THI R1,1 DU FOR CLI ?
13E8 2135 1083 BNZS $TSTDU2 BRANCH: YES.
13EA C510 000C 1084 CLHI R1,X'0C' DU FOR PASLA ?
13EE 2332 1085 BES $TSTDU2 BRANCH: YES.
13F0 2511 1086 $TSTDU1 LCS R1,1 "NOT DU" EXIT: R1=CC=0
13F2 C710 FFFF 1087 $TSTDU2 XHI R1,-1 "DU" EXIT: R1=CC<>0
13F6 0300 1723 1088 LB R0,SINK PUT DEVICE ADDRESS IN R0
13FA 030F 1089 BR LINK RETURN
1090 *-----
1091 * TO DIRECT INPUT AND OUTPUT TO CONSOLE DEVICE
1092 *
13FC D300 0A10 1093 SFTKB LB R0,IO GET KEYBOARD DEVICE
1400 9410 1094 EXBR R1,R0
1402 0610 1095 OAR R1,R0
1404 4010 2020 1096 STH R1,IOSAVE KB DEVICE = LIST DEVICE ***
1408 030F 1097 BR LINK RETURN
1098 *-----
1099 * TO PUT KEYBOARD DEVICE IN READ MODE
1100 *
140A D300 172A 1101 Kbread LB R0,CONADR
140E DE00 172C 1102 OC R0,CONRD OC CONSOLE - READ COMMAND
1412 0B00 1723 1103 RD R0,SINK READ A DUMMY CHARACTER (SET BUSY)
1416 4890 1726 1104 LH R9,PASFLG PASLA ?
141A 4200 141A 1105 NOP * FOR SPECIAL KB DEVICE
141E 2333 1106 TTYGET BZS KBXIT NO. BRANCH TO EXIT
1420 DE00 1744 1107 OC R0,CONRQ2S YES. OC (REQUEST TO SEND)
1424 0304 1108 KRXIT BR R4 RETURN
1109 *-----
1110 * TO SET UP KEYBOARD DEV TO READ WITH INT ENABLED
1111 *
1426 0000 2030 1112 KBRD STM R0,RSVAVE SAVE REGISTERS
142A 0300 172A 1113 LB R0,CONADR GET KB DEV ADR
142E 4810 1726 1114 LH R1,PASFLG PASLA ?
1432 2333 1115 BZS KBRD1
1434 DE00 1744 1116 OC R0,CONRQ2S
1438 DE00 1739 1117 KBRD1 OC R0,CONENRD CONSOLE : ENABLE, READ
143C 0100 2030 1118 LM R0,RSVAVE RESTORE REGISTERS
1440 030F 1119 BR LINK RETURN
1120 *-----
1121 * LIST DEVICE SET UP ROUTINE

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ELI10690
ELI10700
ELI10710
ELI10720
ELI10730
ELI10740
ELI10750
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ELI10800
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FLI10970
ELI10980
ELI10990
ELI11000
ELI11010
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ELI11080
ELI11090
ELI11100
ELI11110
ELI11120
ELI11130
ELI11140
ELI11150
ELI11160
ELI11170
ELI11180
ELI11190
ELI11200
ELI11210

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1442	4010	176A	1122	*				ELI11220
1446	0310	2021	1123	SETUP	STA	R1,SET.RTN		ELI11230
144A	9111		1124		LB	R1,IOSAVE+1	GET LIST DEVICE IDENTIFIER	ELI11240
144C	0301	0A11	1125		SLHLS	R1,1	HW INDEX	ELI11250
1450	0E01	1720	1126		LB	R0,IO+1(R1)	GET LIST DEVICE ADDRESS	ELI11260
1454	4810	176A	1127		OC	R0,CONWRT(R1)		ELI11270
1458	0301		1128		LDA	R1,SET.RTN		ELI11280
			1129		SR	R1	RETURN	ELI11290
			1130	*	*****			ELI11300
			1131	*	LOW CORE SET UP ROUTINE			ELI11310
			1132	*				ELI11320
145A	2410		1133	LCORE	LIS	R1,0		ELI11330
145C	2422		1134		LIS	R2,2		ELI11340
145E	C830	004E	1135		LHI	R3,X'4E'		ELI11350
1462	2400		1136		LIS	R0,0		ELI11360
1464	4001	0000	1137	ZER01	STH	R0,0(R1)		ELI11370
1468	C110	1464	1138		BXLE	R1,ZER01	ZERO CORE FROM 0 THRU X'4F'	ELI11380
146C	C810	00A0	1139		LHI	R1,X'80'		ELI11390
1470	C830	00CE	1140		LHI	R3,X'CE'		ELI11400
1474	4001	0000	1141	ZER02	STH	R0,0(R1)		ELI11410
1478	C110	1474	1142		BXLE	R1,ZER02	ZERO CORE FROM X'80' THRU X'CF'	ELI11420
147C	C800	15AA	1143		LHI	R0,XI32	INTERRUPT HANDLER ROUTINE	ELI11430
1480	C830	08CE	1144		LHI	R3,X'8CE'		ELI11440
1484	4001	0000	1145	ZFR03	STH	R0,0(R1)		ELI11450
1488	C110	1484	1146		BXLE	R1,ZER03	SET UP INT SERVICE POINTER TABLE	ELI11460
148C	C830	1680	1147		LHI	R3,II		ELI11470
1490	4030	0036	1148		STH	R3,X'36'	ILL INST INT NEW PSW LOC	ELI11480
1494	C840	16CA	1149		LHI	R4,MM		ELI11490
1498	4040	003E	1150		STH	R4,X'3E'	M. M. INT NEW PSW LOC	ELI11500
149C	C830	167C	1151		LHI	R3,AF		ELI11510
14A0	4030	004E	1152		STH	R3,X'4E'	ARITHMETIC FAULT NEW PSW LOC(32-BIT)	ELI11520
			1153	*			FIXED PT DIVIDE FAULT NEW PSW LOC	ELI11530
14A4	C840	2030	1154		LHI	R4,RSAVE		ELI11540
14A8			1155		IFZ	ADC-2		ELI11550
14A8	4810	1712	1156		LH	R1,MOD32		ELI11560
14AC	4230	14CE	1157		BNZ	LCORE32		ELI11570
			1158	*				ELI11580
			1159	*	SET UP LOW CORE FOR 16 BIT MACHINE			ELI11590
			1160	*				ELI11600
14B0	4040	0022	1161		STH	R4,X'22'	REG SAVE POINTER	ELI11610
14B4	C830	166A	1162		LHI	R3,FP		ELI11620
14B8	4030	002E	1163		STH	R3,X'2E'	FLOATING PT FAULT INT NEW PSW LOC	ELI11630
14BC	4850	0A2A	1164		LH	R5,PSW2		ELI11640
14C0	4050	0044	1165		STH	R5,X'44'	HW EXT INT NEW PSW STATUS	ELI11650
14C4	C850	159C	1166		LHI	R5,XI16		ELI11660
14C8	4050	0046	1167		STH	R5,X'46'	EXT INT NEW PSW LOC	ELI11670
14CC	050F		1168		BR	LINK		ELI11680
			1169		ENDC			ELI11690
			1170	*				ELI11700
			1171	*	SET UP LOW CORE FOR 32 BIT MACHINE			ELI11710
			1172	*				ELI11720
14CE	4040	0086	1173	LCORE32	STH	R4,X'86'	REG SAVE POINTER	ELI11730
14D2	C840	2028	1174		LHI	R4,PSWSAVE	PPF PSW SAVE AREA	ELI11740

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14D6	4040	0084	1175	STH	R4,X'84'	. POINTER	ELI11750
14DA	C830	1672	1176	LHI	R3,RP		ELI11760
14DE	4030	0096	1177	STH	R3,X'96'	RELOC/PROTECT INT NEW PSW LOC	ELI11770
14E2	0310	172A	1178	LB	R1,CONADR	LOAD CONSOLE I/O ADDRESS	ELI11780
14E6	0A11		1179	AAR	R1,R1		ELI11790
14E8	C800	1506	1180	LHI	R0,KBINT0	R0 = A(KEYBOARD INT HANDLER)	ELI11800
14EC	4001	00D0	1181	STH	R0,X'D0'(R1)	STORE @ X'D0'+2(KB DEV ADR)	ELI11810
14F0	2410		1182	LIS	R1,0	TO SET UP SERVICE POINTER TABLE	ELI11820
14F2	C830	15AA	1183	LHI	R3,XI32		ELI11830
14F6	4821	1910	1184	LCORE32A LH	R2,DEVSADR(R1)	GET DEV ADR FROM TABLE	ELI11840
14FA	021F		1185	3MR	LINK	DONE. RETURN	ELI11850
14FC	0A22		1186	AAR	R2,R2		ELI11860
14FE	4032	00D0	1187	STH	R3,X'D0'(R2)	STORE @ X'D0'+2(DEV ADR)	ELI11870
1502	2612		1188	AIS	R1,2		ELI11880
1504	2207		1189	BS	LCORE32A		ELI11890
			1190				ELI11900
			1191	*	KEYBOARD INTERRUPT HANDLER		ELI11910
			1192	*			ELI11920
1506	C330	0020	1193	KBINT0	THI	R3,X'20'	IS BREAK KEY DEPRESSED ?
150A	4330	1552	1194	BZ	KBINT1		NO
150E	0300	0A10	1195	LB	R0,I0		ELI11940
1512	C500	0005	1196	CLHI	R0,5	IS IT MICROBUS ?	ELI11950
1516	4230	1532	1197	BNE	KBINT0	NO, BRANCH	ELI11960
151A	DE20	1736	1198	OC	R2,MREADC	YES, ISSUE READ	ELI11970
151E	9D23		1199	SSR	R2,R3		ELI11980
1520	2081		1200	BTBS	8,1		ELI11990
1522	9824		1201	KBINT0C RDR	R2,R4	KNOCK DOWN BREAK	ELI12000
1524	9D23		1202	SSR	R2,R3		ELI12010
1526	C330	0020	1203	THI	R3,X'20'	BREAK STILL THERE ?	ELI12020
152A	4230	1522	1204	BNZ	KBINT0C	YES, KNOCK IT DOWN AGAIN	ELI12030
152E	4300	158A	1205	B	RETOPSW	NO, RETURN ON OLD PSW	ELI12040
1532	4850	1726	1206	KBINT0B LH	R5,PASFLG	CONSOLE ON PASLA ?	ELI12050
1536	2339		1207	BZS	KBINT0A	BRANCH IF NO.	ELI12060
1538	9824		1208	RDR	R2,R4		ELI12070
153A	9D23		1209	SSR	R2,R3		ELI12080
153C	2281		1210	BFBS	8,1		ELI12090
153E	0844		1211	LDAR	R4,R4		ELI12100
1540	4230	158A	1212	BNZ	RETOPSW	IGNORE FRERR ONLY	ELI12110
1544	4300	1568	1213	KBINT0D B	KBINT3		ELI12120
1548	9D23		1214	KBINT0A SSR	R2,R3		ELI12130
154A	C330	0020	1215	THI	R3,X'20'		ELI12140
154E	2033		1216	BTBS	3,3	WAIT FOR BREAK RELEASE	ELI12150
1550	2206		1217	BS	KBINT0D	GO TO COMMAND MODE	ELI12160
1552	C500	0005	1218	KBINT1 CLHI	R0,5	IS IT MICROBUS ?	ELI12170
1556	4230	1568	1219	BNE	KBINT3	NO, BRANCH	ELI12180
155A	DE20	1736	1220	OC	R2,MREADC	READ COMMAND TO MICROBUS	ELI12190
155E	9D23		1221	SSR	R2,R3		ELI12200
1560	2081		1222	BTBS	8,1		ELI12210
1562	9824		1223	RDR	R2,R4	KNOCK DOWN INTERRUPT	ELI12220
1564	4300	158A	1224	B	RETOPSW	RETURN	ELI12230
1568	4020	1720	1225	KBINT3 STH	R2,INTDEV		ELI12240
156C	D230	1722	1226	STB	R3,INTSTA		ELI12250
1570			1227	IFZ	ADC-2		ELI12260

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15E4	2450	1281	*				ELI12810
15E6	4865 1910	1282	XI16A	LIS	R5,0		ELI12820
15EA	4210 1636	1283	XI1	LH	R6,DEVSADR(R5)	GET DEV ADRS FROM TABLE	ELI12830
15EE	0562	1284		BM	XIERR	TABLE OVERFLOW.	ELI12840
15F0	2333	1285		CLAR	R6,R2	COMPARE INTERRUPTING DEVICE ADDRESS	ELI12850
15F2	2652	1286		BES	XI2		ELI12860
15F4	2207	1287		AIS	R5,2		ELI12870
15F6	4865 192A	1288		BS	XI1		ELI12880
15FA	4330 1636	1289	XI2	LH	R6,DEVINT(R5)	GET INTERRUPT HANDLER ADDRESS	ELI12890
15FE	4060 1634	1290		BZ	XIERR	INTERRUPT NOT EXPECTED	ELI12900
		1291		STH	R6,XIEXIT		ELI12910
		1292	*				ELI12920
1602		1293		IFZ	ADC-2		ELI12930
1602	4860 1712	1294		LH	R6,MOD32	32-BIT MACHINE ?	ELI12940
1606	2339	1295		BZS	XI3	BRANCH IF NO.	ELI12950
		1296		ENOC			ELI12960
1608	9051	1297		SRLS	R5,1		ELI12970
160A	90A4	1298		SRLS	R10,4		ELI12980
160C	C4A0 000F	1299		NHI	R10,15		ELI12990
1610	D4A5 1922	1300		CLB	R10,INTLVL(R5)	CHECK PROPER INTERRUPT LEVEL	ELI13000
1614	4230 1646	1301		BNE	LVLERR		ELI13010
		1302	*				ELI13020
1618	4860 171E	1303	XI3	LH	R6,OLOC	GET PSW AT TIME OF INTERRUPT	ELI13030
161C	C560 1114	1304		CLHI	R6,\$TIMER1		ELI13040
1620	2187	1305		BLS	XI4	WAS INTERRUPT IN TIMER ROUTINE ?	ELI13050
1622	C560 1128	1306		CLHI	R6,\$TIMXT		ELI13060
1626	2384	1307		BNLS	XI4	BRANCH IF NO.	ELI13070
1628	0100 2030	1308		LM	R0,RSAVE	RESTORE FROM 'TIMER' ENTRY	ELI13080
162C	2303	1309		BS	XI5		ELI13090
162E	0100 2080	1310	XI4	LM	R0,INTSAV	RESTORE FROM XI16/XI32 ENTRY	ELI13100
1632	4300 1632	1311	XI5	B	*	AND GO TO INTERRUPT HANDLER	ELI13110
	0000 1634	1312	XIEXIT	EQU	**2	NOTE: 16 KB RESTRICTION !	ELI13120
		1313	*				ELI13130
		1314	*			EXTERNAL INTERRUPT ERROR ROUTINE	ELI13140
		1315	*				ELI13150
1636	C860 4634	1316	XIERR	LHI	R6,C'F4'	ERROR # F4	ELI13160
163A	4060 179A	1317		STH	R6,ERRNO		ELI13170
163E	41F0 0FDE	1318		BAL	LINK,ERRALL	'ERROR XXF4', 'DEV DDD STA SS'	ELI13180
		1319	*			'PSW PPPP LOC LLLL'	ELI13190
1642	4300 0AFA	1320		B	OPTIN1	TO ENTER COMMAND MODE	ELI13200
		1321	*				ELI13210
		1322	*			DEVICE INTERRUPTED IN WRONG INTERRUPT LEVEL	ELI13220
		1323	*				ELI13230
1646	C860 4636	1324	LVLERR	LHI	R6,C'F6'	ERROR # F6	ELI13240
164A	4060 179A	1325		STH	R6,ERRNO		ELI13250
164E	D3AA 1778	1326		LB	R10,HEXTAB(R10)	CONVERT TO ASCII	ELI13260
1652	02A0 17FB	1327		STB	R10,ERRLVL	AND STORE ERROR LEVEL IN MESSAGE	ELI13270
1656	41F0 0FDE	1328		BAL	LINK,ERRALL	'ERROR XXF6', 'DEV DDD STA SS'	ELI13280
		1329	*			'PSW PPPP LOC LLLL'	ELI13290
165A	C850 17E6	1330		LHI	R5,INTLVL		ELI13300
165E	4050 174E	1331		STH	R5,ISITERR	SET FLAG TO OVERRIDE NOMSG OPTION	ELI13310
1662	41F0 11F0	1332		BAL	LINK,PRINT	'INTERRUPTED IN LEVEL N'	ELI13320
1666	4300 0AFA	1333		B	OPTIN1	ENTER COMMAND MODE.	ELI13330

EXEC - ETPE R03P4 (W/CONDITIONAL ASSEMBLY)

		1334	*-----			ELI13340
		1335	* SPURIOUS INTERRUPT HANDLERS			ELI13350
		1336	*			ELI13360
		1337	*			ELI13370
166A		1338	IFZ ADC-2			ELI13380
		1339	* FLOATING-PT ARITH FAULT INT TRAP (16 BIT PROCESSOR)			ELI13390
		1340	*			ELI13400
166A	48E0 0028	1341	FP LH R14,X'28'	OLD PSW (16-BIT PROCESSOR)		ELI13410
166E	48F0 002A	1342	LH R15,X'2A'	OLD LOC		ELI13420
		1343	ENDC			ELI13430
		1344	*			ELI13440
		1345	* RELOCATION/PROTECTION INT TRAP			ELI13450
		1346	*			ELI13460
1672	C820 4635	1347	RP LHI R2,C'F5'			ELI13470
1676	4020 179A	1348	STH R2,ERRNO	SET ERROR # F5		ELI13480
167A	230C	1349	BS COMM			ELI13490
		1350	*			ELI13500
		1351	* ARITHMETIC FAULT INT (32-BIT PROCESSOR) TRAP			ELI13510
167C		1352	IFZ ADC-2			ELI13520
		1353	* FIXED-PT DIVIDE FAULT INT (16-BIT PROCESSOR) TRAP			ELI13530
		1354	ENDC			ELI13540
		1355	*			ELI13550
167C	C820 4631	1356	AF LHI R2,C'F1'			ELI13560
1680	4020 179A	1357	STH R2,ERRNO	SET ERROR # F1		ELI13570
1684		1358	IFZ ADC-2			ELI13580
1684	4820 1712	1359	LH R2,MOD32			ELI13590
1688	2135	1360	BNZS COMM			ELI13600
168A	48E0 0048	1361	LH R14,X'48'	OLD PSW (16-BIT PROCESSOR)		ELI13610
168E	48F0 004A	1362	LH R15,X'4A'	OLD LOC (16-BIT PROCESSOR)		ELI13620
		1363	ENDC			ELI13630
1692	40E0 171A	1364	COMM STH R14,OPSW			ELI13640
1696	40F0 171E	1365	STH R15,OLOC			ELI13650
169A	4800 0A24	1366	COMM1 LH R0,PSW2			ELI13660
169E	9520	1367	EPSR R2,R0	NO INT. , REG SET 15		ELI13670
16A0	41F0 0F6C	1368	BAL LINK,ERR	PRINT 'ERROR XXFN'		ELI13680
16A4	40F0 174E	1369	STH LINK,ISITERR	FORCE PRINT		ELI13690
16A8	41E0 10A2	1370	BAL RET,ERRPL1	PRINT 'PSW PPPP LOC LLLL'		ELI13700
16AC	4300 0AFA	1371	B OPTIN1	ENTER COMMAND MODE		ELI13710
		1372	*			ELI13720
		1373	* ILLEGAL INSTRUCTION INTERRUPT TRAP			ELI13730
		1374	*			ELI13740
16B0	C820 4632	1375	II LHI R2,C'F2'			ELI13750
16B4	4020 179A	1376	STH R2,ERRNO	SET ERROR # F2		ELI13760
16B8		1377	IFZ ADC-2			ELI13770
16B8	4820 1712	1378	LH R2,MOD32			ELI13780
16BC	2135	1379	BNZS II32			ELI13790
16BE	48E0 0030	1380	LH R14,X'30'	OLD PSW		ELI13800
16C2	48F0 0032	1381	LH R15,X'32'	OLD LOC		ELI13810
		1382	ENDC			ELI13820
16C6	4300 1692	1383	II32 B COMM			ELI13830
		1384	*			ELI13840
		1385	* MACHINE MALFUNCTION INTERRUPT TRAP			ELI13850
		1386	*			ELI13860

EXEC - ETPE R03P4 (W/CONDITIONAL ASSEMBLY)

16CA	95AA	1387	MM	EPSR	R10,R10	CAPTURE MMINT PSW	ELI13870
16CC	C820 4633	1388		LHI	R2,C'F3'		ELI13880
16D0	4020 179A	1389		STH	R2,ERRNO	SET ERROR # F3	ELI13890
16D4	48E0 0022	1390		LH	R14,X'22'	OLD PSW (32-BIT PROCESSOR)	ELI13900
16D8	48F0 0026	1391		LH	R15,X'26'	OLD LOC	ELI13910
16DC		1392		IFZ	ADC-2		ELI13920
16DC	4820 1712	1393		LH	R2,MOD32		ELI13930
16E0	2135	1394		BNZS	MM32		ELI13940
16E2	48E0 0038	1395		LH	R14,X'38'	OLD PSW (16 BIT PROCESSOR)	ELI13950
16E6	48F0 003A	1396		LH	R15,X'3A'	OLD LOC	ELI13960
		1397		ENDC			ELI13970
16EA	C4E0 FFF0	1398	MM32	NHI	R14,X'FFF0'		ELI13980
16EE	C4A0 000F	1399		NHI	R10,X'000F'		ELI13990
16F2	06EA	1400		OAR	R14,R10		ELI14000
16F4	40E0 171A	1401		STH	R14,OPSW		ELI14010
16F8	40F0 171E	1402		STH	R15,OLOC		ELI14020
16FC		1403		IFZ	ADC-2		ELI14030
16FC	C810 7FFF	1404		LHI	R1,X'7FFF'		ELI14040
1700	2711	1405	MM16	SIS	R1,1		ELI14050
1702	2021	1406		BPS	MM16		ELI14060
		1407		ENDC			ELI14070
1704	C800 080F	1408		LHI	R0,X'080F'		ELI14080
1708	9104	1409		SLHLS	R0,4	R0 = X'80F0'	ELI14090
170A	9520	1410		EPSR	R2,R0	HALT PROCESSOR	ELI14100
		1411	*				ELI14110
		1412	*	WHEN EXE/RUN IS DEPRESSED, ERROR MSG IS PRINTED.			ELI14120
		1413	*				ELI14130
170C	4300 169A	1414		9	COMM1		ELI14140
		1415	*	*****			ELI14150
		1416	*	ETPE CONSTANTS & TABLES			ELI14160
		1417	*				ELI14170
1710	0000	1418	FIRST	DCX	0		ELI14180
1712	0000	1419	MOD32	DCX	0	FLAG FOR 32-BIT M/C (NON-ZERO)	ELI14190
1714	0000	1420	INTPSW	DCX	0	(FOR 32-BIT M/C ONLY)	ELI14200
1718		1421		ALIGN	8		ELI14210
		1422	*	-----			ELI14220
1718	0000	1423	OPSW32	DCX	0	OLD PSW STORAGE AREA	ELI14230
171A	0000	1424	OPSW	DCX	0		ELI14240
171C	0000	1425		DCX	0		ELI14250
171E	0000	1426	OLOC	DCX	0		ELI14260
		1427	*	-----			ELI14270
1720	0000	1428	INTDEV	DCX	0	INTERRUPTING DEV ADR	ELI14280
	0000 1720	1429	ERRDEV	EQU	INTDEV	ERROR DEVICE #	ELI14290
1722	00	1430	INTSTA	DB	0	INTERRUPTING DEV STATUS	ELI14300
	0000 1722	1431	ERRSTA	EQU	INTSTA	ERRONEOUS STATUS	ELI14310
1723	00	1432	SINK	DB	0	BIT BUCKET	ELI14320
1724	80	1433	NORM	DB	X'80'		ELI14330
1725	40	1434	INCR	DB	X'40'		ELI14340
1726		1435		DB	*	(ALIGN ON HW BOUNDRY)	ELI14350
1726	0000	1436	PASFLG	DCX	0	SET WHEN CONSOLE ON PASLA/PALM	ELI14360
1728	0000	1437	PASFLG2	DCX	0	SET WHEN LIST DEVICE ON PASLA	ELI14370
		1438	*	-----			ELI14380
		1439	*	ETPE IO COMMANDS			ELI14390

EXEC - ETPE R43P4 (W/CONDITIONAL ASSEMBLY)

172A	0000	1440	*				ELI14400
		1441	CONADR	DCX	0	CONSOLE DEVICE ADDRESS	ELI14410
		1442	*				ELI14420
172C	0000	1443	CONRD	DCX	0	CONSOLE READ/WRITE COMMANDS	ELI14430
	0000 172D	1444	CONWRT	EQU	CONRD+1		ELI14440
172E	B9AB	1445	CRTRD	DCX	B9AB	FOR CRT	ELI14450
1730	A4D8	1446	CLIFRD	DCX	A4D8	* CURRENT LOOP INTERFACE	ELI14460
1732	0080	1447	LPWRT	DCX	0080	* LINE PRINTER	ELI14470
1734	A9AB	1448	CARRD	DCX	A9AB	* CAROUSEL 300	ELI14480
1736	8202	1449	MREADC	DCX	8202	* MICROBUS	ELI14490
		1450	*				ELI14500
1738	0000	1451	CON2ND	DCX	0	2ND COMMAND; ENABLE READ COMMAND	ELI14510
	0000 1739	1452	CONENRD	EQU	CON2ND+1		ELI14520
173A	F879	1453	CRT2ND	DCX	F879	FOR CRT	ELI14530
173C	0064	1454	CLIF2ND	DCX	0064	* CURRENT LOOP INTERFACE	ELI14540
173E	0000	1455		DCX	0	* DUMMY HW FOR LP	ELI14550
1740	F069	1456	CAR2ND	DCX	F069	* CAROUSEL 300	ELI14560
1742	0000	1457		DCX	0	* DUMMY HW FOR MICROBUS	ELI14570
		1458	*				ELI14580
1744	00	1459	CONRQ2S	DB	0	CONSOLE REQUEST TO SEND CMD	ELI14590
1745	38	1460	CRTRQ2S	DB	X*38*	FOR CRT	ELI14600
1746	00	1461		DB	0	* DUMMY BYTE FOR CLI	ELI14610
1747	00	1462		DB	0	* DUMMY BYTE FOR LP	ELI14620
1748	23	1463	CARRQ2S	DB	X*23*	* CAROUSEL 300	ELI14630
1749	00	1464		DB	0	* DUMMY BYTE FOR MICROBUS	ELI14640
174A		1465		DB	*	(ALIGN ON HW BOUNDARY)	ELI14650
		1466	*	-----			ELI14660
174A	158A	1467	K3INT	DC	Z(RETOPSW)	KEYBOARD INT RETURN ADR	ELI14670
174C	0000	1468	BRKVECT	DC	Z(0)	BREAK KEY VECTOR	ELI14680
174E	0000	1469	ISITERR	DCX	0		ELI14690
1750	0000	1470	NOERR	DCX	0		ELI14700
1752	0000	1471	SELTST	DCX	0	HIGHEST SELECTED TEST #	ELI14710
1754	0000	1472	WASDU	DCX	0	1 IF KEYBOARD DEVICE WAS OFF	ELI14720
1756	0000	1473	WASDU1	DCX	0	NON-ZERO IF TOTAL,TOTERR TO PRINT	ELI14730
1758	0000	1474	TOTAL	DCX	0	# OF TIMES THE SELECTED TESTS RUN	ELI14740
175A	0000	1475	TOTERR	DCX	0	TOTAL ERRORS DETECTED WHILE DU	ELI14750
175C	0000	1476	BYESTNO	DCX	0	CURRENT TEST # IN BINARY	ELI14760
175E	0000	1477	COUNT	DCX	0		ELI14770
1760	0000	1478	NEXTST	DCX	0	NEXT TEST #	ELI14780
1762	0000	1479	\$NULL	DCX	0	NULL HW FOR DISPLAY USE	ELI14790
1764	0000	1480	PAUSE	DCX	0	SET DURING TRANSMISSION PAUSE (C300)	ELI14800
1766	0000	1481	OUT.SAV	DAC	0	OUTCHR RETURN ADDRESS SAVE	ELI14810
1768	0000	1482	BRK.SAV	DAC	0	TSTBRK RETURN ADDRESS SAVE	ELI14820
176A	0000	1483	SET.RTN	DAC	0	SETUP RETURN ADDRESS SAVE	ELI14830
176C	0000	1484	COMRET	DAC	0	ERRCOM RETURN ADDRESS SAVE	ELI14840
		1485	*				ELI14850
176E	0001	1486	DECTAB	DC	1,10,100,1000,10000		ELI14860
1770	000A						
1772	0064						
1774	03E8						
1776	2710						
1778	3031 3233 3435 3637	1487	HEXTAB	DB	C*0123456789ABCDEF*		ELI14870
1780	3839 4142 4344 4546						

1888	1A26		1542	DC	TEST2		ELI15420
188A	1AD4		1543	DC	TEST3		ELI15430
188C	1896		1544	DC	TEST4		ELI15440
188E	1C84		1545	DC	TEST5		ELI15450
			1546	*			ELI15460
1890	434F 4D4D 4F4E 2045		1547	TITLE DC	C'COMMON EIGHT-LINE INTERRUPT MODULE TEST 06-134R04'		ELI15470
1898	4947 4854 2D4C 494E						
18A0	4520 494E 5445 5252						
18A8	5550 5420 4D4F 4455						
18B0	4C45 2054 4553 5420						
18B8	3036 2D31 3334 5230						
18C0	3420						
18C2	0D0A		1548	DCX	DOA		ELI15480
18C4	4745 4E45 5241 5445		1549	MESSAGE DC	C'GENERATE INTERRUPT',X'DOA'		ELI15490
18CC	2049 4E54 4552 5255						
18D4	5054						
18D6	0D0A						
18D8	434D 4420 4144 5220		1550	CMMSG DC	C'CMD ADR '		ELI15500
18E0	2A2A 2A20		1551	ASCAOR DC	C'*** ',X'DOA'		ELI15510
18E4	0D0A						
18E6	4558 5020		1552	ASCMSG DC	C'EXP '		ELI15520
18EA	2A2A 2A20		1553	ASCEXP DC	C'*** ',X'DOA'		ELI15530
18EE	0D0A						
18F0	5245 5345 5420		1554	RSTMSG DC	C'RESET '		ELI15540
18F6	2A2A 2A20		1555	ASCRST DC	C'*** ',X'DOA'		ELI15550
18FA	0D0A						
18FC	4E4F 204D 4F52 4520		1556	QUEMSG DC	C'NO MORE INTERRUPTS',X'DOA'		ELI15560
1904	494E 5445 5252 5550						
190C	5453						
190E	0D0A						
			1557	*	*****		ELI15570
			1558	*			ELI15580
	0000 1910		1559	DEVSADR EQU *	INTERRUPTING DEVICE TABLE		ELI15590
1910	0000		1560	LINE0 DCX 0			ELI15600
1912	0000		1561	LINE1 DCX 0			ELI15610
1914	0000		1562	LINE2 DCX 0			ELI15620
1916	0000		1563	LINE3 DCX 0			ELI15630
1918	0000		1564	LINE4 DCX 0			ELI15640
191A	0000		1565	LINE5 DCX 0			ELI15650
191C	0000		1566	LINE6 DCX 0			ELI15660
191E	0000		1567	LINE7 DCX 0			ELI15670
1920	FFFF		1568	DCX FFFF			ELI15680
			1569	*			ELI15690
1922			1570	INTLVL DO 8	EXPECTED INTERRUPT LEVELS FOR ABOVE		ELI15700
1922	00		1571	DB 0			ELI15710
1923	00		1571	DB 0			
1924	00		1571	DB 0			
1925	00		1571	DB 0			
1926	00		1571	DB 0			
1927	00		1571	DB 0			
1928	00		1571	DB 0			
1929	00		1571	DB 0			
			1572	*			ELI15720
	0000 192A		1573	DEVINT EQU *	INTERRUPT HANDLER VECTORS		ELI15730
192A	0000		1574	HOLRO DCX 0	FOR LINE 0		ELI15740

192C	0000	1575	HDLR1	DCX	0	ELI15750
192E	0000	1576	HDLR2	DCX	0	ELI15760
1930	0000	1577	HDLR3	DCX	0	ELI15770
1932	0000	1578	HDLR4	DCX	0	ELI15780
1934	0000	1579	HDLR5	DCX	0	ELI15790
1936	0000	1580	HDLR6	DCX	0	ELI15800
1938	0000	1581	HDLR7	DCX	0	ELI15810
		1582	*			ELI15820
		1583	*			ELI15830
		1584	*	*****		ELI15840
		1585	*			ELI15850
		1586	*			ELI15860
		1587	*	PURPOSE OF TEST:		ELI15870
		1588	*	TEST 0 VERIFIES THAT A 'CLEAR' COMMAND (X'08') DOES CLEAR ALL		ELI15880
		1589	*	PENDING INTERRUPTS WITHIN THE EIGHT-LINE INTERRUPT MODULE.		ELI15890
		1590	*			ELI15900
		1591	*	ASSUMPTIONS:		ELI15910
		1592	*	IT IS ASSUMED THAT THE PROCESSOR, MEMORY, AND TELETYPE TESTS HAVE		ELI15920
		1593	*	BEEN RUN PRIOR TO SELECTING THIS TEST.		ELI15930
		1594	*			ELI15940
		1595	*	DESIGN SPECIFICATIONS:		ELI15950
		1596	*	DEVICE INTERRUPTS ARE ENABLED, AND ALL LINES ARE SET. A 'CLEAR'		ELI15960
		1597	*	COMMAND IS ISSUED, AND ALL LINES ARE MASKED. PROCESSOR INTERRUPTS		ELI15970
		1598	*	ARE ENABLED. NO INTERRUPT SHOULD OCCUR, AFTER A FIXED DELAY,		ELI15980
		1599	*	PROCESSOR INTERRUPTS ARE DISABLED, AND THE TEST TERMINATES.		ELI15990
		1600	*			ELI16000
		1601	*	THE TEST IS REPEATED EIGHT TIMES, EACH TIME USING THE ADDRESS OF		ELI16010
		1602	*	THE NEXT SEQUENTIAL INTERRUPT LINE AS THE DEVICE ADDRESS TO WHICH		ELI16020
		1603	*	ALL COMMANDS ARE SENT. IF FALSE SYNC RESULTS FROM AN OUTPUT		ELI16030
		1604	*	COMMAND FOR ANY ADDRESS, AN ERROR MESSAGE IS PRINTED,		ELI16040
		1605	*	AND THE TEST ABORTED.		ELI16050
		1606	*			ELI16060
		1607	*			ELI16070
		1608	*	HOW TO RUN THE TEST:		ELI16080
		1609	*	ENTER APPROPRIATE VALUES FOR THE TIMVAL AND DEVADR OPTIONS, SELECT		ELI16090
		1610	*	THE TEST, AND ENTER 'RUN'. NO MANUAL INTERVENTION IS REQUIRED.		ELI16100
		1611	*			ELI16110
		1612	*	OPTIONS:		ELI16120
		1613	*	TEST, DEVADR, INTLEV, CONTIN, LOOP, TIMVAL.		ELI16130
		1614	*			ELI16140
		1615	*	ERRORS:		ELI16150
		1616	*	01 09		ELI16160
		1617	*			ELI16170
		1618	*			ELI16180
		1619	TEST0	EQU	*	ELI16190
		1620	*			ELI16200
		1621		LHI	R1,DEVINT	ELI16210
		1622		LHI	R0,TOINT	ELI16220
		1623		LHR	R2,R1	ELI16230
		1624	TST0.0	STH	R0,0(R2)	ELI16240
		1625		AIS	R2,2	ELI16250
		1626		CLHI	R2,16(R1)	ELI16260
		1627		BLS	TST0.0	ELI16270
		1628	*			ELI16280
		1629		LH	R2,DEVSADR	ELI16290

FOR LINE 7

UNEXPECTED INTPT HDLR.

LINE 0 ADDRESS

1954	4020	2010	1630	TST0.1	STH	R2,CMDADR	ADDRESS TO WHICH COMMAND SENT	ELI16300
1958	DE20	2015	1631		OC	R2,ENST	(ENAB INTPTS, SET MODE)	ELI16310
195C	4240	1EFA	1632		BTC	4,TT09X	FALSE SYNC.	ELI16320
1960	DA20	2014	1633		WD	R2,ALLI	SET ALL LINES,	ELI16330
1964	9E20	2016	1634		OC	R2,CLRI	THEN SEND 'CLEAR' COMMAND	ELI16340
1968	DE20	2013	1635		OC	R2,ENMK	(ENAB INTPTS, LOAD MASK MODE)	ELI16350
196C	DA20	2014	1636		WD	R2,ALLI	MASK ALL LINES	ELI16360
1970	2400		1637		LIS	R0.0		ELI16370
1972	4000	200A	1638		STH	R0,INTCNT	INIT COUNT	ELI16380
			1639	*				ELI16390
1976	41F0	1F58	1640	TST0.2	BAL	LINK,DELAY	NO INTERRUPT SHOULD OCCUR.	ELI16400
			1641	*				ELI16410
197A	4810	1910	1642		LH	R1,DEVSADR		ELI16420
197E	4820	2010	1643		LH	R2,CMDADR		ELI16430
1982	2621		1644		AIS	R2,1	DEVICE ADDRESS	ELI16440
1984	C521	0008	1645		CLHI	R2,8(R1)		ELI16450
1988	4280	1954	1646		BL	TST0.1	REPEAT FOR ALL LINE ADDRESSES.	ELI16460
			1647	*				ELI16470
198C	4300	0E5E	1648	TOEND	B	TSTEND	RETURN TO EXECUTIVE.	ELI16480
			1649	*				ELI16490
1990	41F0	1094	1650	TOINT	BAL	R15,TT01		ELI16500
1994	4800	200A	1651		LH	R0,INTCNT		ELI16510
1998	2601		1652		AIS	R0,1		ELI16520
199A	4000	200A	1653		STH	R0,INTCNT		ELI16530
199E	C500	0008	1654		CLHI	R0,8	ALLOW UP TO 8	ELI16540
19A2	4280	1976	1655		BL	TST0.2		ELI16550
19A6	4300	198C	1656		B	TOEND		ELI16560
			1657	*				ELI16570
			1658	*				ELI16580
			1659	*	*****			ELI16590
			1660	*				ELI16600
			1661	*	PURPOSE OF TEST:			ELI16610
			1662	*	TEST 1 VERIFIES THAT ALL DEVICE INTERRUPTS ARE DISABLED WHEN A			ELI16620
			1663	*	'DISABLE' COMMAND IS ISSUED; THE ABILITY OF THE MASK TO PREVENT			ELI16630
			1664	*	INTERRUPTS IS ALSO TESTED.			ELI16640
			1665	*				ELI16650
			1666	*	ASSUMPTIONS:			ELI16660
			1667	*	IT IS ASSUMED THAT THE PROCESSOR, MEMORY, AND TELETYPE TESTS HAVE			ELI16670
			1668	*	BEEN RUN PRIOR TO SELECTING THIS TEST.			ELI16680
			1669	*				ELI16690
			1670	*	DESIGN SPECIFICATIONS:			ELI16700
			1671	*	DEVICE INTERRUPTS ARE ENABLED AND ALL LINES ARE SET, A 'DISABLE'			ELI16710
			1672	*	COMMAND IS ISSUE-, AND ALL LINES ARE MASKED. PROCESSOR INTERRUPTS			ELI16720
			1673	*	ARE ENABLED. NO INTERRUPT SHOULD OCCUR.			ELI16730
			1674	*				ELI16740
			1675	*	AFTER A FIXED DELAY, PROCESSOR INTERRUPTS ARE DISABLED, DEVICE			ELI16750
			1676	*	INTERRUPTS ARE ENABLED, AND ALL LINES ARE UNMASKED (ALL LINES ARE			ELI16760
			1677	*	SET). PROCESSOR INTERRUPTS ARE ENABLED. NO INTERRUPT SHOULD OCCUR.			ELI16770
			1678	*	AFTER A FIXED DELAY, PROCESSOR INTERRUPTS ARE DISABLED, AND THE			ELI16780
			1679	*	TEST TERMINATES.			ELI16790
			1680	*				ELI16800
			1681	*	THE TEST IS REPEATED EIGHT TIMES, EACH TIME USING THE ADDRESS OF			ELI16810
			1682	*	THE NEXT SEQUENTIAL INTERRUPT LINE AS THE DEVICE ADDRESS TO WHICH			ELI16820
			1683	*	ALL COMMANDS ARE SENT.			ELI16830
			1684	*				ELI16840

		1685	*	HOW TO RUN THE TEST:		ELI16850
		1686	*	ENTER APPROPRIATE VALUES FOR THE TIMVAL AND DEVA DR OPTIONS, SELECT		ELI16860
		1687	*	THE TEST, AND ENTER 'RUN'. NO MANUAL INTERVENTION IS REQUIRED.		ELI16870
		1688	*			ELI16880
		1689	*	OPTIONS:		ELI16890
		1690	*	TEST, DEVA DR, INTLEV, CONTIN, LOOP, TIMVAL.		ELI16900
		1691	*			ELI16910
		1692	*	ERRORS:		ELI16920
		1693	*	U2		ELI16930
		1694	*			ELI16940
	0000 19AA	1695	TEST1	EQU *		ELI16950
		1696	*			ELI16960
	19AA C810 192A	1697	LHI	R1,DEVINT		ELI16970
	19AE C800 1A0C	1698	LHI	R0,TIINT	UNEXPECTED INTPT HDLR	ELI16980
	19B2 0821	1699	LHR	R2,R1		ELI16990
	19B4 4002 0000	1700	TST1.0	STH R0,0(R2)		ELI17000
	19B8 2622	1701	AIS	R2,2		ELI17010
	19BA C521 0010	1702	CLHI	R2,16(R1)		ELI17020
	19BE 2085	1703	BLS	TST1.0		ELI17030
		1704	*			ELI17040
	19C0 4820 1910	1705	LH	R2,DEVSADR	LINE 0 ADDRESS	ELI17050
	19C4 4020 2010	1706	TST1.1	STH R2,CMDADR	ADDRESS TO WHICH COMMAND SENT	ELI17060
	19C8 2400	1707	LIS	R0,0		ELI17070
	19CA 4000 200A	1708	STH	R0,INTCNT		ELI17080
		1709	*			ELI17090
	19CE DE20 2015	1710	OC	R2,ENST	(ENAB INTPTS, SET MODE)	ELI17100
	19D2 DA20 2014	1711	WD	R2,ALLI	SET ALL LINES	ELI17110
	19D6 DE20 2017	1712	OC	R2,DSMK	(DISAB INTPTS, LOAD MASK MODE).	ELI17120
	19DA DA20 2014	1713	WD	R2,ALLI	MASK ALL LINES	ELI17130
	19DE 41F0 1F58	1714	BAL	LINK*DELAY	SHOULD NOT INTERRUPT.	ELI17140
		1715	*			ELI17150
	19E2 DE20 2013	1716	OC	R2,ENMK	(ENAB INTPTS, LOAD MASK MODE)	ELI17160
	19E6 DA20 200C	1717	WD	R2,ZERO	UNMASK ALL LINES	ELI17170
	19EA DE20 2015	1718	OC	R2,ENST	(ENAB INTPTS, SET MODE)	ELI17180
	19EE DA20 2014	1719	WD	R2,ALLI	SET ALL LINES	ELI17190
	19F2 41F0 1F58	1720	TST1.2	BAL LINK*DELAY	NO INTERRUPT SHOULD OCCUR	ELI17200
		1721	*			ELI17210
		1722	*			ELI17220
	19F6 4810 1910	1723	LH	R1,DEVSADR	LINE 0 ADDRESS	ELI17230
	19FA 4820 2010	1724	LH	R2,CMDADR		ELI17240
	19FE 2621	1725	AIS	R2,1	DEVICE ADDRESS	ELI17250
	1A00 C521 0008	1726	CLHI	R2,8(R1)		ELI17260
	1A04 4280 19C4	1727	BL	TST1.1	REPEAT FOR ALL LINE ADDRESSES.	ELI17270
		1728	*			ELI17280
	1A08 4300 0E5E	1729	T1END	B TSTEND	RETURN TO EXECUTIVE.	ELI17290
		1730	*			ELI17300
	0000 1A0C	1731	T1INT	EQU *	UNEXPECTED INTERRUPTS	ELI17310
	1A0C 41F0 1DBC	1732	BAL	R15,TT02		ELI17320
	1A10 4800 200A	1733	LH	R0,INTCNT		ELI17330
	1A14 2601	1734	AIS	R0,1		ELI17340
	1A16 4000 200A	1735	STH	R0,INTCNT		ELI17350
	1A1A C500 0008	1736	CLHI	R0,8		ELI17360
	1A1E 4280 19F2	1737	BL	TST1.2	ALLOW UP TO 8 SPUR INT	ELI17370
	1A22 4300 1A08	1738	B	T1END		ELI17380
		1739	*			ELI17390

		1740	*			ELI17400
		1741	*	*****		ELI17410
		1742	*			ELI17420
		1743	*	PURPOSE OF TEST:		ELI17430
		1744	*	TEST 2 VERIFIES THAT INTERRUPT LINES WITHIN THE MODULE MAY BE SET		ELI17440
		1745	*	INDIVIDUALLY UNDER PROGRAM CONTROL.		ELI17450
		1746	*			ELI17460
		1747	*	ASSUMPTIONS:		ELI17470
		1748	*	IT IS ASSUMED THAT THE PROCESSOR, MEMORY, AND TELETYPE TESTS HAVE		ELI17480
		1749	*	BEEN RUN PRIOR TO SELECTING THIS TEST.		ELI17490
		1750	*			ELI17500
		1751	*	DESIGN SPECIFICATIONS:		ELI17510
		1752	*	DEVICE INTERRUPTS ARE DISABLED, AND ALL LINES ARE MASKED. A 'CLEAR'		ELI17520
		1753	*	COMMAND IS THEN ISSUED, AND A SINGLE LINE IS SET. PROCESSOR		ELI17530
		1754	*	INTERRUPTS ARE ENABLED. A SINGLE INTERRUPT, FROM THE SET LINE,		ELI17540
		1755	*	IS EXPECTED. THIS SEQUENCE IS REPEATED UNTIL EACH LINE HAS BEEN		ELI17550
		1756	*	INDIVIDUALLY SET.		ELI17560
		1757	*			ELI17570
		1758	*	THE TEST IS REPEATED EIGHT TIMES, EACH TIME USING THE ADDRESS OF		ELI17580
		1759	*	THE NEXT SEQUENTIAL INTERRUPT LINE AS THE DEVICE ADDRESS TO WHICH		ELI17590
		1760	*	ALL COMMANDS ARE SENT.		ELI17600
		1761	*			ELI17610
		1762	*	HOW TO RUN THE TEST:		ELI17620
		1763	*	ENTER APPROPRIATE VALUES FOR THE TIMVAL AND DEVADR OPTIONS, SELECT		ELI17630
		1764	*	THE TEST, AND ENTER 'RUN'. NO MANUAL INTERVENTION IS REQUIRED.		ELI17640
		1765	*			ELI17650
		1766	*	OPTIONS:		ELI17660
		1767	*	TEST, DEVADR, INTLEV, CONTIN, LOOP, TIMVAL.		ELI17670
		1768	*			ELI17680
		1769	*	ERRORS:		ELI17690
		1770	*	01,04,06,08		ELI17700
		1771	*			ELI17710
		1772	TEST2	EQU *		ELI17720
		1773		LH R2,DEVSA	LINE 0 ADDRESS	ELI17730
		1774	*			ELI17740
		1775	TST2.0	STH R2,CMDADR	ADDRESS TO WHICH COMMAND SENT	ELI17750
		1776		LHI R8,X'100'	INITIAL LINE SELECTED = 0	ELI17760
		1777		STH R8,PATSAV		ELI17770
		1778	*			ELI17780
		1779	TST2.05	LH R2,CMDADR		ELI17790
		1780		OC R2,DSMK	(DISAB INTPTS, LOAD MASK MODE)	ELI17800
		1781		WD R2,ALLI	MASK ALL LINES	ELI17810
		1782		OC R2,CLRI	THEN SEND 'CLEAR' COMMAND	ELI17820
		1783	*			ELI17830
		1784	TST2.1	LHI R1,DEVINT		ELI17840
		1785		LHI R0,TT01X	UNEXPECTED INTPT HOLR	ELI17850
		1786		LHR R2,R1		ELI17860
		1787	TST2.2	STH R0,0(R2)		ELI17870
		1788		AIS R2,2		ELI17880
		1789		CLHI R2,16(R1)		ELI17890
		1790		BLS TST2.2		ELI17900
		1791	*			ELI17910
		1792		LHI R14,TST2.4	EXIT ADDRESS	ELI17920
		1793		LHI R9,T2INT	VECTOR ADDRESS	ELI17930
		1794		BAL R15,WALKIT	'WALKS' SELECTED LINE ACROSS MODULF	ELI17940
1A26	0000 1A26					
1A26	4820 1910					
1A2A	4020 2010					
1A2E	C880 0100					
1A32	4080 1FB8					
1A36	4820 2010					
1A3A	DE20 2017					
1A3E	DA20 2014					
1A42	DE20 2016					
1A46	C810 192A					
1A4A	C800 1090					
1A4E	0821					
1A50	4002 0000					
1A54	2622					
1A56	C521 0010					
1A5A	2085					
1A5C	C8E0 1A8A					
1A60	C890 1AA0					
1A64	41F0 1F72					

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1795 *                               AND ADJUSTS DEVINT TABLE          ELI17950
1796 *                               ELI17960
1A68 2400 1797 LIS R0,0                               ELI17970
1A6A 4000 200A 1798 STH R0,INTCVT                               ELI17980
1A6E 4820 2010 1799 LH R2,CMDADR                               ELI17990
1A72 0E20 2016 1800 OC R2,CLRI                               CLEAR ALL INTPTS          ELI18000
1A76 0E20 2015 1801 OC R2,ENST                               (ENAB INTPTS, SET MODE)  ELI18010
1A7A DA20 1FR9 1802 WD R2,PATSAV+1                               SET SELECTED LINE        ELI18020
1A7E 41F0 1F58 1803 BAL LINK,DELAY                               SHOULD INTERRUPT - ONCE ONLY. ELI18030
1804 *                               ELI18040
1A82 41F0 1E0C 1805 BAL R15,TT04                               INTERRUPT TIMEOUT -      *** ELI18050
1A86 4300 1A36 1806 B TST2.05                               ELI18060
1807 *                               ELI18070
1A8A 4810 1910 1808 TST2.4 LH R1,DEVSADR                               LINE 0 ADDRESS           ELI18080
1A8E 4820 2010 1809 LH R2,CMDADR                               ELI18090
1A92 2621 1810 AIS R2,1                               DEVICE ADDRESS           ELI18100
1A94 C521 0008 1811 CLHI R2,8(R1)                               ELI18110
1A98 4280 1A2A 1812 BL TST2.0                               REPEAT FOR ALL LINE ADDRESSES. ELI18120
1A9C 4300 1A00 1813 B T2END                               ELI18130
1814 *                               ELI18140
1AA0 4860 1FB6 1815 T2INT LH R6,WALKSV                               CURRENT LINE POINTER     ELI18150
1AA4 C870 1E02 1816 LHI R7,TT08X                               DOUBLE INTERRUPT VECTOR  ELI18160
1AA8 4076 0000 1817 STH R7,0(R6)                               ELI18170
1818 *                               ELI18180
1AAC CB60 192A 1819 SHI R6,DEVINT                               ELI18190
1AB0 9061 1820 SRLS R6,1                               ELI18200
1AB2 4A60 185E 1821 AH R6,DEVADR+6                               LINE 0 ADDRESS           ELI18210
1AB6 4560 1720 1822 CLH R6,INTDEV                               ELI18220
1ABA 2337 1823 BES T2INT,1                               ELI18230
1ABC 4870 1720 1824 LH R7,INTDEV                               ELI18240
1AC0 4070 2006 1825 STH R7,EXPDEV                               ELI18250
1AC4 41F0 1E7C 1826 BAL R15,TT06                               ELI18260
1AC8 41F0 1F58 1827 T2INT.1 BAL LINK,DELAY                               WAIT FOR MORE INTPTS.   ELI18270
1ACC 4300 1A46 1828 B TST2.1                               MOVE TO NEXT LINE       ELI18280
1829 *                               ELI18290
1AD0 4300 0E5E 1830 T2END B TSTEND                               ELI18300
1831 *                               ELI18310
1832 *                               ELI18320
1833 *                               ELI18330
1834 * *****                               ELI18340
1835 *                               ELI18350
1836 * PURPOSE OF TEST:                               ELI18360
1837 * TEST 3 VERIFIES THAT INTERRUPT LINES WITHIN THE MODULE MAY BE ELI18370
1838 * MASKED INDIVIDUALLY UNDER PROGRAM CONTROL. ELI18380
1839 *                               ELI18390
1840 * ASSUMPTIONS:                               ELI18400
1841 * IT IS ASSUMED THAT THE PROCESSOR, MEMORY, AND TELETYPE TESTS HAVE ELI18410
1842 * BEEN RUN PRIOR TO SELECTING THIS TEST. ELI18420
1843 *                               ELI18430
1844 * DESIGN SPECIFICATIONS:                               ELI18440
1845 * DEVICE INTERRUPTS ARE ENABLED, AND ALL LINES ARE SET. A SINGLE ELI18450
1846 * LINE IS MASKED, AND PROCESSOR INTERRUPTS ARE ENABLED, A SINGLE ELI18460
1847 * INTERRUPT, FROM THE MASKED LINE, IS EXPECTED. THIS SEQUENCE IS ELI18470
1848 * REPEATED UNTIL EACH LINE HAS BEEN INDIVIDUALLY MASKED. ELI18480
1849 *                               ELI18490

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		1850	*	THE TEST IS REPEATED EIGHT TIMES, EACH TIME USING THE ADDRESS OF	ELI18500
		1851	*	THE NEXT SEQUENTIAL INTERRUPT LINE AS THE DEVICE ADDRESS TO WHICH	ELI18510
		1852	*	ALL COMMANDS ARE SENT.	ELI18520
		1853	*		ELI18530
		1854	*	HOW TO RUN THE TEST:	ELI18540
		1855	*	ENTER APPROPRIATE VALUES FOR THE TIMVAL AND DEVADR OPTIONS; SELECT	ELI18550
		1856	*	THE TEST, AND ENTER 'RUN'. NO MANUAL INTERVENTION IS REQUIRED.	ELI18560
		1857	*		ELI18570
		1858	*	OPTIONS:	ELI18580
		1859	*	TEST, DEVADR, INTLEV, CONTIN, LOOP, TIMVAL.	ELI18590
		1860	*		ELI18600
		1861	*	ERRORS:	ELI18610
		1862	*	03,04,08	ELI18620
		1863	*		ELI18630
		1864	TEST3	EQU *	ELI18640
1AD4	0000 1AD4	1865	LH	R2,DEVSA DR	ELI18650
	4820 1910	1866	*	LINE 0 ADDRESS	ELI18660
1ADA	4020 2010	1867	TST3.0	STH R2,CMDADR	ELI18670
1ADC	C880 0100	1868	LHI	R8,X'100'	ELI18680
1AE0	4080 1FB8	1869	STH	R8,PATSAV	ELI18690
1AE4	2400	1870	LIS	R0,0	ELI18700
1AE6	4000 200A	1871	STH	R0,INTCNT	ELI18710
		1872	*	INIT COUNT.	ELI18720
1AEA	C860 187C	1873	TST3.1	LHI R6,T3INT.4	ELI18730
1AEE	C870 192A	1874	LHI	R7,DEVINT	ELI18740
1AF2	4067 0000	1875	TST3.2	STH R6,0(R7)	ELI18750
1AF6	2672	1876	AIS	R7,2	ELI18760
1AF8	C570 193A	1877	CLHI	R7,DEVINT+16	ELI18770
1AFC	2085	1878	RLS	TST3.2	ELI18780
		1879	*		ELI18790
1AFE	C890 1844	1880	TST3.3	LHI R9,T3INT	ELI18800
1802	C8E0 1B2A	1881	LHI	R14,TST3.5	ELI18810
1806	41FC 1F72	1882	BAL	R15,WALKIT	ELI18820
		1883	*	SELECT LINE; SET UP FOR TEST.	ELI18830
180A	4820 2010	1884	TST3.4	LH R2,CMDADR	ELI18840
180E	DE20 2015	1885	OC	R2,ENST	ELI18850
1812	DA20 2014	1886	WD	R2,ALLI	ELI18860
1816	DE20 2013	1887	OC	R2,ENMK	ELI18870
181A	DA20 1FB9	1888	WD	R2,PATSAV+1	ELI18880
181E	41F0 1F58	1889	BAL	LINK,DELAY	ELI18890
1822	41F0 1E0C	1890	BAL	R15,T04	ELI18900
1826	4300 1AEA	1891	B	TST3.1	ELI18910
		1892	*	SOFTWARE TIMEOUT	ELI18920
		1893	*	TRY NEXT PATTERN IF TIMEOUT	ELI18930
		1894	TST3.5	EQU *	ELI18940
		1895	*	COME HERE FROM T3INT WHEN	ELI18950
182A	41F0 1F58	1896	BAL	LINK,DELAY	ELI18960
182E	4810 1910	1897	LH	R1,DEVSA DR	ELI18970
1832	4820 2010	1898	LH	R2,CMDADR	ELI18980
1836	2621	1899	AIS	R2,1	ELI18990
1838	C521 0006	1900	CLHI	R2,8(R1)	ELI19000
183C	4280 1AD8	1901	BL	TST3.0	ELI19010
		1902	*	RUN WITH NEXT LINE ADDRESS	ELI19020
		1903	T3END	EQU *	ELI19030
1840	0000 1840	1904	B	TSTEND	ELI19040
	4300 0E5E			RETURN TO EXECUTIVE.	

		1905	*			ELI19050
		1906	*			ELI19060
		1907	*			ELI19070
		1908	T3INT	EQU *	EXPECTED INTERRUPT RECEIVED	ELI19080
1844	0000 1844	1909		LHI R7,DEVINT	VECTOR TABLE	ELI19090
1848	C870 192A	1910	T3INT.1	LH R6,0(R7)	PICK UP VECTOR	ELI19100
184C	4867 0000	1911		CLHI R6,T3INT	DOES IT POINT HERE ?	ELI19110
1850	C560 1844	1912		BE T3INT.2	YES -	ELI19120
1854	4330 185C	1913		AIS R7,2		ELI19130
1856	2672	1914		CLHI R7,DEVINT+16		ELI19140
185A	C570 193A	1915		BLS T3INT.1		ELI19150
	2089	1916	*			ELI19160
195C	C860 1E02	1917	T3INT.2	LHI R6,TT08X	DOUBLE INTERRUPT HANDLER	ELI19170
1860	4067 0000	1918		STH R6,0(R7)		ELI19180
1864	CB70 192A	1919		SHI R7,DEVINT		ELI19190
1868	9071	1920		SRLS R7,1	CORRESPONDING LINE ADDRESS	ELI19200
186A	4A70 185E	1921		AH R7,DEVADR+6	LINE 0 ADDRESS	ELI19210
186E	4570 1720	1922		CLH R7,INTDEV	DID THE CORRECT DEVICE INTERRUPT ?	ELI19220
1872	2135	1923		BNES T3INT.4		ELI19230
		1924	*		WHEN MASKED.	ELI19240
1874	41F0 1F58	1925	T3INT.3	BAL LINK,DELAY		ELI19250
1878	4300 1AEA	1926		B TST3.1		ELI19260
		1927	*			ELI19270
197C	41F0 10E4	1928	T3INT.4	BAL R15,TT03	WRONG LINE INTERRUPTED	ELI19280
1380	4800 200A	1929		LH R0,INTCNT		ELI19290
1884	2601	1930		AIS R0,1		ELI19300
1886	4000 200A	1931		STH R0,INTCNT		ELI19310
188A	C500 0008	1932		CLHI R0,8	ALLOW UP TO 8 SPUR INT.	ELI19320
189E	4280 1874	1933		BL T3INT.3		ELI19330
1892	4300 1840	1934		B T3END		ELI19340
		1935	*			ELI19350
		1936	*	*****		ELI19360
		1937	*			ELI19370
		1938	*	PURPOSE OF TEST:		ELI19380
		1939	*	TEST 4 VERIFIES THAT INTERRUPT LINES WITHIN THE MODULE MAY BE RESET		ELI19390
		1940	*	INDIVIDUALLY UNDER PROGRAM CONTROL.		ELI19400
		1941	*			ELI19410
		1942	*	ASSUMPTIONS:		ELI19420
		1943	*	IT IS ASSUMED THAT THE PROCESSOR, MEMORY, AND TELETYPE TESTS HAVE		ELI19430
		1944	*	BEEN RUN PRIOR TO SELECTING THIS TEST.		ELI19440
		1945	*			ELI19450
		1946	*	DESIGN SPECIFICATIONS:		ELI19460
		1947	*	DEVICE INTERRUPTS ARE DISABLED, AND ALL LINES ARE MASKED.		ELI19470
		1948	*	DEVICE INTERRUPTS ARE ENABLED, ALL LINES		ELI19480
		1949	*	ARE SET, AND A SINGLE LINE IS RESET, PROCESSOR INTERRUPTS ARE		ELI19490
		1950	*	ENABLED. SEVEN INTERRUPTS, ONE FROM EACH LINE EXCEPT THE LINE RESET,		ELI19500
		1951	*	ARE EXPECTED. THIS SEQUENCE IS REPEATED UNTIL EACH LINE HAS BEEN		ELI19510
		1952	*	INDIVIDUALLY RESET.		ELI19520
		1953	*			ELI19530
		1954	*	THE TEST IS REPEATED EIGHT TIMES, EACH TIME USING THE ADDRESS OF		ELI19540
		1955	*	THE NEXT SEQUENTIAL INTERRUPT LINE AS THE DEVICE ADDRESS TO WHICH		ELI19550
		1956	*	ALL COMMANDS ARE SENT.		ELI19560
		1957	*			ELI19570
		1958	*	HOW TO RUN THE TEST:		ELI19580
		1959	*	ENTER APPROPRIATE VALUES FOR THE TIMVAL AND DEVADR OPTIONS, SELECT		ELI19590

			1960	*	THE TEST, AND ENTER 'RUN'. NO MANUAL INTERVENTION IS REQUIRED.		ELI19600
			1961	*			ELI19610
			1962	*	OPTIONS:		ELI19620
			1963	*	TEST, DEVADR, INTLEV, CONTIN, LOOP, TIMVAL.		ELI19630
			1964	*			ELI19640
			1965	*	ERRORS:		ELI19650
			1966	*	04.05.06.08		ELI19660
			1967	*			ELI19670
			1968	TST4	EQU *		ELI19680
1896	0000 1896		1969	LH	R2,DEVSA DR	LINE 0 ADDRESS	ELI19690
	4620 1910		1970	*			ELI19700
			1971	TST4.0	STH R2,CMDADR	ADDRESS TO WHICH COMMAND SENT	ELI19710
189A	4020 2010		1972	LHI	R8,X'100'	TO SELECT FIRST LINE = LINE 0	ELI19720
189E	C880 0100		1973	STH	R8,PATSAV		ELI19730
19A2	4080 1F88		1974	*			ELI19740
			1975	TST4.1	LHI R6,T4INT		ELI19750
18A6	C860 1C10		1976	LHI	R7,DEVINT		ELI19760
18AA	C870 192A		1977	TST4.2	STH R6,0(R7)	EXPECTED INTERRUPT POINTERS	ELI19770
18AE	4067 0000		1978		AIS R7,2		ELI19780
1892	2672		1979	CLHI	R7,DEVINT+16		ELI19790
18A4	C570 193A		1980	BLS	TST4.2		ELI19800
18B8	2085		1981	*			ELI19810
			1982	TST4.3	LHI R9,T4INT.4	UNMASKED LINE INTERRUPT VECTOR	ELI19820
18BA	C890 1C62		1983	LHI	R14,TST4.6	EXIT ADDRESS	ELI19830
18BE	C8E0 1BF6		1984	BAL	R15,WALKIT	SELECT LINE; SET UP FOR TEST.	ELI19840
18C2	41FC 1F72		1985	*			ELI19850
			1986	TST4.4	LH R2,CMDADR		ELI19860
18C6	4820 2010		1987	OC	R2,DSMK	(DISAB INTPTS, LOAD MASK MODE)	ELI19870
18CA	0E20 2017		1988	WD	R2,ALLI	MASK ALL INTPTS	ELI19880
18CE	0A20 2014		1989	OC	R2,ENST	(ENAB INTPTS, SET MODE)	ELI19890
18D2	0E20 2015		1990	WD	R2,ALLI	SET ALL LINES	ELI19900
18D6	0A20 2014		1991	OC	R2,ENRT	(ENAB INTPTS, RESET MODE)	ELI19910
18DA	0E20 2012		1992	LB	R8,PATSAV+1		ELI19920
18DE	0380 1F89		1993	WDR	R2,R8	RESET TEST BIT	ELI19930
18E2	9A28		1994	*			ELI19940
			1995	LIS	R0,0		ELI19950
18E4	2400		1996	STH	R0,INTCNT	RESET COUNTER	ELI19960
18E6	4000 200A		1997	BAL	LINK,DELAY	WAIT FOR INTERRUPTS	ELI19970
18EA	41F0 1F58		1998	BAL	R15,TT04	SOFTWARE TIMEOUT	ELI19980
18EE	41F0 1E0C		1999	B	TST4.1	TRY NEXT PATTERN ON TIMEOUT	ELI19990
18F2	4300 18A6		2000	*			ELI20000
			2001	*			ELI20010
			2002	TST4.6	BAL LINK,DELAY	SHOULD NOT INTERRUPT.	ELI20020
18F6	41F0 1F58		2003	LH	R1,DEVSA DR	LINE 0 ADDRESS	ELI20030
18FA	4810 1910		2004	LH	R2,CMDADR		ELI20040
18FE	4820 2010		2005	AIS	R2,1		ELI20050
1C02	2621		2006	CLHI	R2,8(R1)		ELI20060
1C04	C521 0008		2007	BL	TST4.0	RUN WITH NEXT LINE ADDRESS.	ELI20070
1C08	4280 189A		2008	*			ELI20080
			2009	T4END	EQU *		ELI20090
	0000 1C0C		2010	B	TSTEND	RETURN TO EXECUTIVE.	ELI20100
1C0C	4300 0E5E		2011	*			ELI20110
			2012	*			ELI20120
			2013	*			ELI20130
			2014	T4INT	EQU *	EXPECTED INTERRUPT RECEIVED.	ELI20140
	0000 1C10						

1C10	C870	192A	2015		LHI	R7,DEVINT	VECTOR TABLE	ELI20150
1C14	4867	0000	2016	T4INT.1	LH	R6,0(R7)	PICK UP VECTOR	ELI20160
1C18	C560	1C10	2017		CLHI	R6,T4INT	DOES IT POINT HERE ?	ELI20170
1C1C	4330	1C28	2018		BE	T4INT.2	YES -	ELI20180
1C20	2672		2019		AIS	R7,2		ELI20190
1C22	C570	193A	2020		CLHI	R7,DEVINT+16		ELI20200
1C26	2089		2021		BLS	T4INT.1		ELI20210
			2022	*				ELI20220
1C28	C860	1ED2	2023	T4INT.2	LHI	R6,TT08X	DOUBLE INTERRUPT HDLR	ELI20230
1C2C	4067	0000	2024		STH	R6,0(R7)		ELI20240
1C30	C870	192A	2025		SHI	R7,DEVINT		ELI20250
1C34	9071		2026		SRLS	R7,1	CORRESPONDING LINE ADDRESS	ELI20260
1C36	4A70	185E	2027		AH	R7,DEVADR+6	LINE 0 ADDRESS	ELI20270
1C3A	4570	1720	2028		CLH	R7,INTDEV	DID CORRECT DEVICE INTERRUPT ?	ELI20280
1C3E	2335		2029		BES	T4INT.3		ELI20290
1C40	4070	2006	2030		STH	R7,EXPDEV	EXPECTED DVADR ON INTERRUPT	ELI20300
1C44	41F0	1E3E	2031		BAL	R15,TT05	ERROR TT05 - LINE INTERRUPTED	ELI20310
			2032	*			OUT OF SEQUENCE	ELI20320
1C48	48F0	200A	2033	T4INT.3	LH	R15,INTCNT	HOW MANY TIMES, NOW ?	ELI20330
1C4C	26F1		2034		AIS	R15,1		ELI20340
1C4E	40F0	200A	2035		STH	R15,INTCNT		ELI20350
1C52	C5F0	0007	2036		CLHI	R15,7	ENOUGH ?	ELI20360
1C56	4280	18EA	2037		BL	TST4,5	NO.	ELI20370
1C5A	41F0	1F58	2038		BAL	LINK,DELAY	EXTRA INTPT QUEUED ?	ELI20380
1C5E	4300	18A6	2039		B	TST4,1	TRY NEXT PATTERN	ELI20390
			2040	*				ELI20400
1C62	4870	1F86	2041	T4INT.4	LH	R7,WALKSV		ELI20410
1C66	C860	1ED2	2042		LHI	R6,TT08X		ELI20420
1C6A	4067	0000	2043		STH	R6,0(R7)		ELI20430
1C6E	C870	192A	2044		SHI	R7,DEVINT		ELI20440
1C72	9071		2045		SRLS	R7,1		ELI20450
1C74	4A70	185E	2046		AH	R7,DEVADR+6		ELI20460
1C78	4070	2006	2047		STH	R7,EXPDEV		ELI20470
1C7C	41F0	1E7C	2048		BAL	R15,TT06	INTERRUPT WHEN RESET	ELI20480
1C80	4300	1C48	2049		B	T4INT.3	CONTINUE	ELI20490
			2050	*				ELI20500
			2051	*				ELI20510
			2052	*				ELI20520
			2053	*			*****	ELI20530
			2054	*				ELI20540
			2055	*				ELI20550
			2056	*			PURPOSE OF TEST:	ELI20560
			2057	*			TEST 5 VERIFIES THAT INTERRUPTS MAY BE GENERATED BY MANUALLY	ELI20570
			2058	*			CONNECTING CERTAIN PINS ON CONNECTOR 'A' OF THE 7" 29-237	ELI20580
			2059	*			EIGHT LINE INTERRUPT MODULE (SEE PAGE 6 OF 06-134R03A15)	ELI20590
			2060	*				ELI20600
			2061	*			ASSUMPTIONS:	ELI20610
			2062	*			IT IS ASSUMED THAT THE PROCESSOR, MEMORY, AND TELETYPE TESTS HAVE	ELI20620
			2063	*			BEEN RUN PRIOR TO SELECTING THIS TEST.	ELI20630
			2064	*				ELI20640
			2065	*			DESIGN SPECIFICATIONS:	ELI20650
			2066	*			A 'CLEAR' COMMAND IS ISSUED, DEVICE INTERRUPTS ARE ENABLED AND ALL	ELI20660
			2067	*			LINE ARE MASKED. THE MESSAGE 'GENERATE INTERRUPT' IS PRINTED ON THE	ELI20670
			2068	*			CONSOLE. PROCESSOR INTERRUPTS ARE ENABLED, AND THE PROGRAM WAITS	ELI20680
			2069	*			SEVERAL SECONDS FOR AN INTERRUPT. AFTER AN INTERRUPT HAS OCCURRED	ELI20690

		2070	*	THE PROGRAM PRINTS OUT THE DEVICE ADDRESS AND STATUS. IT THEN		ELI20700
		2071	*	WAITS FOR ANY QUEUED INTERRUPTS. IF NO INTERRUPTS OCCUR THE		ELI20710
		2072	*	PROGRAM PRINTS OUT 'NO MORE INTERRUPTS'. THIS PROCESS IS REPEATED		ELI20720
		2073	*	EIGHT TIMES, THE TEST TERMINATES.		ELI20730
		2074	*			ELI20740
		2075	*	HOW TO RUN THE TEST:		ELI20750
		2076	*	ENTER APPROPRIATE VALUES FOR THE TIMVAL AND DEVADR OPTIONS, SELECT		ELI20760
		2077	*	THE TEST, AND ENTER 'RUN'. THE PROGRAM DELAYS APPROX. 20 SECONDS		ELI20770
		2078	*	WAITING FOR AN INTERRUPT. CONNECT PIN 200-A ON THE INTERRUPT		ELI20780
		2079	*	MODULE BOARD TO PINS 100-A, 102-A,...., 114A TO MANUALLY GENERATE		ELI20790
		2080	*	INTERRUPTS ON LINES 0-7, RESPECTIVELY.		ELI20800
		2081	*	OPTIONS:		ELI20810
		2082	*	TEST, DEVADR, INTLEV, CONTIN, LOOP, TIMVAL.		ELI20820
		2083	*			ELI20830
		2084	*	ERRORS:		ELI20840
		2085	*	U3,07		ELI20850
		2086	*			ELI20860
		2087	*			ELI20870
		2088	TST5	EQU *	MANUAL INTERRUPT TEST	ELI20880
		2089	*			ELI20890
		2090	TST5.0	LIS R0,0		ELI20900
	1C84	2400				ELI20910
	1C86	4000	2091	STH R0,INTCNT	INITIALIZE COUNT	ELI20920
			2092	*		ELI20930
	1C8A	C810	2093	TST5.1	LHI R1,DEVINT	ELI20940
	1C8E	C800	2094		LHI R0,T5INT	INTERRUPT VECTOR
	1C92	0821	2095		LHR R2,R1	ELI20950
	1C94	4002	2096	TST5.11	STH R0,0(R2)	EXPECTED INTERRUPT HOLDER
	1C98	2622	2097		AIS R2,2	ELI20970
	1C9A	C521	2098		CLHI R2,16(R1)	ELI20980
	1C9E	2085	2099		BLS TST5.11	ELI20990
			2100	*		ELI21000
	1CA0	4820	2101		LH R2,DEVSDR	LINE '0' ADDRESS
	1CA4	4020	2102		STH R2,CMDADR	ELI21010
			2103	*		ELI21020
	1CA8	2501	2104	TST5.2	LCS R0,1	ELI21030
	1CAA	DE20	2105		OC R2,CLRI	CLEAR ALL LINES
	1CAE	DE20	2106		OC R2,ENRT	ELI21050
	1CB2	9A20	2107		WDR R2,R0	RESET ALL LINES
	1CB4	DE20	2108		OC R2,ENMK	(ENAB INTPTS, LOAD MASK CODE)
	1CB8	2400	2109		LIS R0,0	ELI21070
	1CBA	9A20	2110		WDR R2,R0	UNMASK ALL LINES
	1CB0	DE20	2111		OC R2,ENMK	ELI21090
	1CC0	DA20	2112		WD R2,ALLI	ELI21100
			2113	*		MASK ALL LINES
	1CC4	C850	2114		LHI R5,MESSAGE	ELI21120
	1CC8	41F0	2115		BAL R15,PRINT	ELI21130
	1CCC	2571	2116		LCS R7,1	'GENERATE INTERRUPT'
	1CCE	4070	2117		STH R7,MSK	INITIALIZE MASK VALUE
			2118	*		ELI21150
	1CD2	C800	2119		LHI R0,X'0600'	ELI21160
	1CD6	41F0	2120		BAL LINK,TIMER	WAIT FOR SOME MILLISECONDS....
	1CDA	41F0	2121		BAL LINK,DELAY	CHECK FOR INTERRUPT
	1CDE	41F0	2122		BAL LINK,TSTBRK	ELI21200
	1CE2	41F0	2123		BAL R15,TT07	TIMEOUT- NO INTERRUPT.
	1CE6	4300	2124		B TST5.4	ELI21220
						ELI21230
						ELI21240

		2125	*				ELI21250		
		2126	*				ELI21260		
1CEA	4880	1720	2127	TST5.3	LH	R8,INTDEV	CHECK FOR QUEUED INTERRUPTS	E	ELI21270
1CEE	4880	1910	2128		SH	R8,DEVSADR	LINE NUMBER IN REG.8		ELI21280
1CF2	C870	0100	2129		LHI	R7,X'100'			ELI21290
1CF6	CC78	0000	2130		SRHL	R7,0(R8)	SHIFT		ELI21300
1CFA	4070	1FB8	2131		STH	R7,PATSAV	STORE PATTERN IN PATSAV		ELI21310
1CFE	C890	1D58	2132		LHI	R9,T5ERR	UNEXPECTED INTERRUPT VECTOR		ELI21320
1002	C8E0	1C8A	2133		LHI	R14,TST5.1	DUMMY EXIT		ELI21330
1006	41F0	1F72	2134		BAL	LINK,WALKIT	STORE ERROR VECTOR IN DEVICE		ELI21340
100A	4810	1FB8	2135		LH	R1,PATSAV	INTRPT HOLDER FOR LINE WHICH		ELI21350
100E	4710	2018	2136		XH	R1,MSK	JUST INTERRUPTED.	F	ELI21360
1012	4010	2018	2137		STH	R1,MSK		E	ELI21370
1016	4820	1910	2138		LH	R2,DEVSADR			ELI21380
101A	0E20	2012	2139		OC	R2,ENRT	RESET THE LINE WHICH INTRP'ED	EL	ELI21390
101E	DA20	1FB9	2140		WD	R2,PATSAV+1			ELI21400
1022	0E20	2013	2141		OC	R2,ENMK			ELI21410
1026	0A20	2019	2142		WD	R2,MSK+1	MASK OUT LINES WHICH HAVE INTRP'D		ELI21420
102A	41F0	1F58	2143		BAL	LINK,DELAY	CHECK FOR MORE INTERRUPTS		ELI21430
102E	41F0	1358	2144		BAL	LINK,TSTBRK			ELI21440
1032	4800	183A	2145		LH	R0,NOMSG+6	COMMENTS ALLOWED?		ELI21450
1036	4230	1D42	2146		BNZ	TST5.4			ELI21460
103A	C850	18FC	2147		LHI	R5,QUEMSG			ELI21470
103E	41F0	11F0	2148		BAL	LINK,PRINT	PRINT 'NO MORE INTERRUPTS'		ELI21480
			2149	*					ELI21490
1042	4800	200A	2150	TST5.4	LH	R0,INTCNT	TIMEOUT OR INTRPT-INCREMENT COUNT		ELI21500
1046	2601		2151		AIS	R0,1			ELI21510
1048	4000	200A	2152		STH	R0,INTCNT			ELI21520
104C	C500	0008	2153		CLHI	R0,8			ELI21530
1050	4230	1C8A	2154		BNE	TST5.1	ALLOW A TIMEOUTS,INTPTS		ELI21540
	0000	1054	2155	T5END	EQU	*			ELI21550
1054	4300	0E5E	2156		B	T5END			ELI21560
			2157	*					ELI21570
1058	C8F0	1CFA	2158	T5ERR	LHI	R15,TST5.3	HERE WHEN DEVICE INTERRUPTS		ELI21580
105C	4300	1DF4	2159		B	TT03	TWICE OR MORE		ELI21590
			2160	*					ELI21600
	0000	1D60	2161	T5INT	EQU	*	HERE WHEN MODULE INTERRUPTS.		ELI21610
			2162	*			ALL DEVICE ADDRESS CHECKING		ELI21620
			2163	*			DONE BY SUPERVISOR; TIMEOUT CHECK		ELI21630
			2164	*			BY TST5.4.		ELI21640
	0000	1D60	2165	PRDOS	EQU	*	PRINTS 'DEV DDD STA SS' COMMENT		ELI21650
1060	0000	2130	2166		STM	R0,ERRSAVE+64			ELI21660
1064	2403		2167		LIS	R0,3			ELI21670
1066	4810	1720	2168		LH	R1,INTDEV			ELI21680
106A	C820	17BC	2169		LHI	R2,ASCIDEV			ELI21690
106E	41F0	1190	2170		BAL	LINK,HEXASC			ELI21700
1072	2402		2171		LIS	R0,2			ELI21710
1074	0310	1722	2172		LB	R1,INTSTA			ELI21720
1078	C820	17C4	2173		LHI	R2,ASCISTA			ELI21730
107C	41F0	1190	2174		BAL	LINK,HEXASC			ELI21740
1080	C850	17B8	2175		LHI	R5,DEVMSG			ELI21750
1084	41F0	11F0	2176		BAL	LINK,PRINT	'DEV DDD STA SS'		ELI21760
1088	0100	2130	2177		LM	R0,ERRSAVE+64			ELI21770
108C	4300	1CEA	2178		B	TST5.3	DO ANOTHER TIME.	E	ELI21780
			2179	*					ELI21790

			2180	*			ELI21800
			2181	*			ELI21810
1090	C8F0 0E5E		2182	TT01X	LHI R15,TSTEND	SPVSR RETURN	ELI21820
	0000 1D94		2183	TT01	EQU *	INTERRUPT AFTER 'CLEAR' COMMAND	ELI21830
1094	40F0 1F56		2184		STH R15,LINKSAV		ELI21840
1098	40F0 174E		2185		STH R15,ISITERR	FORCE ERROR PRINT	ELI21850
109C	C800 3031		2186		LHI R0,C'01'		ELI21860
10A0	4000 179A		2187		STH R0,ERRNO		ELI21870
10A4	41E0 102A		2188		BAL R14,ERR1	'ERROR TT01'	ELI21880
10A8	4800 1720		2189		LH R0,INTDEV		ELI21890
10AC	4000 1720		2190		STH R0,ERRDEV		ELI21900
10B0	41E0 1034		2191		BAL R14,ERR01	'DEV DDD'	ELI21910
10B4	4300 1F1A		2192		B TSTENDX		ELI21920
			2193	*			ELI21930
10B8	C8F0 0E5E		2194	TT02X	LHI R15,TSTEND	SPVSR RETURN	ELI21940
	0000 1DB8		2195	TT02	EQU *	INTERRUPT WHILE DISABLED	ELI21950
10BC	40F0 1F56		2196		STH R15,LINKSAV		ELI21960
10C0	40F0 174E		2197		STH R15,ISITERR		ELI21970
10C4	C800 3032		2198		LHI R0,C'02'		ELI21980
10C8	4000 179A		2199		STH R0,ERRNO		ELI21990
10CC	41E0 102A		2200		BAL R14,ERR1	'ERROR TT02'	ELI22000
10D0	4800 1720		2201		LH R0,INTDEV		ELI22010
10D4	4000 1720		2202		STH R0,ERRDEV		ELI22020
10D8	41E0 1034		2203		BAL R14,ERR01	'DEV DDD'	ELI22030
10DC	4300 1F1A		2204		B TSTENDX		ELI22040
			2205	*			ELI22050
10E0	C8F0 0E5E		2206	TT03X	LHI R15,TSTEND	SPVSR RETURN	ELI22060
	0000 10E4		2207	TT03	EQU *	INTERRUPT WHILE MASKED	ELI22070
10E4	40F0 1F56		2208		STH R15,LINKSAV		ELI22080
10E8	40F0 174E		2209		STH R15,ISITERR		ELI22090
10EC	C800 3033		2210		LHI R0,C'03'		ELI22100
10F0	4000 179A		2211		STH R0,ERRNO		ELI22110
10F4	41E0 102A		2212		BAL R14,ERR1	'ERROR TT03'	ELI22120
10F8	4800 1720		2213		LH R0,INTDEV		ELI22130
10FC	4000 1720		2214		STH R0,ERRDEV		ELI22140
1E00	41E0 1034		2215		BAL R14,ERR01	'DEV DDD'	ELI22150
1E04	4300 1F1A		2216		B TSTENDX		ELI22160
			2217	*			ELI22170
1E08	C8F0 0E5E		2218	TT04X	LHI R15,TSTEND	SPVSR RETURN	ELI22180
	0000 1E0C		2219	TT04	EQU *	INTERRUPT TIMEOUT	ELI22190
1E0C	40F0 1F56		2220		STH R15,LINKSAV		ELI22200
1E10	40F0 174E		2221		STH R15,ISITERR		ELI22210
1E14	C800 3034		2222		LHI R0,C'04'		ELI22220
1E18	4000 179A		2223		STH R0,ERRNO		ELI22230
1E1C	41E0 102A		2224		BAL R14,ERR1	'ERROR TT04'	ELI22240
1E20	4800 1FB6		2225		LH R0,WALKSV	POINTER SAVE	ELI22250
1E24	C800 192A		2226		SHI R0,DEVINT	DEVINT TABLE START	ELI22260
1E28	9001		2227		SRLS R0,1	(R0) = DEVICE ADDRESS	ELI22270
1E2A	4A00 185E		2228		AH R0,DEVADR+6	LINE 0 ADDRESS	ELI22280
1E2E	4000 1720		2229		STH R0,ERRDEV		ELI22290
1E32	41E0 1034		2230		BAL R14,ERR01	'DEV DDD'	ELI22300
1E36	4300 1F1A		2231		B TSTENDX		ELI22310
			2232	*			ELI22320
1E3A	C8F0 0E5E		2233	TT05X	LHI R15,TSTEND	SPVSR RETURN	ELI22330
	0000 1E3E		2234	TT05	EQU *	WRONG INTERRUPT LINE (PRIORITY)	ELI22340

1E3E	40F0	1F56	2235	STH	R15,LINKSAV		ELI22350
1E42	40F0	174E	2236	STH	R15,ISITERR		ELI22360
1E46	C800	3035	2237	LHI	R0,C'05'		ELI22370
1E4A	4000	179A	2238	STH	R0,ERRNO		ELI22380
1E4E	41E0	102A	2239	BAL	R14,ERR1	'ERROR TT05'	ELI22390
1E52	4800	1720	2240	LH	R0,INTDEV		ELI22400
1E56	4000	1720	2241	STH	R0,ERRDEV		ELI22410
1E5A	41E0	1034	2242	BAL	R14,ERRD1	'DEV DDD'	ELI22420
1E5E	4810	2006	2243	LH	R1,EXPDEV		ELI22430
1E62	2403		2244	LIS	R0,3		ELI22440
1E64	C820	18EA	2245	LHI	R2,ASCEXP		ELI22450
1E68	41F0	1190	2246	BAL	R15,HEXASC		ELI22460
1E6C	C850	18E6	2247	LHI	R5,ASCMSG		ELI22470
1E70	41F0	11F0	2248	BAL	R15,PRINT	'EXP DDD'	ELI22480
1E74	4300	1F1A	2249	B	TSTENDX		ELI22490
			2250	*			ELI22500
1E78	C8F0	0E5E	2251	TT06X	LHI R15,TSTEND	SPVSR RETURN	ELI22510
	0000	1E7C	2252	TT06	EQU *	INTERRUPT AFTER LINE RESET	ELI22520
1E7C	40F0	1F56	2253	STH	R15,LINKSAV		ELI22530
1E80	40F0	174E	2254	STH	R15,ISITERR		ELI22540
1E84	C800	3036	2255	LHI	R0,C'06'		ELI22550
1E88	4000	179A	2256	STH	R0,ERRNO		ELI22560
1E8C	41E0	102A	2257	BAL	R14,ERR1	'ERROR TT06'	ELI22570
1E90	4800	1720	2258	LH	R0,INTDEV		ELI22580
1E94	4000	1720	2259	STH	R0,ERRDEV		ELI22590
1E98	41E0	1034	2260	BAL	R14,ERRD1	'DEV DDD'	ELI22600
1E9C	4810	2006	2261	LH	R1,EXPDEV		ELI22610
1EA0	2403		2262	LIS	R0,3		ELI22620
1EA2	C820	18F6	2263	LHI	R2,ASCRST		ELI22630
1EA6	41F0	1190	2264	BAL	R15,HEXASC		ELI22640
1EAA	C850	18F0	2265	LHI	R5,RSTMSG		ELI22650
1EAE	41F0	11F0	2266	BAL	R15,PRINT	'RESET DDD'	ELI22660
1EB2	4300	1F1A	2267	B	TSTENDX		ELI22670
			2268	*			ELI22680
1EB6	C8F0	0E5E	2269	TT07X	LHI R15,TSTEND	SPVSR RETURN	ELI22690
	0000	1EBA	2270	TT07	EQU *	NO INTERRUPT FROM EXTERNAL LINE	ELI22700
1EB8	40F0	1F56	2271	STH	R15,LINKSAV		ELI22710
1EBE	40F0	174E	2272	STH	R15,ISITERR	FORCE ERROR PRINT	ELI22720
1EC2	C800	3037	2273	LHI	R0,C'07'		ELI22730
1EC6	4000	179A	2274	STH	R0,ERRNO		ELI22740
1ECA	41E0	102A	2275	BAL	R14,ERR1	'ERROR TT07'	ELI22750
1ECE	4300	1F1A	2276	B	TSTENDX		ELI22760
			2277	*			ELI22770
			2278	*			ELI22780
1ED2	C8F0	0E5E	2279	TT08X	LHI R15,TSTEND	SPVSR RETURN	ELI22790
	0000	1ED6	2280	TT08	EQU *	MULTIPLE INTERRUPTS	ELI22800
1ED6	40F0	1F56	2281	STH	R15,LINKSAV		ELI22810
1EDA	40F0	174E	2282	STH	R15,ISITERR		ELI22820
1EDE	C800	3038	2283	LHI	R0,C'08'		ELI22830
1EE2	4000	179A	2284	STH	R0,ERRNO		ELI22840
1EE6	41E0	102A	2285	BAL	R14,ERR1	'ERROR TT08'	ELI22850
1EEA	4800	1720	2286	LH	R0,INTDEV	INTERRUPTING DEVICE	ELI22860
1EEE	4000	1720	2287	STH	R0,ERRDEV		ELI22870
1EF2	41E0	1034	2288	BAL	R14,ERRD1	'DEV DDD'	ELI22880
1EF6	4300	1F1A	2289	B	TSTENDX		ELI22890

		2290	*				ELI22900
1EFA	C8F0	0E5E					ELI22910
1EFE	40F0	1F56	2291	TT09X	LHI	R15, TSTEND	ELI22920
1F02	40F0	174E	2292	TT09	STH	R15, LINKSAV	ELI22930
1F06	C800	3039	2293		STH	R15, ISITERR	ELI22940
1F0A	4000	179A	2294		LHI	R0, C'09'	ELI22950
			2295		STH	R0, ERRNO	ELI22960
			2296	*			ELI22970
1F0E	41E0	102A	2297		BAL	R14, ERR1	ELI22980
1F12	4020	1720	2298		STH	R2, ERRDEV	ELI22990
1F16	41E0	1034	2299		BAL	R14, ERR01	ELI23000
	0000	1F1A	2300	TSTENDX	EQU	*	ELI23010
1F1A	4800	175A	2301		LH	R0, TOTERR	ELI23020
1F1E	2601		2302		ALS	R0, 1	ELI23030
1F20	4000	175A	2303		STH	R0, TOTERR	ELI23040
			2304	*			ELI23050
1F24	4810	2010	2305		LH	R1, CMDADR	ELI23060
1F28	2403		2306		LIS	R0, 3	ELI23070
1F2A	4000	174E	2307		STH	R0, ISITERR	ELI23080
1F2E	4000	1750	2308		STH	R0, NOERR	ELI23090
1F32	C820	18E0	2309		LHI	R2, ASCADR	ELI23100
1F36	41F0	1190	2310		BAL	R15, HEXASC	ELI23110
1F3A	C850	1808	2311		LHI	R5, CMDMSG	ELI23120
1F3E	41F0	11F0	2312		BAL	R15, PRINT	ELI23130
1F42	41F0	1274	2313		BAL	R15, CRLF	ELI23140
1F46	2400		2314		LIS	R0, 0	ELI23150
1F48	4000	174E	2315		STH	R0, ISITERR	ELI23160
1F4C	41F0	1358	2316		BAL	LINK, TSTBRK	ELI23170
1F50	48F0	1F56	2317		LH	R15, LINKSAV	ELI23180
1F54	030F		2318		BR	R15	ELI23190
			2319	*			ELI23200
1F56	0000		2320	LINKSAV	OCX	0	
							RETURN ADDRESS
			2322	* SUBROUTINE DELAY - SOFTWARE INTERRUPT TIMEOUT.			ELI23220
			2323	* CALLING SEQUENCE:			ELI23230
			2324	*	BAL	LINK, DELAY	ELI23240
			2325	*			ELI23250
1F58	4850	1852	2326	DELAY	LH	R5, TIMVAL+6	ELI23260
1F5C	4330	1F6A	2327		BZ	DLY, 2	ELI23270
1F60	4870	0A26	2328		LH	R7, PSW3	ELI23280
1F64	9567		2329		EPSR	R6, R7	ELI23290
	0000	1F66	2330	DLY, 0	EQU	*	ELI23300
1F66	2751		2331		SIS	R5, 1	ELI23310
1F68	2031		2332		BNZS	DLY, 0	ELI23320
1F6A	4870	0A24	2333	DLY, 2	LH	R7, PSW2	ELI23330
1F6E	9567		2334		EPSR	R6, R7	ELI23340
1F70	030F		2335		BR	LINK	ELI23350
			2336	*			ELI23360
			2337	* SUBROUTINE WALKIT - 'WALKS' SELECTED LINE ACROSS MODULE.			ELI23370
			2338	* CALLING SEQUENCE:			ELI23380
			2339	*	STH	R8, PATSAV	ELI23390
			2340	*	LHI	R9, VECTOR	ELI23400
			2341	*	LHI	R14, EXIT ADDRESS	ELI23410
			2342	*	BAL	R15, WALKIT	ELI23420

COMMAND ADDRESS PRINT & RETURN

FORCE ERROR PRINT
SUPPRESS THAT PRINT.

'CMD ADR DDD'

CLEAR ERROR FLAG

RETURN ADDRESS

COMMON PSW3 USED IN I/O TEST

ELI
DISABLE EXTR. INTPTS

INITIAL BIT PATTERN
FOR SELECTED LINE
(TAKEN IF ALL BITS 0 OR 1 TOGETHER)

		2343	*				ELI23430	
1F72	C860	193A	2344	WALKIT	LHI	R6,DEVINT+16	NTPT HDLR TABLE	ELI23440
1F76	4880	1FB8	2345		LH	R8,PATSAV		ELI23450
1F7A	CE80	0001	2346		SRHA	R8,1		ELI23460
1F7E	4080	1F8A	2347		STH	R8,PATSAV	UPDATED PATTERN	ELI23470
1F82	0878		2348		LHR	R7,R8	COPY CURRENT PATTERN	ELI23480
1F84	2762		2349	WALK.1	SIS	R6,2		ELI23490
1F86	CE70	0001	2350		SRHA	R7,1		ELI23500
1F8A	2186		2351		BCS	WALK.2		ELI23510
1F8C	033E		2352		BZR	R14	ZERO RESULT - EXIT	ELI23520
1F8E	C570	FFFF	2353		CLHI	R7,-1	ALL BITS SET ?	ELI23530
1F92	033E		2354		BEK	R14	YES. EXIT.	ELI23540
1F94	2208		2355		RS	WALK.1		ELI23550
1F96	4096	0000	2356	WALK.2	STH	R9,0(R6)	CURRENT VECTOR	ELI23560
1F9A	4060	1FB6	2357		STH	R6,WALKSV	POINTER TO VECTOR	ELI23570
1F9E	2401		2358		LIS	R0,1		ELI23580
1FA0	DE00	200E	2359		OC	R0,DISINC		ELI23590
1FA4	DA00	1FB9	2360		WD	R0,PATSAV+1		ELI23600
1FA8	DA00	200C	2361		WD	R0,ZERO		ELI23610
1FAC	D800	200C	2362		WH	R0,ZERO		ELI23620
1FB0	DE00	200F	2363		OC	R0,DISNORM		ELI23630
1FB4	030F		2364		BR	R15	RETURN	ELI23640
			2365	*				ELI23650
1FB6	0000		2366	WALKSV	DCX	0	POINTER TO CURRENT VECTOR	ELI23660
1FB8	0000		2367	PATSAV	DCX	0	UPDATED PATTERN	ELI23670
			2368	*				ELI23680
			2369	*	*****			ELI23690
			2370	*				ELI23700
	0000	1F8A	2371	INIT	EQU	*	TO SET UP BASIC TABLES, ETC.	ELI23710
			2372	*				ELI23720
1FBA	C810	1910	2373		LHI	R1,DEVSADR	SET UP DEVSADR TABLE ***	ELI23730
1FBE	4800	185E	2374		LH	R0,DEVADR+6	LINE 0 ADDRESS	ELI23740
1FC2	C400	0FFF	2375		NHI	R0,X'0FFF'		ELI23750
1FC6	0820		2376		LHR	R2,R0	SAVE COPY	ELI23760
1FCB	4001	0000	2377	INIT.1	STH	R0,0(R1)	EIGHT CONSECUTIVE DVAOR'S	ELI23770
1FCC	2601		2378		AIS	R0,1		ELI23780
1FCE	2612		2379		AIS	R1,2		ELI23790
1FD0	C502	0008	2380		CLHI	R0,8(R2)		ELI23800
1FD4	2086		2381		BLS	INIT.1		ELI23810
			2382	*				ELI23820
1FD6	C810	1922	2383		LHI	R1,INTLVL	SET UP INTLVL TABLE ***	ELI23830
1FDA	0821		2384		LHR	R2,R1		ELI23840
1FDC	4800	1846	2385		LH	R0,INTLEV+6	LINE 0 ADDRESS. INTLEV	ELI23850
1FE0	C400	000F	2386		NHI	R0,15	EXTRACT INTLVL INFO	ELI23860
1FE4	D202	0000	2387	INIT.2	STB	R0,0(R2)		ELI23870
1FE8	2621		2388		AIS	R2,1		ELI23880
1FEA	C521	0008	2389		CLHI	R2,8(R1)		ELI23890
1FEE	2085		2390		BLS	INIT.2		ELI23900
			2391	*				ELI23910
1FF0	C810	192A	2392		LHI	R1,DEVINT	SET UP DEVINT TABLE ***	ELI23920
1FF4	2400		2393		LIS	R0,0	NO INTERRUPTS EXPECTED	ELI23930
1FF6	0821		2394		LHR	R2,R1		ELI23940
1FF8	4002	0000	2395	INIT.3	STH	R0,0(R2)		ELI23950
1FFC	2622		2396		AIS	R2,2		ELI23960
1FFE	C521	0010	2397		CLHI	R2,16(R1)		ELI23970

CHKSUM

		2444	*			ELI24440
		2445	*	CHKSUM		ELI24450
		2446	*	(THE FOLLOWING CODE IS NOT PART OF THE TEST.)		ELI24460
		2447	*			ELI24470
		2448	*			ELI24480
2270	2400	2449	\$CHKSUM	LIS R0,0	PUNCH M17 TAPE WITH CHECKSUM	ELI24490
2272	9510	2450		EPSR R1,R0	SELECT REG. SET 0	ELI24500
		2451	*			ELI24510
2274	C810 0A00	2452		LDAI R1,ORIGIN1	START	ELI24520
2278	2421	2453		LIS R2,1	INCREMENT	ELI24530
227A	C830 201A	2454		LDAI R3,LNZB	FINAL	ELI24540
227E	2440	2455		LIS R4,0	CHECKSUM BYTE	ELI24550
2280	D351 0000	2456	\$GEN	LB R5,0(R1)		ELI24560
2284	0745	2457		XAR R4,R5		ELI24570
2286	C110 2280	2458		BXLE R1,\$GEN		ELI24580
228A	D240 0099	2459		STB R4,MN+3	CHECKSUM BYTE TO BOOT LOADER	ELI24590
		2460	*			ELI24600
228E	C810 0080	2461	\$TAPE	LHI R1,X'0080'		ELI24610
2292	9E21	2462		OCR R2,R1	DISPLAY : NORMAL MODE	ELI24620
2294	9444	2463		EXBR R4,R4		ELI24630
2296	9824	2464		WHR R2,R4	CHECKSUM BYTE TO D1	ELI24640
2298	9411	2465		EXBR R1,R1		ELI24650
229A	9501	2466		EPSR R0,R1	HALT PROCESSOR.	ELI24660
229C	D360 007A	2468	\$PUNCH	LB R6,X'7A'	GET BOUTDV (PUNCH) ADDRESS.	ELI24680
22A0	DE60 007B	2469		OC R6,X'7B'	START TAPE PUNCH	ELI24690
22A4	9D60	2470		SSR R6,R0		ELI24700
22A6	2081	2471		BTBS 8,1		ELI24710
22A8	41F0 22EA	2472		BAL R15,\$TAPL	PUNCH LEADER	ELI24720
22AC	9411	2473		EXBR R1,R1	(R1) = X'0080'	ELI24730
22AE	C830 00CF	2474		LHI R3,X'CF'		ELI24740
22B2	DA61 0000	2475	\$PNCH1	WD R6,0(R1)	PUNCH BOOT LOADER	ELI24750
22B6	9D60	2476		SSR R6,R0		ELI24760
22B8	2081	2477		BTBS 8,1		ELI24770
22BA	C110 22B2	2478		BXLE R1,\$PNCH1		ELI24780
22BE	41F0 22F0	2479		BAL R15,\$TAPL1	PUNCH ONE-FOLD GAP.	ELI24790
		2480	*			ELI24800
22C2	D340 0099	2481		LB R4,MN+3	GET CHECKSUM BYTE	ELI24810
22C6	C810 0A00	2482		LDAI R1,ORIGIN1	(NORMALLY X'A00')	ELI24820
22CA	C830 201A	2483		LDAI R3,LNZB		ELI24830
22CE	D351 0000	2484	\$PNCH2	LB R5,0(R1)	PUNCH PROGRAM	ELI24840
22D2	0745	2485		XAR R4,R5		ELI24850
22D4	9A65	2486		WDR R6,R5		ELI24860
22D6	9401	2487		EXBR R0,R1		ELI24870
22D8	9820	2488		WHR R2,R0	DATA ADDRESS TO DISPLAY.	ELI24880
22DA	9D60	2489		SSR R6,R0		ELI24890
22DC	2081	2490		BTBS 8,1		ELI24900
22DE	C110 22CE	2491		BXLE R1,\$PNCH2		ELI24910
22E2	41F0 22EA	2492		BAL R15,\$TAPL	PUNCH TRAILER.	ELI24920
22E6	4300 228E	2493		B \$TAPE	DISPLAY CHECKSUM, HALT PROCESSOR.	ELI24930

CHKSUM

22EA	C800	0100	2495	\$TAPL	LHI	R0,56	TO PUNCH BLANK LEADER	ELI24950
22EE	2303		2496		BS	\$TAPLP		ELI24960
22F0	C800	00B0	2497	\$TAPL1	LHI	R0,128	TO PUNCH 1-FOLD GAP	ELI24970
22F4	2701		2498	\$TAPLP	SIS	R0,1		ELI24980
22F6	032F		2499		BNPR	R15	RETURN	ELI24990
22F8	2430		2500		LIS	R3,0		ELI25000
22FA	9A63		2501		WDR	R6,R3	PUNCH BLANK FRAME	ELI25010
22FC	9D68		2502		SSP	R6,R8		ELI25020
22FE	2081		2503		BTBS	8,1		ELI25030
2300	2206		2504		BS	\$TAPLP	CONTINUE.	ELI25040
			2505	*				ELI25050
2302			2506		END			ELI25060

CHKSUM

CLIFADR	0000	0A14	132*															
CLIFRD	0000	1730	1446*															
CLRI	0000	216	1634	1782	1800	2105	2415*	2416										
CMADR	0000	2010	1650	1643	1706	1724	1775	1779	1799	1909	1867	1884	1898	1971	1986			
			2004	2102	2305	2410*												
CMMSG	0000	1808	1550*	2311														
COMM	0000	1692	1349	1360	1364*	1383												
COMM1	0000	169A	1366*	1414														
COMRET	0000	176C	684	691	1484*													
COM2ND	0000	1738	196	205	206	1451*	1452											
COMADR	0000	172A	202	1015	1036	1101	1113	1178	1255	1441*								
COMENRD	0000	1739	1117	1452*														
COMRD	0000	172C	203	204	216	1012	1102	1443*	1444									
CONRQ2S	0000	1744	190	208	209	1107	1116	1459*										
CONTIN	0000	1828	576	1521*														
CONWRT	0000	1720	1127	1444*														
COUNT	0000	175E	538	551	553	1477*												
CRLF	0000	1274	223	229	305	347	373	465	486	610	950*	1024	2313					
CRT2ND	0000	173A	1453*															
CRTRD	0000	172E	1445*															
CRTRQ2S	0000	1745	1460*															
JECASC	0000	1188	875*															
DECHEX	0000	0CF2	412*															
DECHEX1	0000	0D24	418	430*														
DECLP1	0000	0CFC	416*	429														
DECLP2	0000	0D14	423*	426														
DECLP3	0000	0D1C	424	427*														
DECTAB	0000	176E	422	880	1486*													
DEFTSTS	0000	187E	438	440	1536*													
DELAY	0000	1F58	1640	1714	1720	1803	1827	1889	1896	1925	1997	2002	2038	2121	2143			
			2326*															
DEVAUR	0000	1858	1525*	1821	1921	2027	2046	2228	2374									
DEVINT	0000	192A	1289	1573*	1621	1697	1784	1819	1874	1877	1909	1914	1919	1976	1979			
			2015	2020	2025	2044	2093	2226	2344	2392								
DEVMSG	0000	1788	740	1498*	1499	1500	1501	2175										
DEVMSG2	0000	17C8	716	1502*	1503													
DEVSADR	0000	1910	1184	1283	1559*	1629	1642	1705	1723	1773	1808	1865	1897	1969	2003			
			2101	2128	2138	2373												
DISINC	0000	2 0E	2359	2408*														
DISNORM	0000	200F	2363	2409*														
DISPLAY	0000	0F46	628*															
DLY.0	0000	1F66	2330*	2332														
DLY.2	0000	1F6A	2327	2333*														
DSMK	0000	2017	1712	1780	1987	2416*												
ECHO	0000	131C	1010*	1234														
ECHO1	0000	1336	1011	1018*														
ECHRTN	0000	1338	1014	1019*														
ENMK	0000	2013	1635	1716	1887	2108	2111	2141	2412*									
ENRT	0000	2012	1991	2106	2139	2411*	2412											
ENST	0000	2015	1631	1710	1718	1801	1885	1989	2414*									
EOTMSG	0000	17FE	579	1510*														
ERR	0000	0F6C	645*	1368														
ERR1	0000	102A	647	650	661	666	672	677	705*	2188	2200	2212	2224	2239	2257			

CHKSUM

ERRALL	0000	0F0E	2275	2285	2297														
ERRCOM	0000	0FF6	675*	1318	1328														
ERRCOM1	0000	1014	646	655	660	665	671	676	684*										
ERRCOM2	0000	0F78	688	694*															
ERRD	0000	0F8A	648*	650	663	668	674	680											
ERRD1	0000	1434	654*																
ERRDEV	0000	1720	657	712*	2191	2203	2215	2230	2242	2260	2288	2299							
ERRDS	0000	0FB2	713	733	1429*	2190	2202	2214	2229	2241	2259	2287	2298						
ERRDS1	0000	1464	664*																
ERRL	0000	0FC6	667	678	732*														
ERRL1	0000	108A	669*																
ERRLVL	0000	17FB	673	746*															
ERRMSG	0000	1792	1327	1509*															
ERRNO	0000	179A	705	1493*	1494	1495													
ERRPL1	0000	14A2	500	1317	1325	1348	1357	1376	1389	1495*	2187	2199	2211	2223	2238				
ERRS	0000	0F9E	2256	227*	2284	2295													
ERRS1	0000	104C	679	756*	1370														
ERRSAVE	0000	20F0	659*																
ERRSTA	0000	1722	662	722*															
ETESTNO	0000	1798	645	552	654	659	664	669	675	2166	2177	2437*							
EXPDEV	0000	2006	723	737	1431*														
FIRST	0000	1710	499	532	1494*														
FOUND1	0000	0DAC	1825	2030	2047	2243	2261	2404*											
FOUND2	0000	00B0	315	322	326	1418*													
FP	0000	166A	472	482*															
GETCHR	0000	1310	478	483*															
HALT9	0000	0EFC	1162	1341*															
HDLR0	0000	192A	244	360	770	779	1004*												
HDLR1	0000	192C	594	600*	607	699													
HDLR2	0000	192E	1574*																
HDLR3	0000	1930	1575*																
HDLR4	0000	1932	1576*																
HDLR5	0000	1934	1577*																
HDLR6	0000	1736	1578*																
HDLR7	0000	1738	1579*																
HEXASC	0000	1190	1580*																
HEXASC1	0000	119A	1581*																
HEXTAB	0000	1778	530	715	725	735	739	749	759	762	857*	2170	2174	2246	2264				
II	0000	16B0	2310	861*	868														
II32	0000	16C6	861*	868	824	864	889	1326	1487*										
IMPTOP	0000	0000R	332	772	824	864	889	1326	1487*										
INCR	0000	1725	1147	1375*															
INIT	0000	1FBA	1379	1383*															
INIT.1	0000	1FC8	629	1434*															
INIT.2	0000	1FE4	487	2371*															
INIT.3	0000	1FF8	2377*	2381															
INITRET	0000	00C4	2387*	2390															
INTCNT	0000	200A	2395*	2398															
INTDEV	0000	1720	492*																
			1638	1651	1653	1708	1733	1735	1798	1871	1929	1931	1996	2033	2035				
			2091	2150	2152	2406*													
			1225	1262	1279	1428*	1429	1822	1824	1922	2028	2127	2168	2189	2201				

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		2261	2305	2373	2377	2379	2383	2384	2389	2392	2394	2397	2450	2452
		2456	2458	2461	2462	2465	2465	2466	2473	2473	2475	2478	2482	2484
		2487	2491											
R10	0000 000A	70*	1260	1260	1261	1280	1298	1299	1300	1326	1326	1327	1387	1387
		1399	1400											
R11	0000 000B	71*												
R12	0000 000C	72*	238	255	264	275	385	388	398	402	406	421	449	481
		776												
R13	0000 000D	73*												
R14	0000 000E	74*	294	304	340	386	389	391	447	452	456	787	790	796
		1341	1361	1564	1380	1390	1395	1398	1400	1401	1792	1881	1983	2133
		2188	2191	2200	2203	2212	2215	2224	2230	2239	2244	2257	2260	2275
		2285	2288	2297	2299	2352	2354							
R15	0000 000F	76*	217	225	244	391	397	401	405	466	467	468	670	770
		771	772	774	778	779	825	852	956	999	1000	1061	1063	1342
		1362	1365	1381	1391	1396	1402	1650	1732	1794	1805	1826	1882	1890
		1928	1984	1998	2031	2033	2034	2035	2036	2048	2115	2123	2158	2182
		2184	2185	2194	2196	2197	2206	2208	2209	2218	2220	2221	2233	2235
		2236	2246	2248	2251	2253	2254	2268	2266	2269	2271	2272	2279	2281
		2282	2291	2292	2293	2310	2312	2313	2317	2318	2364	2472	2479	2492
		2499												
R2	0000 0002	62*	83	103	109	158	159	161	162	168	170	180	185	187
		189	190	194	195	196	203	204	205	206	214	214	215	231
		232	300	301	306	307	309	310	316	319	341	349	351	353
		358	375	376	415	417	427	511	514	515	522	524	525	526
		527	529	531	532	541	542	543	602	646	650	651	655	660
		665	671	676	684	689	690	691	692	714	724	734	738	748
		758	761	805	817	818	820	822	826	865	866	890	891	904
		1040	1041	1043	1048	1052	1055	1134	1184	1186	1186	1186	1187	1198
		1199	1201	1202	1208	1209	1214	1220	1221	1223	1225	1233	1254	1255
		1262	1276	1277	1279	1285	1347	1348	1356	1357	1359	1367	1375	1376
		1378	1388	1389	1393	1410	1623	1624	1625	1626	1629	1630	1631	1633
		1634	1635	1636	1643	1644	1645	1699	1700	1701	1702	1705	1706	1710
		1711	1712	1713	1716	1717	1718	1719	1724	1725	1726	1773	1775	1779
		1780	1781	1782	1786	1787	1788	1789	1799	1800	1801	1802	1809	1810
		1811	1865	1867	1884	1885	1886	1887	1888	1898	1899	1900	1969	1971
		1986	1987	1988	1989	1990	1991	1993	2004	2005	2006	2095	2096	2097
		2098	2101	2102	2105	2106	2107	2108	2110	2111	2112	2138	2139	2140
		2141	2142	2169	2173	2245	2263	2298	2309	2376	2380	2384	2387	2388
		2389	2394	2395	2396	2397	2453	2462	2464	2488				
R3	0000 0003	63*	88	89	90	181	182	185	201	202	211	211	215	216
		217	272	276	280	282	303	316	341	350	354	416	417	419
		420	423	453	457	617	620	794	797	806	806	835	837	845
		849	858	859	860	862	867	876	877	878	880	892	917	919
		1135	114	1144	1147	1148	1151	1152	1162	1163	1176	1177	1183	1187
		1193	1199	1202	1203	1209	1214	1215	1221	1226	1254	1263	2454	2474
		2483	2500	2501										
R4	0000 0004	64*	92	93	94	96	104	106	208	209	210	212	212	234
		236	245	247	248	250	252	259	261	265	292	297	307	312
		317	318	321	324	329	331	332	332	334	335	336	337	351
		356	369	371	384	387	436	458	618	772	780	782	786	788
		821	822	823	824	824	840	843	851	861	862	863	864	864
		865	879	884	885	887	889	889	890	913	914	915	916	927

