Xerox Removable Disk Storage System

Models 7270/7271

Reference Manual

90 30 57A

DISK PACK ORDER CODES

Hexadecimal Code	Function
01	Write
02	Read 2
03	Seek
04	Sense
05	Check-Write
09	Header Write
0A	Header Read
12	Read 1
13	Select Test Mode
23	Release
33	Restore Carriage

Xerox Corporation
701 South Aviation Boulevard
El Segundo, California 90245
213 679-4511

XEROX

Xerox Removable Disk Storage System

Models 7270/7271

Reference Manual

FIRST EDITION

90 30 57A

November 1973

Price: $1.50

Printed in U.S.A.

RELATED PUBLICATIONS

Title	Publication No.
Xerox Sigma 5 Computer/Reference Manual	90 09 59
Xerox Sigma 6 Computer/Reference Manual	90 17 13
Xerox Sigma 7 Computer/Reference Manual	90 09 50
Xerox Sigma 8 Computer/Reference Manual	90 17 49
Xerox Sigma 9 Computer/Reference Manual	90 17 33
Xerox Stand-Alone Systems/OPS Reference Manual	90 10 53
Xerox Symbol/LN,OPS Reference Manual	90 17 90
Xerox Meta-Symbol/LN,OPS Reference Manual	90 09 52
Xerox Macro-Symbol/LN,OPS Reference Manual	90 15 78

Manual Content Codes: BP - batch processing, LN - language, OPS - operations, RP - remote processing, RT - real-time, SM - system management, TS - time-sharing, UT - utilities.

CONTENTS

FIGURES

TABLES

1. GENERAL DESCRIPTION

INTRODUCTION

Xerox Models 7270/7271 Removable Disk Storage System provides high-performance random access storage. It is a moving arm (head), removable-storage medium, 2400 rpm disk system, using a standard 11-high (20 recording surfaces) disk pack. Its cylinder density is 200 cylinders per inch and it has a recording track width appropriate for 406 (400 primary and six alternate cylinders). A cylinder consists of 20 tracks, one on each recording surface, at any one radial position in the pack.

The 400 cylinders with 120 sectors of 1024 bytes each result in a total capacity (per pack) of 49.15 million bytes. Model 7270 consists of a controller and two 49-million byte disk drives. The system may be expanded by adding up to six Model 7271 disk drives, each providing 49 million bytes of storage, for a total on-line capacity of 393 million bytes per controller. Optional dual access to the system is enabled by adding controller model number 1040 and disk drive model number 1041. The removable-storage medium is Model 7274 disk pack. Model 1042 Extended Width Interface Feature (for transferring data in 4-byte parallel form) is also available.

Hardware is provided to maximize detection of data errors while reading. Also, the device can be exercised for maintenance purposes, off-line, without using the controller.

To use this manual effectively, the reader should be familiar with the Xerox Computer Reference Manual applicable to his installation, particularly the discussions of input/output instructions and operations.

Particular features of the system include the following:

- Electromagnetic head positioner – provides an average 35 milliseconds cylinder access time.
- Electro Magnetic tracking – optimizes head positioning precision.
- Dual access (optional) – allows simultaneous read/read, read/write, or write/write on any two spindles in a set of eight.
- Independent Seek operations – allows overlapping of Seek operations to minimize access times on multiple spindle systems.

SYSTEM COMPONETS

A system consists of some combinations of the following components:

Model No.	Component
7270	Disk controller with two 49 million-byte disk drive units.
1040	Additional controller allowing access to disk drives, if dual access feature 1041 is installed (optional).
1041	Dual access feature – one required for each disk drive (optional).
7271	Additional 49-million-byte disk drive units – maximum of eight per controller (optional).
1042	Extended Width Interface.
7274	Removable disk pack.

Table 1 describes the characteristics of the system.

Table 1. Characteristics

Operating Characteristics	
Recording format	1024 bytes/sector 6 sectors/track 400 tracks/surface[†] 20 recording surfaces
Recording method	Track serial
Mode	Double frequency NRZ
Recording frequency	2.50 Mhz
Rotational speed	2400 rpm
Model 7271 spindle capacity (sectored)	49, 152, 000 bytes

[†]There are additional six spare tracks per surface to be used only if the recording surface is flawed.

Table 1. Characteristics (cont.)

Operating Characteristics (cont.)	
Model 7270 (two disk drives) On-line capacity (sectored)	
Minimum	98,304,000 bytes
Maximum (six additional units per controller)	393,216,000 bytes
Nominal access (positioning) time	
Seek	35 msec average (10–70 msec)
Rotational latency	12.5 msec average (0–25 msec)
Transfer rates	
Instantaneous (per sector)	312,500 bytes/second
Average (multiple sectors)	250,800 bytes/second
Physical Characteristics	
Model 1040 Controller (standard Sigma cabinet)	
Height	63.5 in.
Width	31.5 in.
Depth	35.5 in.
Weight	500 lbs.
Model 7271 Disk Drive	
Height	38 in.
Width	27.5 in.
Depth	37.5 in.
Weight	670 lbs.

Table 1. Characteristics (cont.)

Environmental Characteristics	
Power requirements	208 vac, 60 Hz, three-phase
Operating temperature	60° to 90°F
Operating humidity (relative)	25% to 80%
Thermal dissipation	
Model 1040	3435 btu/hr
Model 7271	2830 btu/hr
Cable lengths	
IOP to controller	40 ft maximum
Controller to disk drives	100 ft maximum
Operating and Physical Characteristics of Removable Storage Medium	
Model 7274 disk pack	
Disks/pack	11
Usable recording surfaces/pack	20
Disk surface coating	Magnetic oxide
Disk surface diameter	14 in.
Disk pack canister diameter	15 in.
Disk pack canister	
Height	5.75 in.
Weight	14.5 lb.

2. FUNCTIONAL DESCRIPTION

DATA PRESENTATION

Data is transferred from the I/O system to the controller on a one-, two-, or four-byte interface (depending on options installed) and is transferred between the controller and disk pack unit serially, bit by bit.

SYSTEM STATES

The initial state of the device depends on its power status. Complete absence of power removes the device from the controlling system (i.e., no address recognition). If power is applied to the controller, address recognition occurs when an I/O instruction is issued.

OPERATIONAL STATES

The disk pack system reports the "operational" state if the I/O address is recognized and the device is "operational" as defined below:

1. Disk speed is within operating limits.
2. Temperature is within operating limits.
3. All interlocks are closed.
4. All power is normal in the controller and disk storage units.
5. No "fault" condition exists within a disk storage unit.

When "operational", an SIO instruction will be accepted if

1. The addressed controller/device is "ready".
2. The device is available, that is, not reserved by another controller.
3. No channel interrupts are pending.

The condition of the disk pack system at any given time is returned to the central processor in response to the I/O instructions, SIO, HIO, and TIO. Other I/O instructions, TDV and AIO, obtain other specific indications of device status (see "Status Response").

UNAVAILABLE CONDITION

In the "unavailable" condition, the device is reserved by another controller; therefore, it cannot accept an SIO instruction.

READY CONDITION

In the "ready" condition, the controller can accept an SIO instruction, provided that the addressed device is available and no channel interrupt is pending. To be "ready" the device must be "operational" and execution of an order must not be in progress or pending.

BUSY CONDITION

In the "busy" condition, the controller has accepted an SIO instruction. It will not accept a new order until the current order (or orders) is completed.

MODE

The disk pack system is always in the "automatic" mode, as long as it is in the "operational" state.

TRANSITIONS BETWEEN STATES

Table 2 summarizes the state transitions and conditions required to cause them.

Table 2. State Transitions

Present State	Next State	Conditions
Ready Automatic	Busy Automatic	Acceptance of SIO instruction.
Busy Automatic	Ready Automatic	HIO instruction received, I/O reset signal received, or operation associated with last order completed.

DATA TRANSFER

An operation is initiated from the controlling system by an SIO instruction if the following conditions are satisfied:

1. I/O address recognition exists.
2. Device and controller are in "ready" condition (and no channel interrupt is pending).
3. Device is available.

If these conditions are satisfied, the controller enters the "busy" condition. The controller initiates the transfer of data to or from the disk pack unit, as specified by the order, until the required number of bytes have been transferred. The operation then terminates, and the device returns to the "ready automatic" state.

3. PROGRAM INTERFACE

DISK PACK ORGANIZATION

Each disk pack (see Figure 1) contains 11 disks with 20 usable recording surfaces. (The outside surfaces of the top and bottom disks are not usable.) Each surface has its own movable read/write head but the 20 heads are all aligned in the same vertical plane and move as a unit through the stack, though only one head is reading or writing at any one time. Head numbers range from 0 through 19. Each recording surface is logically divided into six sectors numbered from 0 through 5 and into 406 concentric rings or tracks on which data is recorded. The tracks begin at the periphery of a disk and progress inward toward the center. Each set of 20 tracks aligned vertically in the stack is referred to as a cylinder. Thus, there are 406 cylinders in a disk pack, numbered from 000 through 405. Cylinder 000, for example, consists of track 000 on surface 0, track 000 on surface 1, and so on through track 000 on surface 19.

The addressing is summarized, as follows:

Field	Range
Sector	$0\text{-}5_{10}$
Head	$0\text{-}19_{10}$
Cylinder	$0\text{-}405_{10}$

DATA ORGANIZATION

Data is stored in groups of 1024 bytes. Each data group has a unique address composed of its device, cylinder, head, and sector number. Each data group is preceded by a header containing the cylinder, head, and sector number for the group. The header is used for address identification and verification.

DATA ACCESS

To select a disk pack unit, the I/O instruction must contain an address specifying both the device controller and the device. Since this is a multiunit device controller, its addresses range from 8 through F. The device addresses range from 0 through 7.

To address a particular data group for reading or writing, a Seek order must be given to the device to select the desired cylinder (move the heads into place), head, and sector. While reading or writing the sector (in a Read, Write, Check-Write or Header Read/Write operation), the controller automatically increments the sector number to the next one in sequence. Upon completing the last sector of a track, the controller sets the sector number to zero and increments the head number to the next one in sequence. Thus a single order may cause a number of sequential sectors and then tracks of a cylinder to be read or written. On reaching the end of a cylinder, a new Seek order must be issued to the controller to select the next cylinder, since it does not automatically increment to the next one.

ERROR DETECTION

A two-byte check character is written at the end of each header and at the end of each sector data field. The check characters are computed and inserted by the controller. When reading a header, the check characters are again computed and compared with those read. Any difference causes an "unusual end" termination. Upon reading data, the check characters are also recomputed and compared with those read. In this case, if there is a difference, the controller signals transmission error but not "unusual end".

HEADERS

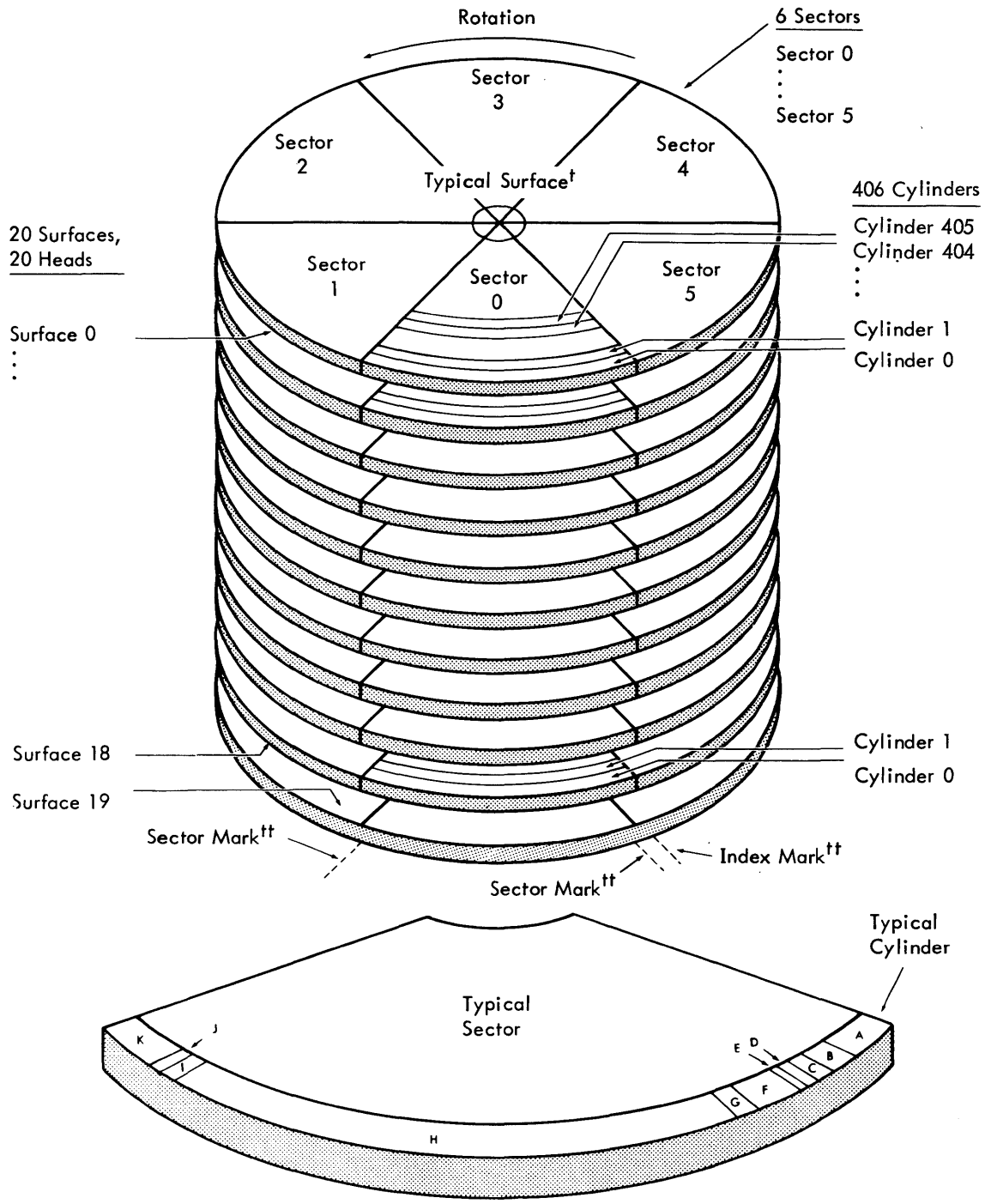
Prior to writing data on the disk, headers must be written identifying the addresses of all data groups (sectors). The headers are used for locating the desired data group and for address verification when successive data groups are written or read. Failure to acquire a given address within one revolution results in a verification error. When the program detects a defective track, it should write flaw marks in all sector headers of the track. A defective track is one in which an error has been detected on each of three successive "write and then read" operations. An alternate head and cylinder number is then obtained from the "flawed" header. The program will normally use the alternate head and cylinder number as the new Seek address.

DEVICE ORDERS

After completing an SIO instruction, or during command chaining, the controller makes a 1-byte service call to the IOP to obtain the order for the next operation. In all data transmission operations, the data address is incremented to the next sector even if it is a nonexistent address, e.g., head 20.

The following list shows order decoding. The reactions to these orders are described in subsequent paragraphs.

Order	Binary Representation Bit Positions 0 1 2 3 4 5 6 7	Hexadecimal Code
Seek	M 0 0 0 0 0 1 1	X'03'
Sense	0 0 0 0 0 1 0 0	X'04'
Write	0 0 0 0 0 0 0 1	X'01'
Header Write	0 0 0 0 1 0 0 1	X'09'
Check-Write	0 0 0 0 0 1 0 1	X'05'
Read 1	0 0 0 1 0 0 1 0	X'12'
Read 2	0 0 0 0 0 0 1 0	X'02'
Header Read	0 0 0 0 1 0 1 0	X'0A'



A Gap	512 Bits	G Data Preamble	136 Bits
B Header Preamble	136 Bits	H Data	8192 Bits
C Header Data	64 Bits	I Data Parity	16 Bits
D Header Parity	16 Bits	J Data Postamble	8 Bits
E Header Postamble	8 Bits	K Post Data Gap	680 Bits
F Post Header Gap	512 Bits		

[t]Data is not recorded on top or bottom surface.

[tt]Sector marks and index mark are on index disk (not shown).

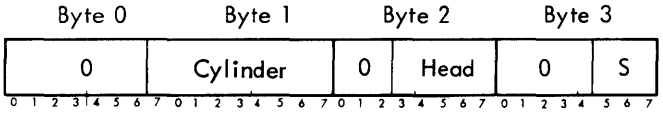
Figure 1. Disk Pack Organization

Order	Binary Representation Bit Positions 0 1 2 3 4 5 6 7	Hexadecimal Code
Restore Carriage	0 0 1 1 0 0 1 1	X'33'
Release	0 0 1 0 0 0 1 1	X'23'
Select Test Mode	0 0 0 1 0 0 1 1	X'13'
Invalid order	0 0 0 0 0 0 0 0	--

where M signifies Modifier bit (explained under "Seek" order).

SEEK (X'03')

The Seek order alerts the controller to request a 4-byte address from the IOP, as indicated below. The controller then directs any subsequent data transmission operation to begin at this address.



where S signifies sector number.

A byte count of less than four causes the controller to signal incorrect length without completing the Seek operation. If the byte count is greater than four, the Seek operation is performed using the first four bytes transferred, but incorrect length will still be signaled. An "unusual end" termination will occur in either case.

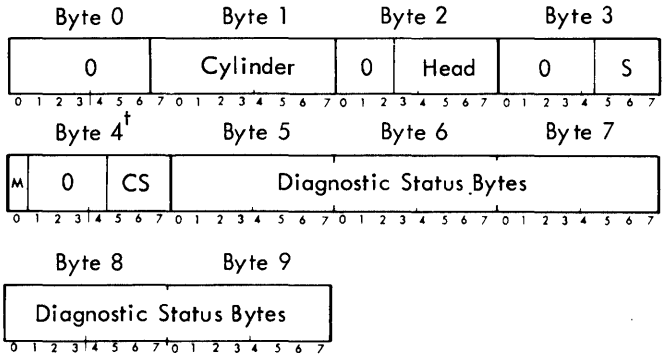
An order Modifier bit (bit 0), when set, causes a device interrupt to be initiated when the positioning system indicates that positioning is complete or a Seek timeout error (i.e., exceeding maximum time allowed for arm movement) has occurred. The interrupt call is always made at the beginning of the sector prior to the one specified by the Seek operation. However, if the controller is "busy" with another device when the device signal is received, the interrupt call to the IOP will be postponed until the controller is "not busy". If the interrupt call is not serviced before the beginning of the next sector, it will be postponed. Seek orders may be issued successively to several devices and each device will interrupt as it completes its operation.

A device interrupt may be cleared by an AIO or HIO instruction, or by an I/O Reset (from the computer control panel). The AIO or HIO clears only a single device interrupt, whereas the I/O Reset simultaneously clears all pending device interrupts.

If a Seek operation indicates a nonexistent cylinder(≥406), head(≥20), or sector(≥6), the controller will proceed to an "unusual end" termination without signaling the device to seek. The Sector Unavailable status bit for TDV will be set (see Table 6). An "unusual end" termination will also occur if the arm is currently in motion when the Seek order is received for the same device. (See Restore Carriage order.)

SENSE (X'04')

The Sense order causes the controller to transmit up to 10 bytes of information to the IOP, as follows:



where

S signifies sector number.

M signifies Modifier bit.

CS signifies current sector number.

Note: Bytes 5-9 are used for diagnostic purposes. See Table 3 for the function of the diagnostic status bits in bytes 8 and 9.

Table 3. Sense Status Bytes 8 and 9

Byte No.	Bit No.	Function
8	0	Data parity error
	1	Check-Write error
	2	Sector verification error
	3	Head verification error
	4	Cylinder verification error
	5	Sector address not zero at start of header write operation
	6	Difference select sent to device
	7	Sector select sent to device
9	0	Control select sent to device
	1	Head select sent to device
	2	Cylinder select sent to device
	3	Seek forward set
	4	Read gate sent to device
	5	Write and erase gate sent to device
	6	Read cylinder select sent to device
	7	Most significant bit of difference

Note: Bytes 8 and 9 are used by diagnostic programs.

†Three bits of byte 4 indicate current sector number, but if the most significant bit (M) is a 1, the arm was in motion at time of sense and current sector number is meaningless. If the head number is nonexistent, (M) will also be a 1.

If the byte count is ten or less, only the requested number of bytes, beginning with byte 0, are transmitted to the IOP. If the byte count is greater than ten, only ten bytes are transmitted. Incorrect length is not signaled, in any case, for the Sense order.

When reading the header during a Sense operation, detection of a flaw mark, verification error, or header parity error causes the pertinent TDV status bits to be set. It also causes "unusual end" termination after the requested bytes have been transferred to the IOP.

During the Sense operation, the cylinder number is obtained from a device register. The head and sector numbers are obtained from controller registers. The current sector number is read from the first header to pass the read head when the Sense order is executed.

The five diagnostic status bytes (for diagnostic programs) contain the two parity bytes read from the current header, a test output from the "differencing logic" normally used during a Seek operation to compute the number of cylinders that the arm must move, and two bytes of status that are shown in Table 3.

WRITE (X'01')

The Write order causes the controller to begin writing bytes at the current address. The current address is the address that was stored in the device and controller registers by a previous Seek order (head and sector portions being subject to updating by intervening "reads" or "writes").

Prior to signaling the device to write and sending data to it, the controller reads headers until the one with the requested sector number is found. If a flaw mark is detected in any header, or if the header of the requested sector does not have correct parity, or if the cylinder or head numbers do not compare with those stored in the registers, then the controller proceeds to an "unusual end" termination without writing. The pertinent TDV status bits are set to indicate flaw mark, incorrect header parity, or verification error.

Writing, once started, continues until any of the following occur:

1. Count done signal is received coincident with the end of a sector (i.e., after writing 1024 data bytes on the sector).

2. Count done signal is received before end of sector, remainder of the sector is written with zeros, and the check character is written. Incorrect length is indicated if the byte count is not a multiple of 1024.

3. The disk pack system becomes "not operational". Writing terminates with an "unusual end".

4. The controller head number increments past the end of the cylinder. Writing terminates with an "unusual end" and the Sector Unavailable status bit is set to 1.

5. A rate error (data overrun) is detected. Writing terminates with transmission error.

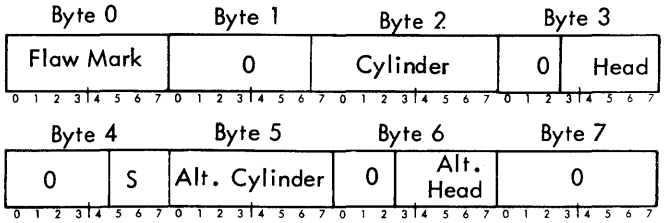
6. I/O Reset or HIO occurs. Writing terminates and the controller immediately proceeds to the "ready" condition without further communication with the IOP.

7. An IOP halt is indicated to the controller. The controller indicates "unusual end" and proceeds to the "ready" condition.

8. A flaw mark in a header, incorrect header parity, or verification error is encountered. The Write order terminates with an "unusual end". The current address is not incremented.

HEADER WRITE (X'09')

The Header Write order causes the controller to consider all subsequent data bytes as header information. Each header requires the following bytes:



After writing the bytes listed, the controller inserts two check bytes that are verified whenever the header is read. The Header Write order must always be accompanied by a byte count specifying a multiple of eight bytes. The controller always begins writing headers on sector zero. If the program specifies starting at other than sector zero, an "unusual end" results. A maximum of 120 headers (1 cylinder) or a minimum of six headers may be written with a single Header Write order.

When a track is found to be defective, Header Write is used to rewrite all six headers of the track, inserting a flaw mark at the beginning of each header. Upon subsequently detecting a flaw mark in any header, the controller assumes the entire track to be faulty and makes no attempt to read additional sectors (except for the Header Read order).

CHECK-WRITE (X'05')

The Check-Write order causes the controller to compare bytes read from the disk storage unit (current address) with bytes received from the IOP. Check-Write errors terminate the operation following the sector in which they occurred. Incorrect length is specified if the byte count is not a multiple of 1024.

The order requires that headers be read so that address acquisition and verification are performed. If a flaw mark is detected, or if the header does not have correct parity, or if the cylinder and head numbers do not compare with those stored in the registers, then an "unusual end" results.

Check-writing continues until any of the following occur:

1. Count done signal is received.
2. The disk pack system becomes "not operational". Check-writing terminates with an "unusual end".
3. The controller head number increments past the end of cylinder. Check-writing terminates with an "unusual end" and the Sector Unavailable status bit is set to 1.
4. A flaw mark, incorrect header parity, or verification error is encountered. The Check-Write order terminates with an "unusual end". The current address is not incremented.
5. A rate error (data overrun) is detected. Check-writing terminates with transmission error.
6. A noncomparison of data bytes or an error in the data check character occurs. Transmission error is indicated.
7. I/O Reset or HIO occurs. Check-writing terminates and the controller proceeds to the "ready" condition without further communication with the IOP.
8. IOP halt is indicated to the controller. The controller indicates "unusual end" and proceeds to the "ready" condition.

READ 1 (X'12')

The Read 1 order causes the controller to begin reading bytes from the file storage at the current address in the controller and transmits the bytes to the IOP. Sector data parity errors are reported following the sector in which they occurred. Such errors will be reported as transmission errors. Sector parity is determined from the check bytes written at the end of each sector. Incorrect length is specified if the byte count is not a multiple of 1024.

Prior to reading data from the sector, and sending it to the IOP, the controller reads headers until the one with the requested sector number is found. If a flaw mark is detected in any header, or if the addressed header does not have correct parity, or if cylinder or head numbers do not compare with those stored in registers, then an "unusual end" occurs without reading data. The pertinent TDV status bits are set to indicate a flaw mark, incorrect header parity, or verification error.

Reading continues until any of the following occur:

1. Count done signal is received coincident with the end of the sector and the check character has been read and compared.
2. Count done signal is received prior to the end of the sector. The remaining bytes are read by the controller (but not transmitted to the IOP) and the check character is read and compared.
3. The disk pack system becomes "not operational". Reading terminates with an "unusual end".
4. The controller head number increments past the end of cylinder. Reading terminates with an "unusual end" and the Sector Unavailable status bit is set to 1.
5. A rate error (data overrun) is detected. Reading terminates with transmission error.
6. A check character is encountered that does not successfully compare. Termination occurs at the end of the sector in error (for the Read 1 order described in this section) or is deferred until the count done signal, as described below for Read 2. Transmission error is indicated.
7. I/O Reset or HIO occurs. Reading terminates and the controller proceeds to "ready" immediately without further communication with the IOP.
8. IOP halt is indicated to the controller. The controller indicates "unusual end" and proceeds to "ready".
9. A flaw mark, incorrect header parity, or verification error is encountered. The Read order terminates with "unusual end". The current address is not incremented.

READ 2 (X'02')

The Read 2 order causes the controller to read and transmit bytes to the IOP from the file storage at the current Seek address in the controller. Sector data parity error is reported following the sector in which count done occurred. Otherwise, operation is as stated in the previous section.

HEADER READ (X'0A')

The Header Read order permits reading headers and transmitting them (excluding the parity check bytes) to the IOP. The byte count must specify that a multiple of eight bytes is to be read. A maximum of 120 headers (1 cylinder) may be read with a single Header Read order.

Detection of the flaw mark causes TDV status bit 1 to be set, but does not have any other effect. An error in the header parity check characters causes TDV status bit 7 to be set and reading to stop with an "unusual end". A verification error also causes reading to stop with an "unusual end". For either type of error, the current address is not incremented.

RESTORE CARRIAGE (X'33')

The Restore Carriage order causes the controller to issue a return-to-zero control signal to the device. Internal registers are cleared, the heads are returned to cylinder 0, and no data bytes are transferred for this order. "Channel end" occurs after order is sent to the device. (Carriage motion is not necessarily completed.)

This order must follow an HIO instruction, Seek timeout error, or Seek "unusual end".

RELEASE (X'23')

The Release order causes the controller to issue a release control signal to the addressed device, thereby releasing

that device for use by the other channel if the device is a dual-channel unit. Execution of this order has no effect if the device is a single-channel unit.

A device is reserved (i.e., it can be used only by the channels reserving it) whenever an SIO instruction is addressed to that device. Releasing the device from the reserved status may be accomplished either by an HIO, a Release order, or by an I/O Reset. The I/O Reset releases all devices on the controller, whereas the Release order or HIO releases only the addressed device. A device may only be released by the reserving controller.

No data bytes are transferred for the Release order.

SELECT TEST MODE (X'13')

The Select Test Mode order causes the controller to make a service call request for one byte of data. Three bits of that byte are used to select a test mode in which all subsequent operations will be performed. This order is used by diagnostic programs.

Bits 0 1 2 3 4 5 6 7	Function
0 0 0 0 0 0 0 0	Exit test mode
0 0 0 0 0 0 0 1	Enter test mode 1
0 0 0 0 0 0 1 0	Enter test mode 2
0 0 0 0 0 1 1 0	Enter test mode 2 – set test parity error

TEST MODE 1

Test mode 1 allows the controller buffer to be filled by a Write order and subsequently read back by a Read order. Most of the controller's data path is checked without using the devices.

TEST MODE 2

Test mode 2 allows the controller's data path and control logic to be tested without using the devices. This mode tests approximately 90 percent of the controller logic. All orders may be executed in this mode and device responses will be simulated internally. In a Read or Check-Write order, the data read from the device is simulated by an incrementing data pattern of 1024 bytes (the first byte containing the value 224).

TEST PARITY ERROR

While operating in test mode 2, this condition forces a data check byte error by changing the first data byte from 224 to 240.

KEY EVENTS

The key events that occur during an I/O operation are described in the following paragraphs. No chronological order of occurrence should be assumed from the order of presentation.

START INPUT/OUTPUT

An I/O operation begins with the execution of an SIO instruction by the controlling system. If I/O address recognition exists and the device is in the "ready" condition and available, the controlling system sets its "I/O address recognition" and "SIO accepted" indicators. The device advances from the "ready" to the "busy" condition. It then requests an order byte from the controlling system and proceeds with the operation defined by the order byte.

UNUSAL END CONDITIONS

After an order is received, the detection of any of the following conditions causes the device to return an "unusual end" indication to the controlling system:

1. Illegal Seek address.
2. Incrementing of storage address past end-of-cylinder number before count done signal received.
3. Out-of-range storage address when Read, Write, Check-Write, Header Read, or Header Write order received by controller.
4. Invalid order code.
5. Loss of read clock from device while reading headers or data.
6. Flaw mark while Read, Write, Check-Write, or Sense order is executed. (The current address is not incremented.)
7. Device leaving "operational" state while controller "busy".
8. IOP halt signaled during data transmission.
9. Incorrect header parity check or verification error while Read, Write, Check-Write, Header Read, or Sense order being executed. (The current address is not incremented.)
10. Receipt of Seek order when arm in motion.
11. Occurrence of Seek timeout error when Seek, Read, Write, Check-Write, Header Read, or Header Write order received.
12. Attempt to start Header Write operation at sector other than zero.
13. Incorrect length on Seek order.

CHANNEL END CONDITIONS

"Channel end" is reported to the controlling system when any of the following conditions are detected.

1. Transmission of 4-byte address to device during Seek order. Arm position (carriage motion) is not necessarily completed.

2. After writing of check bytes in last sector of a Write or Header Write order following count done signal.
3. After reading of check bytes in last sector of a Read, Header Read, or Check-Write order following count done signal.
4. Following the execution of Release or Select Test Mode order.
5. Following count done signal during Sense order.
6. After reading of check bytes during a Read 1 or Check-Write order with a transmission error.
7. Data overrun (rate error).
8. "Unusual end" condition.
9. Following issuance of Restore Carriage order to the device. Arm position (carriage motion) is not necessarily completed.

TRANSMISSION ERROR CONDITIONS

Transmission errors are detected and reported to the controlling system. Conditions causing this error are

1. Failure of the end of sector parity check during a Read or Check-Write operation.
2. Failure of a data byte comparison during a Check-Write operation.
3. Data overrun (rate error).

INCORRECT LENGTH CONDITIONS

Incorrect length errors are detected and reported to the controlling system. Conditions causing this error are

1. A byte count other than four was specified for a Seek order.
2. A byte count that is not a multiple of 1024 for Read, Write, and Check-Write orders.
3. A byte count that is not a multiple of eight (one header) for a Header Read or Header Write order.

STATUS RESPONSE

The device can return status information in response to computer-executed I/O instructions. Detailed explanations of the I/O instructions are in the Xerox computer reference manuals.

I/O INSTRUCTION STATUS BITS

Execution of an I/O instruction provides two bits of information pertaining to the general status of the addressed I/O device and its controller. Table 4 lists the I/O instructions, status bit settings, and the significance of each setting.

DEVICE STATUS BYTE

Eight bits of information are made available to the controlling system in the Device Status Byte in response to the execution of an I/O instruction, as shown in Tables 5, 6, and 7.

OPERATIONAL STATUS BYTE

In addition to the information contained in the Device Status Byte, the Operational Status Byte generated at the end of each I/O operation also provides indicators to the controlling system, as shown in Table 8.

PROGRAMMING CONSIDERATIONS

COMMAND CHAINING

The command chaining feature permits changing tracks or orders. Adequate time is provided to allow command chaining during the intersector gap to change the order from one type to another, or to another order of the same type, as long as no head motion is required. Seek times must be considered when selecting a new cylinder number. Chaining from a Read or Write to another Read or Write is not allowed if the end of the cylinder has been reached. At the end of a cylinder, a Seek or Restore Carriage order must be given before additional data may be transferred.

INTERRUPTS

Two types of interrupts are generated by the controller, those resulting from "channel end" (including "unusual end" or zero byte count) and those resulting from a Seek operation. The former has priority over the latter when both are present at the same time. An interrupt may occur for either a Seek timeout error or to indicate "on-sector" (Seek complete). An "on-sector" interrupt occurs at the beginning of the sector prior to the one specified by the Seek.

Interrupts are cleared by AIO or HIO instructions or by an I/O Reset (from the computer control panel). The AIO or HIO instruction clears only a single interrupt. The I/O Reset signal clears all interrupts. A device interrupt is also cleared if it is not serviced before the next sector is reached. It will be raised to the waiting state again, however, on the following revolution of the disk pack.

If a Seek order is given to a device and is subsequently followed by an HIO to that device (before the Seek operation is completed), then that device will not interrupt upon completing the Seek operation.

SEQUENCE OF ACTIVITY

Figures 2 through 14 illustrate the sequential relationship of the key events that occur during disk pack system operations.

Table 4. I/O Instruction Execution Response

Instruction	Status Bits†		Significance
	CC1 or O	CC2 or C	
SIO	0	0	I/O address recognized and SIO accepted.
	0	1	I/O address recognized, but SIO not accepted.
	1	0	Controller "busy" with device other than one addressed and unable to send status.
	1	1	I/O address not recognized.
HIO	0	0	I/O address recognized and device "not busy" when halt occurred.
	0	1	I/O address recognized and device "busy" when halt occurred.
	1	0	HIO not accepted. Controller "busy" with device other than one addressed and unable to send status.
	1	1	I/O address not recognized.
TIO	0	0	I/O address recognized and SIO can currently be accepted.
	0	1	I/O address recognized, but SIO can not currently be accepted.
	1	0	Controller "busy" with device other than one addressed and unable to send status.
	1	1	I/O address not recognized.
TDV	0	0	I/O address recognized.
	0	1	I/O address recognized and controller is switched to a test mode.
	1	0	Controller "busy" with device other than one addressed, or device "not operational" and unable to send status.
	1	1	Controller address not recognized.
AIO	0	0	Normal interrupt condition present (no "unusual end").
	0	1	Unusual end or transmission error condition present.
	1	0	Invalid code.
	1	1	No interrupt condition present.

†The symbols "CC1" and "CC2" refer to condition code indicators in Xerox 32-bit computers. The symbols "O" and "C" refer, respectively, to Overflow and Carry indicators in Xerox 16-bit computers.

Table 5. Status Response to SIO, TIO, and HIO

Bit Position	Function	State	Meaning
0	Interrupt Pending	1	Interrupt is pending (issued, but not yet acknowledged by an AIO instruction).
1,2	Device Condition		A combination of these two bits indicates the current device condition:
		0 0	Device "ready".
		0 1	Device "not operational".
		1 0	Device unavailable (reserved by other channel).
		1 1	Device "busy".
3	Mode-Automatic or Manual	1	Always in the "automatic" mode.

Table 5. Status Response to SIO, TIO, and HIO (cont.)

Bit Position	Function	State	Meaning
4	Unusual End	1	Previous controller operation terminated due to one of the conditions listed under "Unusual End Conditions".
5,6	Controller Condition		A combination of these two bits indicates the current controller condition:
		0 0	Controller "ready".
		0 1	Not applicable.
		1 0	Not applicable.
		1 1	Controller "busy".
7	Reserved	0	This bit is currently zero; however, it may be used in future enhancements.

Table 6. Status Response to TDV

Bit Position	Function	State	Meaning
0	Data Overrun[t]	1	Data overrun (rate error) has occurred during execution of the previous order.
1	Flaw Mark[t]	1	A flaw mark has been detected while reading header.
2	Sector Unavailable[t]	1	A nonexistent data address was used in the previous Seek order, or the data address was incremented to a nonexistent value and a Read, Write, Header Read, or Header Write was attempted.
3	Reserved	0	This bit is currently zero; however, it may be used in future enhancements.
4	Header Verification Error[t]	1	Header verification error (noncomparison) was detected.
5	On Cylinder[tt]	1	Positioning is completed (head is on cylinder).
6	Seek Timeout Error[tt]	1	Seek timeout error was detected.
7	Header Parity Error[t]	1	Header parity error was detected.

[t]Pertains to current or most recent controller operation.

[tt]Related specifically to that device addressed by TDV instruction.

Table 7. Status Response to AIO

Bit Position	Function	State	Meaning
0	Data Overrun	1	Data overrun (rate error) has occurred during execution of the previous order.
1-3	Reserved	0	These bits are currently zero; however, they may be used in future enhancements.
4	On-Sector Interrupt[t]	1	On-sector interrupt received from device.
5	On Cylinder	1	Positioning is completed (head is on cylinder).
6	Seek Timeout Error	1	Seek timeout error was detected.
7	Reserved	0	This bit is currently zero; however, it may be used in future enhancements.

[t]On-sector interrupt indicates completion of Seek operation or occurrence of Seek timeout error (bit position 6 is a 1); for Seek timeout errors, On Cylinder (bit position 5) is a 0. This bit is reset if an AIO instruction is not received before start of next sector.

Table 8. Operational Status Byte[t]

Function	State	Meaning
Transmission Data Error	1	One of the conditions listed under "Transmission Data Error Conditions" has occurred since the previous order was received.
Incorrect Length	1	One of the conditions listed under "Incorrect Length Conditions" has occurred since the previous order was received.
Chaining Modifier	0	Not used and always zero.
Channel End	1	A "channel end" is reported with every order-in to the IOP. The device has terminated its operation for any reason listed under "Channel End Conditions".
Unusual End	1	The device has terminated its operation for any reason listed under "Unusual End Conditions".

[t]For the bit positions of these functions in the Operational Status Byte, see the applicable Xerox Computer Reference Manual.

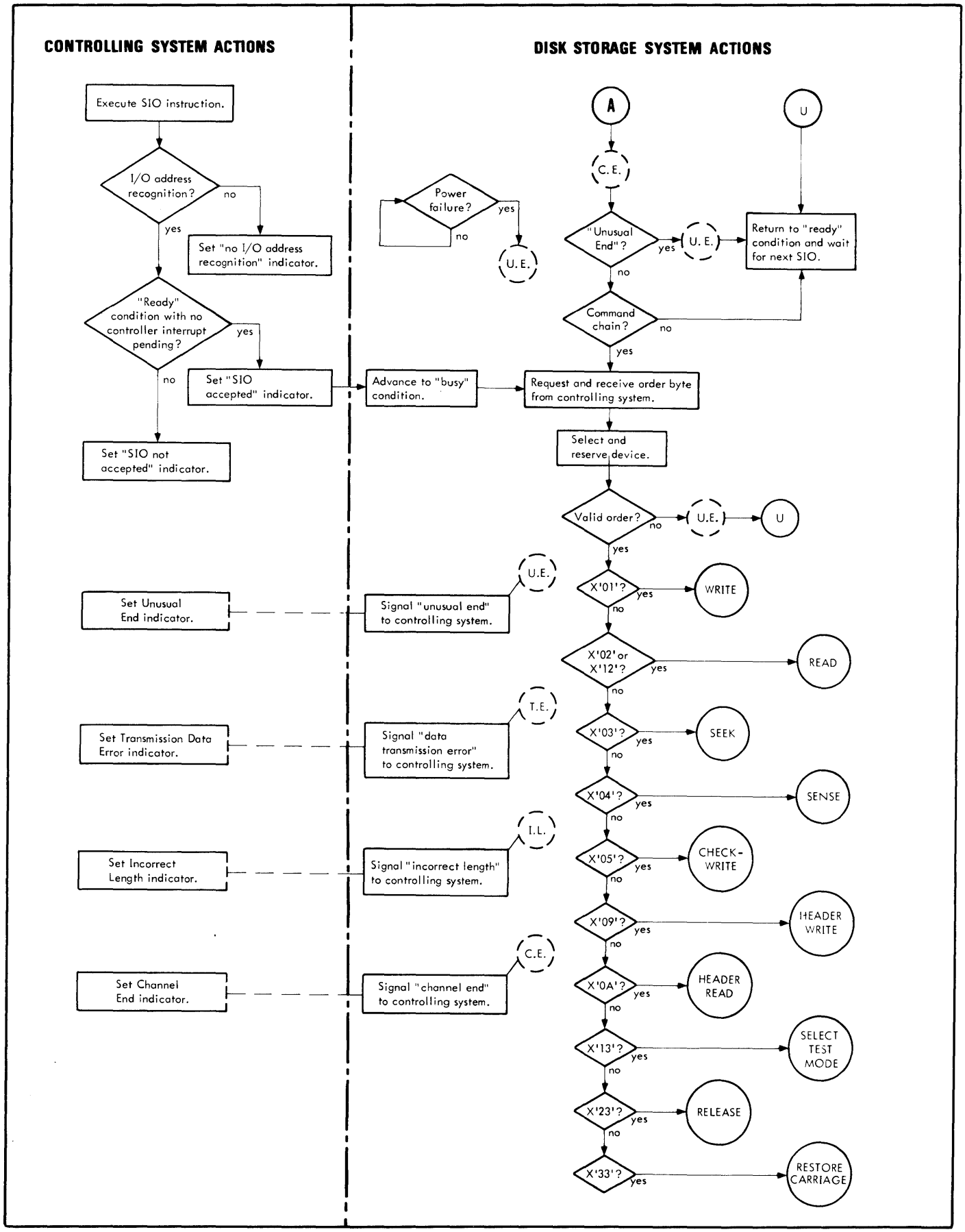


Figure 2. Controlling/Disk Storage System Actions

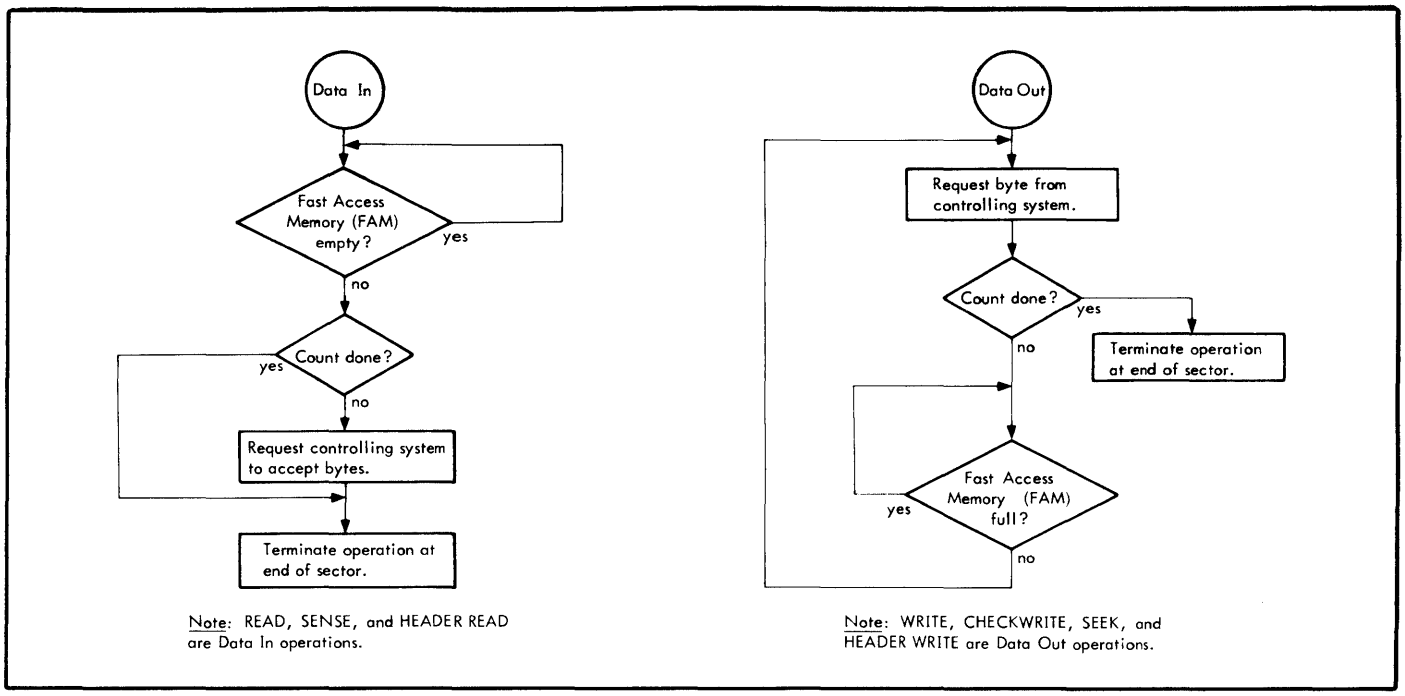


Figure 3. Data In/Data Out Operations

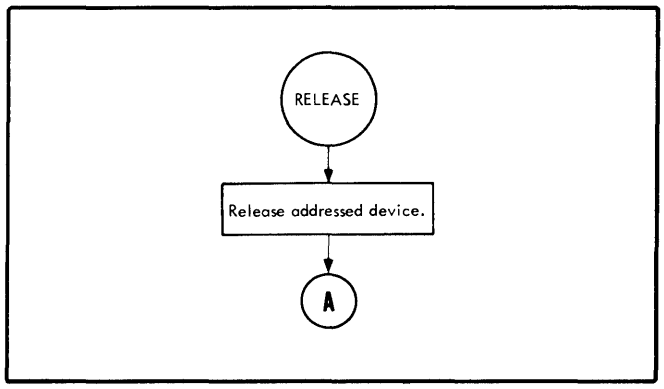


Figure 4. Release Order

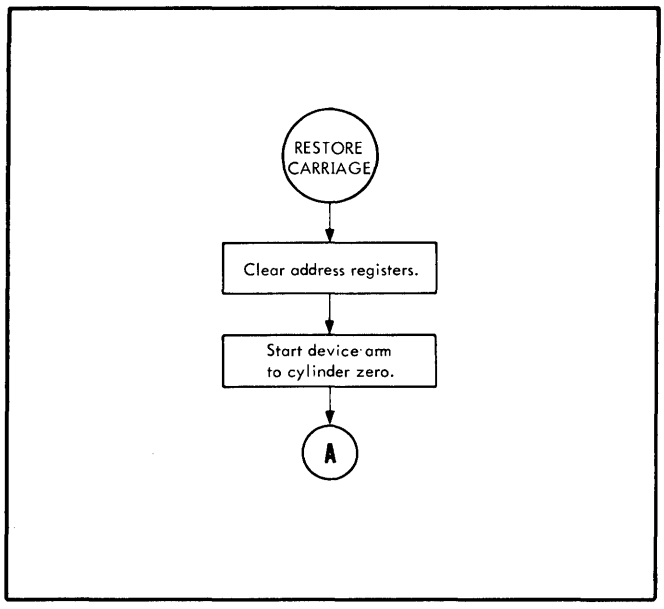


Figure 5. Restore Carriage Order

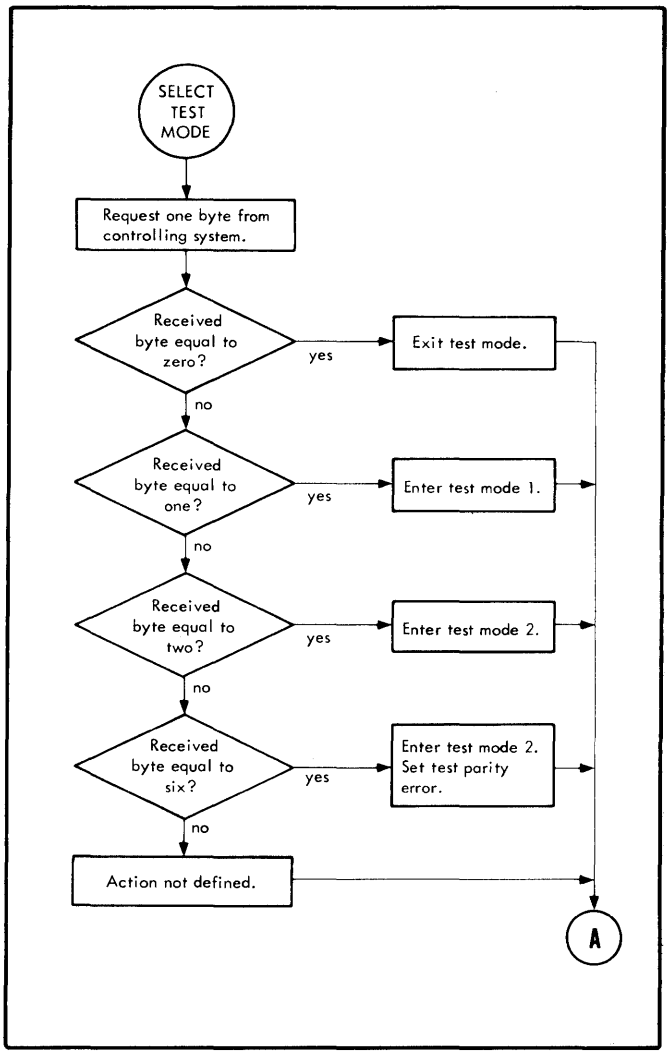


Figure 6. Select Test Mode Order

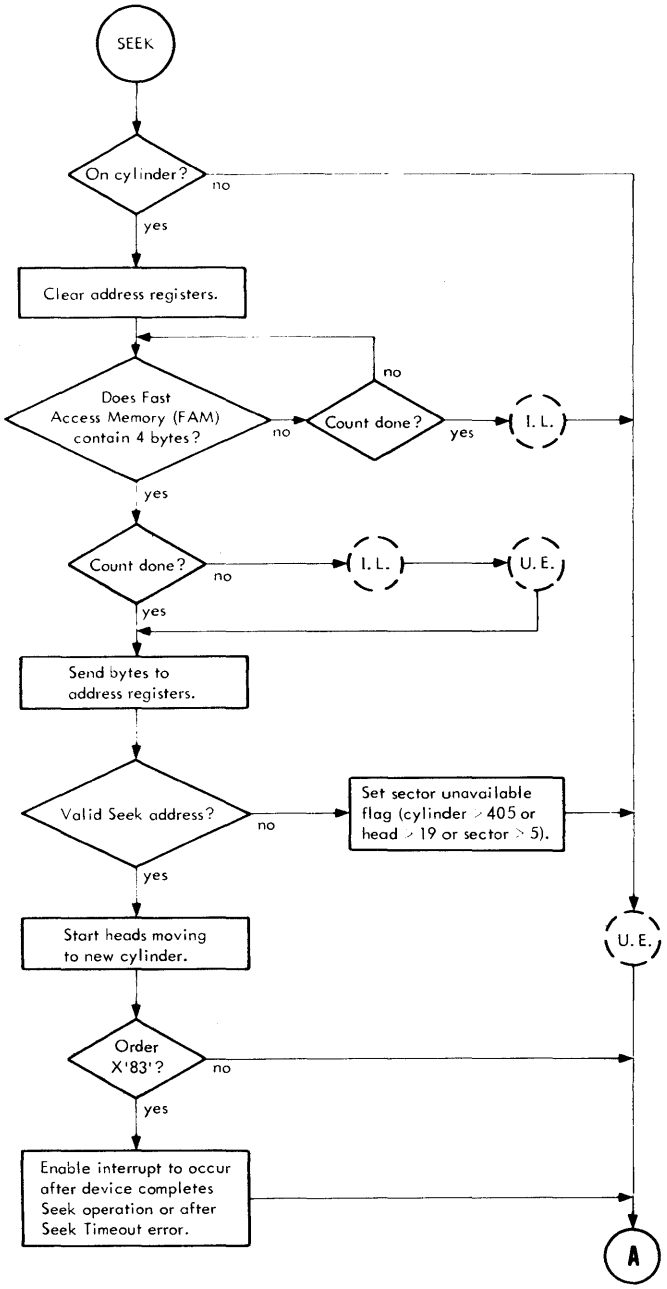


Figure 7. Seek Order

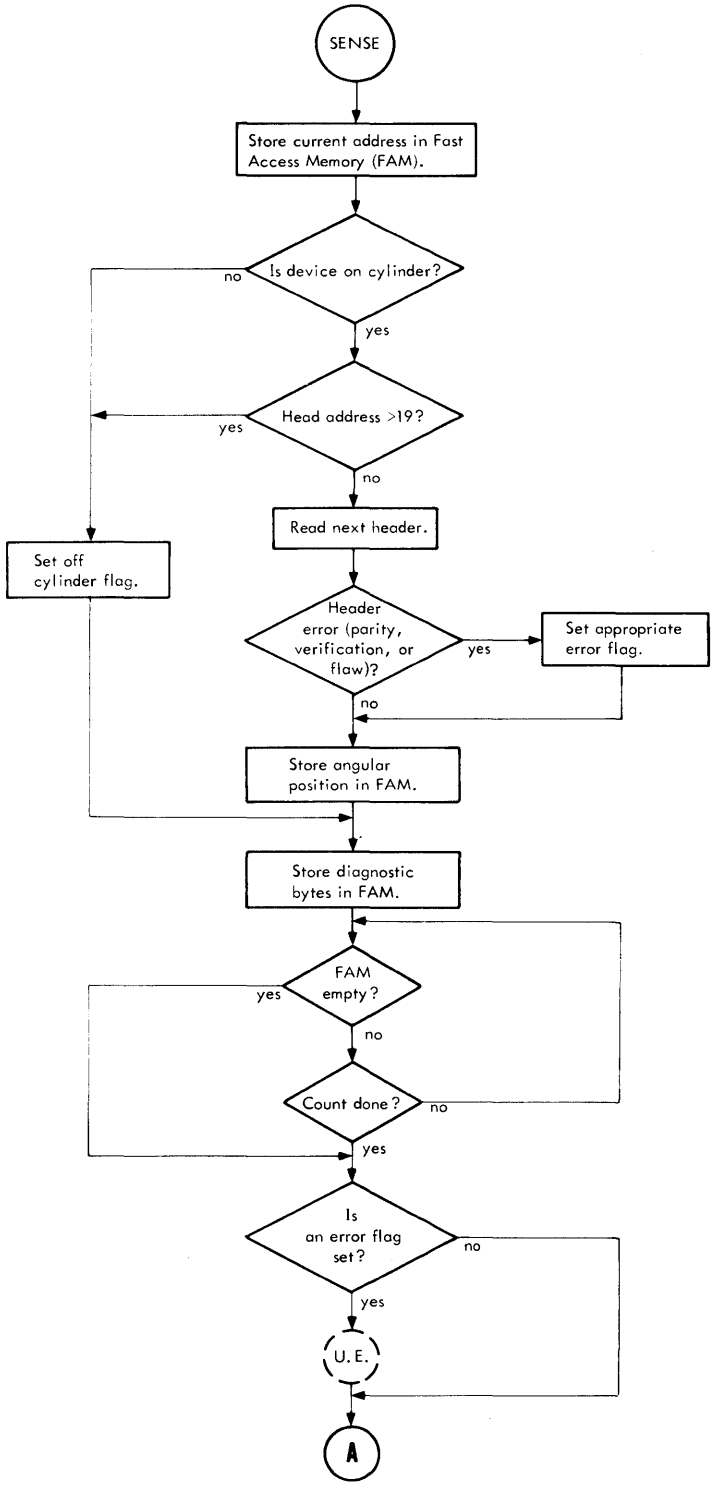


Figure 8. Sense Order

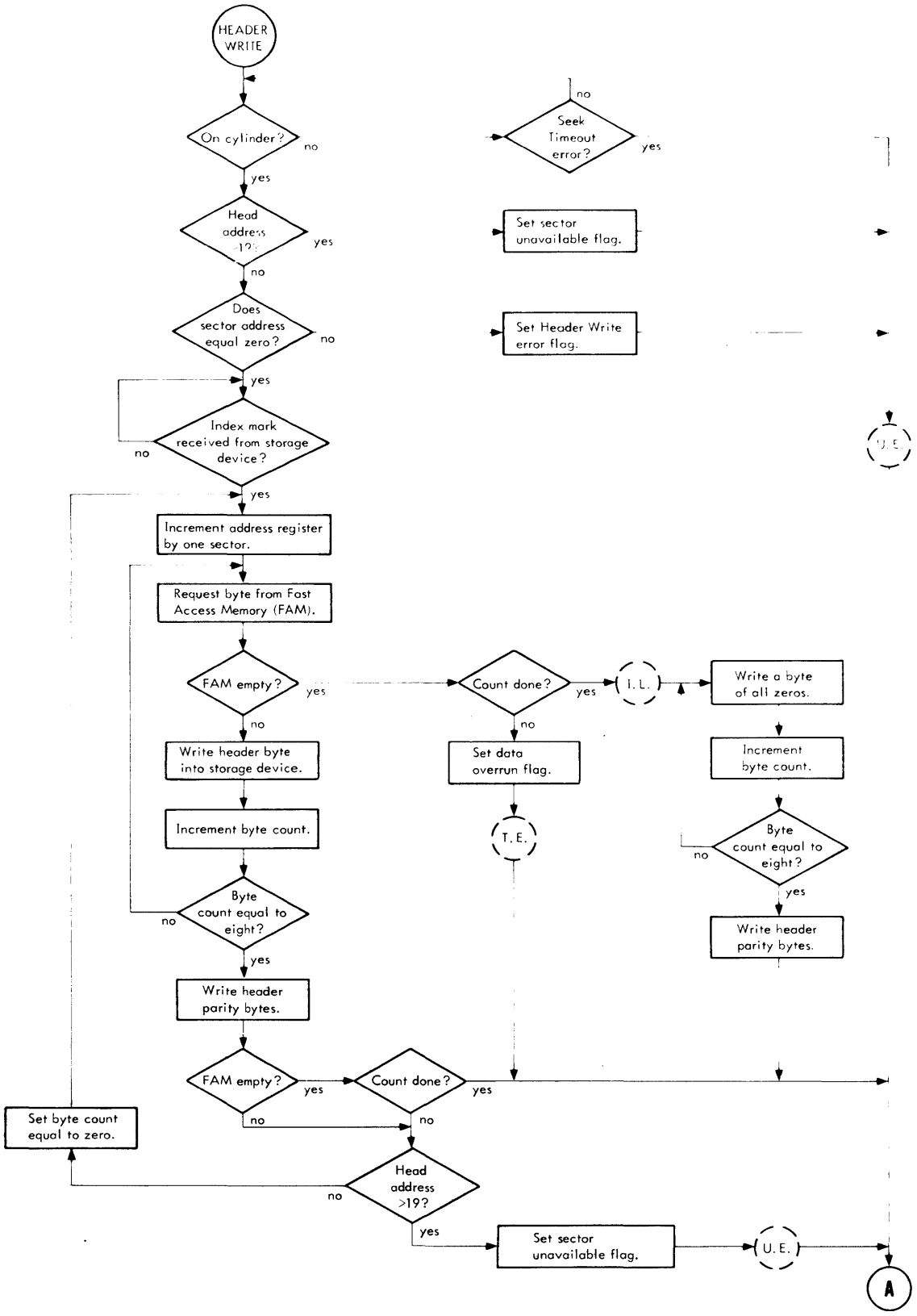


Figure 9. Header Write Order

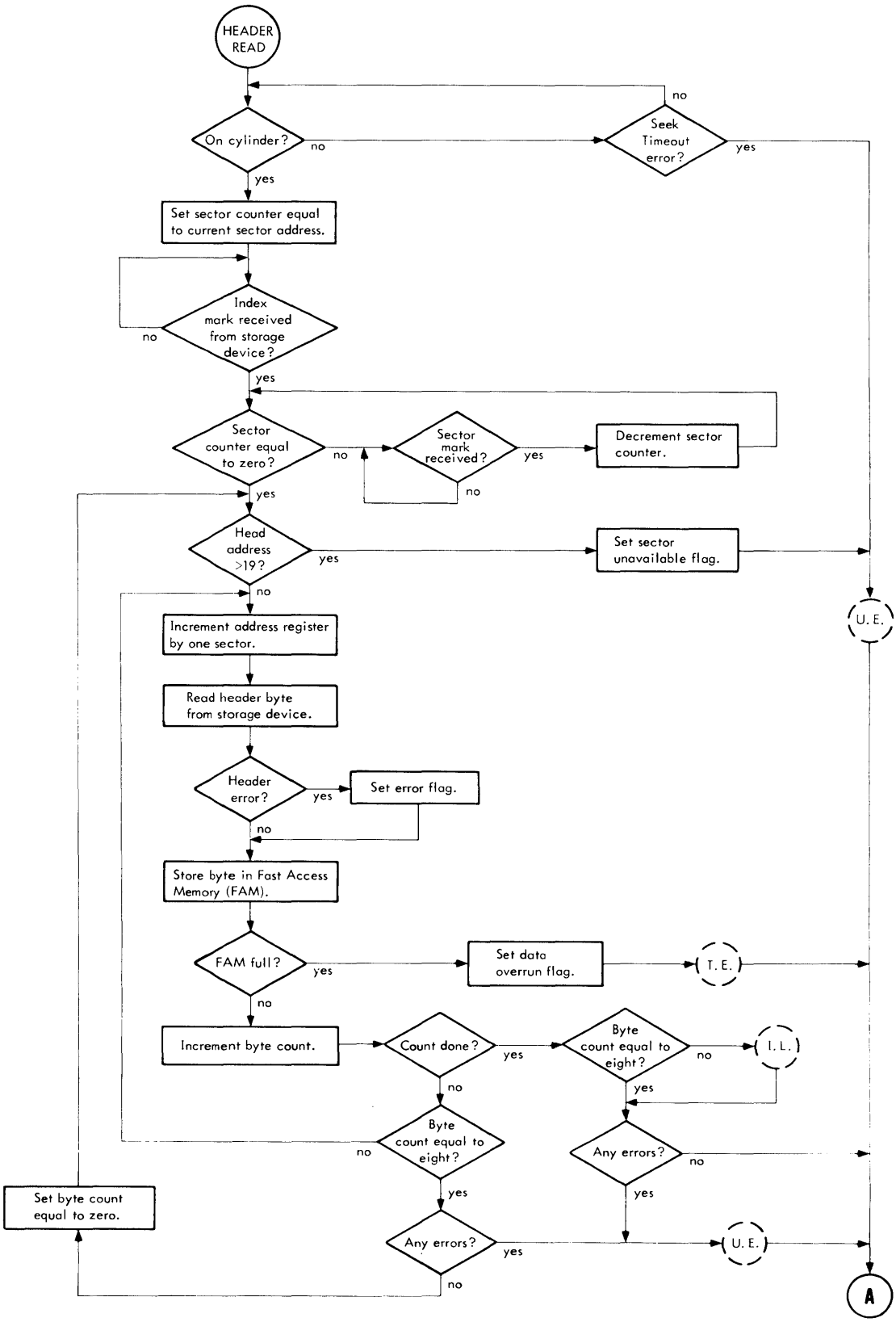


Figure 10. Header Read Order

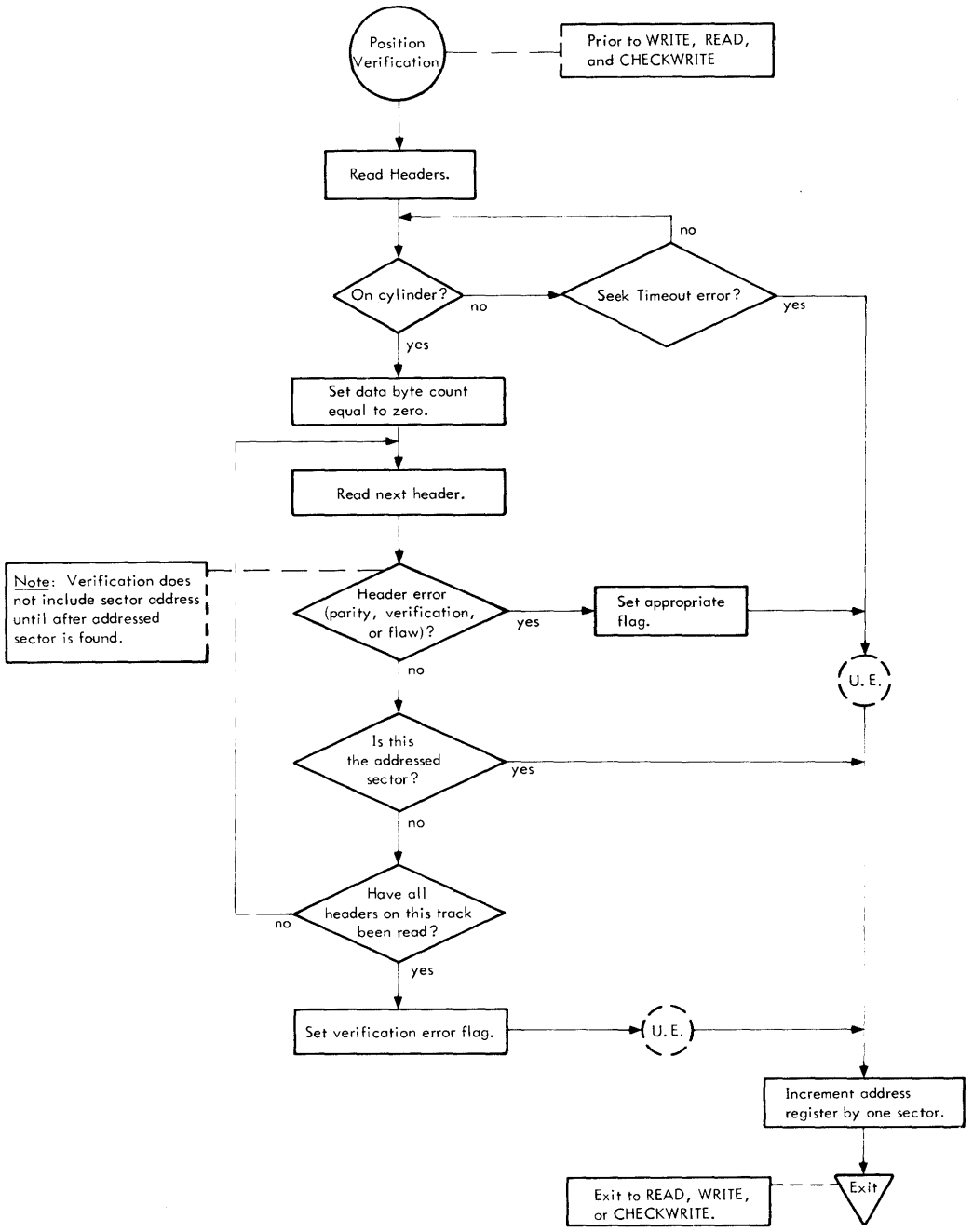


Figure 11. Position Verification

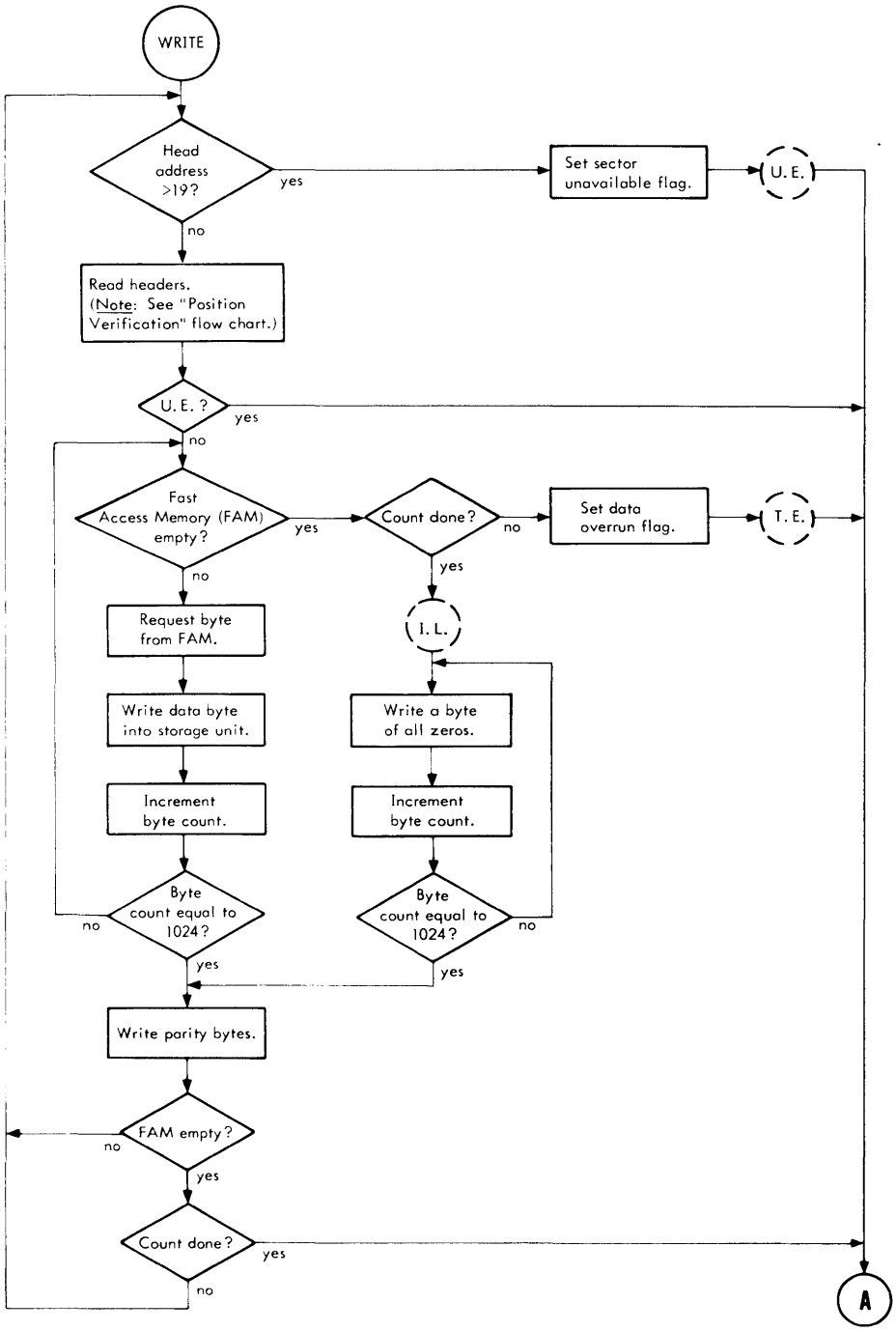


Figure 12. Write Order

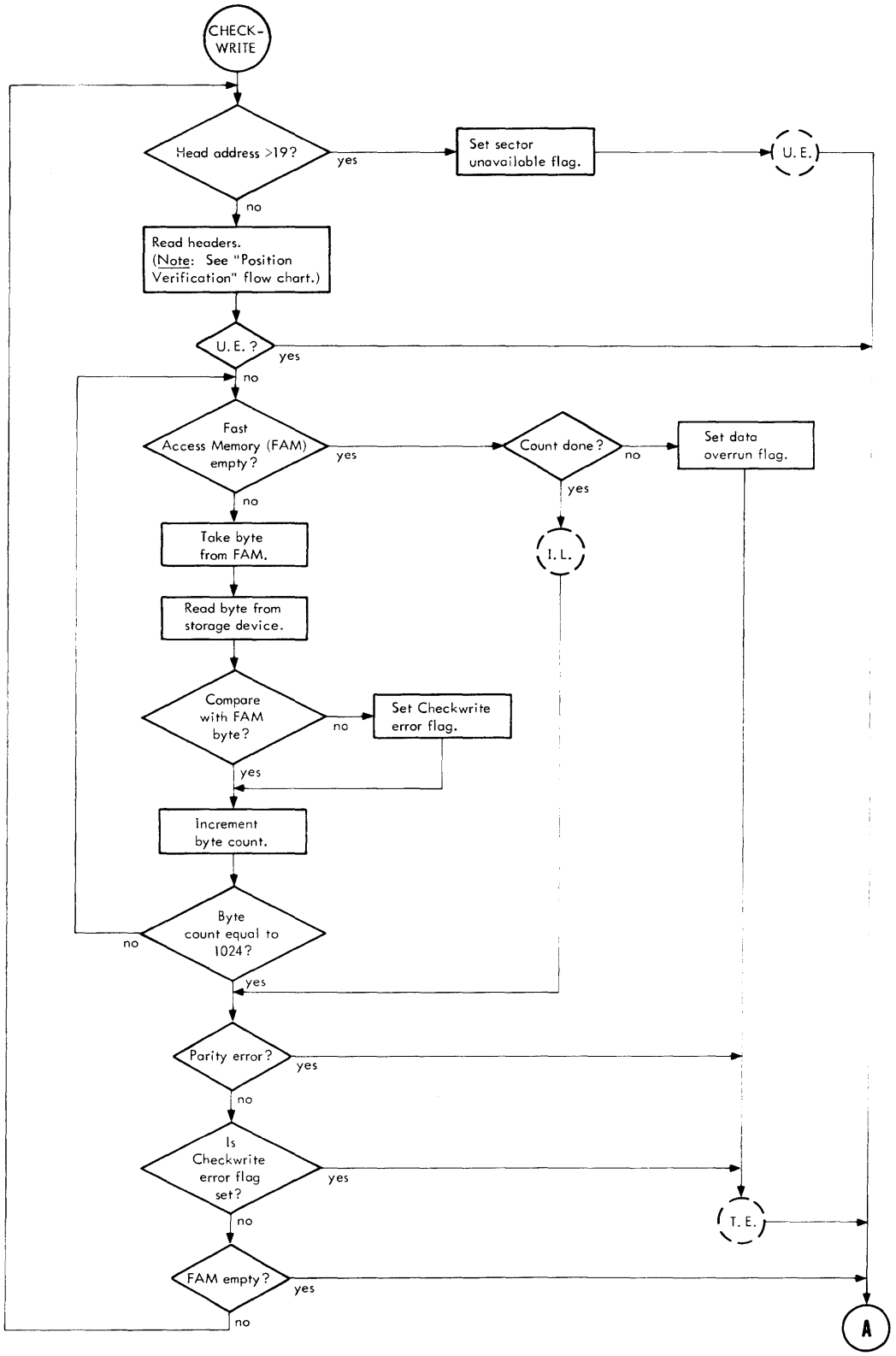


Figure 13. Check-Write Order

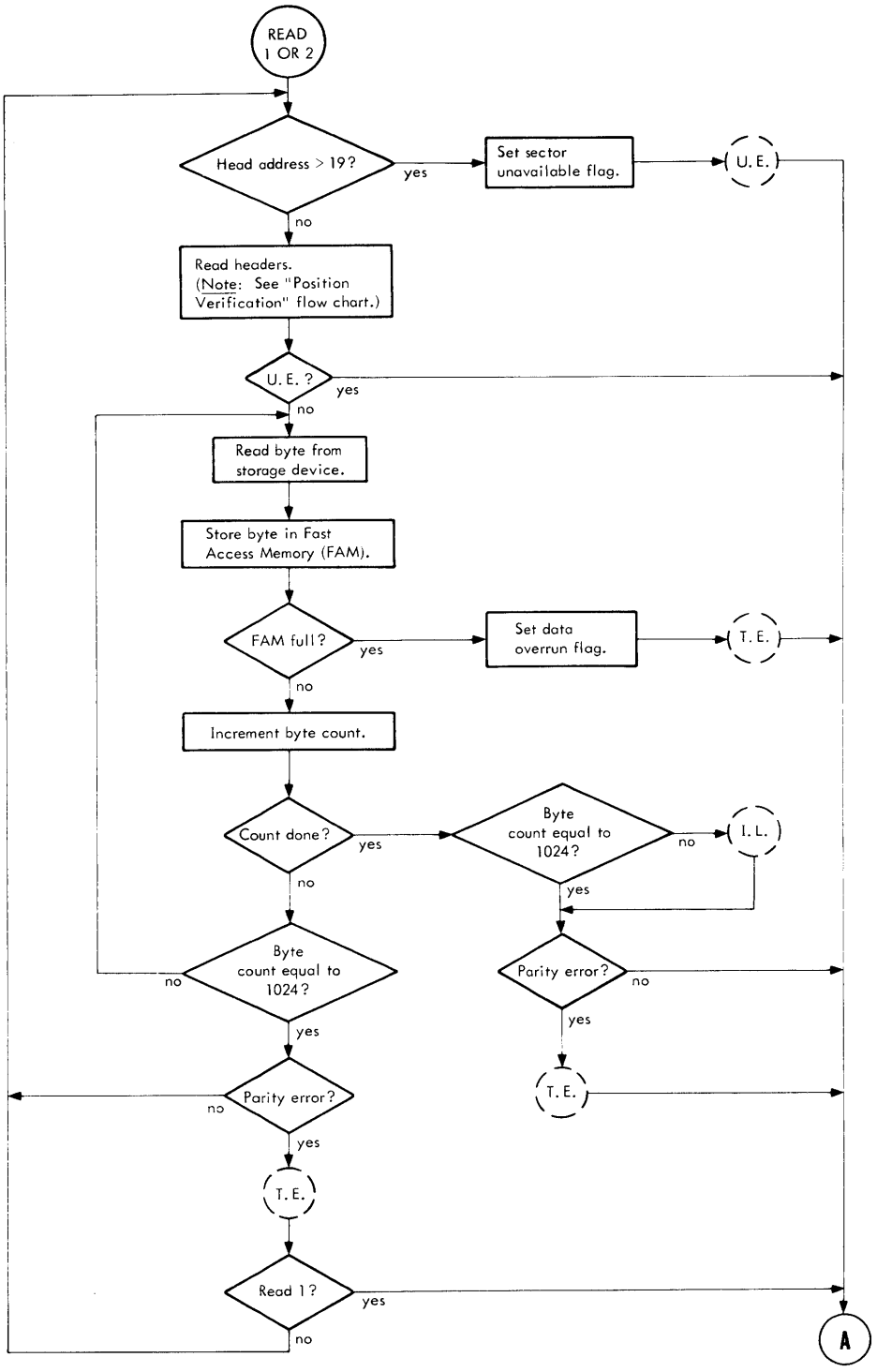


Figure 14. Read Orders

I/O FAULTS AND RECOVERY PROCEDURES

The faults and recovery procedures apply to removable disk storage systems operating with only Xerox 32-bit computers.

This section outlines how the status response and condition code bits associated with specific I/O instructions may be evaluated to determine whether a disk I/O operation has been completed successfully or terminated due to a fault condition. Detailed information is also provided for evaluating failing I/O operations in a prescribed sequence and taking appropriate corrective action.

It is assumed that I/O operations are performed in an I/O interrupt environment and that the status response and condition code bits of an AIO and TDV instruction after an I/O interrupt provide sufficient information to determine if the I/O operation was successful. For failing I/O operations, additional information should be obtained from the status response and condition code bits of a TIO instruction. Note that before the status of any I/O instruction may be used for testing purposes, the condition codes returned with the I/O instruction must be tested to verify that the I/O instruction has been successfully executed and that the status information is available in the register.

Additional assumptions are that (1) a fault can be attributed to a specific disk order, i.e., Seek, Sense, etc., and (2) for multisector operations, the failing sector can be determined. In the case of command lists, a sequence of operations are performed before the I/O interrupt occurs and status is available. When an error occurs, the command list sequence is terminated and the failing order (operation) can be determined from the TIO current command address. The programming system then has its choice of repeating the entire command list or rebuilding the command list (bypassing all nonfailing surface operations) to repeat the operation that failed.

I/O INTERRUPT ENVIRONMENT

The I/O interrupt environment is established by setting flags within the Input/Output Command Doubleword (IOCD) of the command lists and by using specified timeout delays within software timeout routines.

SOFTWARE TIMEOUT VALUES

Recommended software timeout delay for disk operations is a minimum of one second.

IOCD FLAGS

IOCD flag settings depend on the type of computer system.

16-BIT COMPUTER IOCD FLAG

The Interrupt flag (I) is set to a 1 in every IOCD. This will cause an I/O interrupt to be requested whenever a "channel end" or "unusual end" condition occurs. If data chaining is specified, the Interrupt flag should be set to a 1 only in the last IOCD of the command list.

32-BIT COMPUTER IOCD FLAGS

When the disk is operating with a Xerox 32-bit computer, the following flags must be set:

ICE — Interrupt at Channel End. This flag is set to a 1 only in the last IOCD of a command list.

IUE — Interrupt at Unusual End. This flag is set to a 1 in all IOCDs.

HTE — Halt on Transmission Error. This flag is set to a 1 in all IOCDs.

SIL — Suppress Incorrect Length. This flag is set to a 1 whenever an incorrect length indication is to be inhibited from causing an IOP Halt and a subsequent "unusual end" I/O interrupt. An incorrect length indication is generated whenever any of the following conditions occur:

1. During a Read, Write, or Check-Write operation when the number of data bytes is not an integral multiple of 1024 bytes.
2. During a Seek operation when a byte count other than four is specified.
3. During a Header Write or Header Read operation when a byte count other than a multiple of eight bytes is specified.

ADDITIONAL PROGRAMMING CONSIDERATIONS

The following information must also be considered when programming a removable disk storage system.

SURFACE FLAWING

When it has been determined that a sector's data field is unusable due to a defective surface area, all six sectors of the associated cylinder and head should be written with headers containing flaw marks. The use of the alternate cylinder and head assignment fields of these headers is optional.

SURFACE ORDERS

Automatic surface incrementing of sector and head addresses occurs after a surface operation (Read, Write, etc.);

however, automatic cylinder incrementing does not occur. A seek operation must be issued to cross cylinder boundaries to prevent an "unusual end" condition.

When the controller detects a flaw mark during a surface operation, automatic surface address incrementing is inhibited to allow for an immediate Header Read operation to obtain the alternate address.

TIO AND TDV INSTRUCTIONS

Frequent TIO and TDV instructions during surface operations may cause data overrun conditions.

SEEK AND RESTORE ORDERS

The Seek operation may be performed only when the disk is on cylinder. A modifier bit in the Seek order causes an on-cylinder interrupt to occur when the on-cylinder condition has been reached. The interrupt should be used to maximize controller availability.

The on-cylinder interrupt has a window that is one sector ahead of the addressed sector. Thus, it is possible for the condition code bits, CC1 and CC2, of an AIO instruction to be set to a 11 binary configuration (indicating that no interrupt was recognized). This "no interrupt recognized" condition should not be considered as a spurious interrupt.

When the seek modifier is used during Seek operations that are command chained to surface, header, or Sense operations, the on-cylinder interrupt does not occur.

When the Restore operation is performed, the on-cylinder indication must be present before a following Seek operation can be performed. In order to command chain a Restore and Seek order, a Header Read order should be used between the Restore and the Seek orders.

I/O OPERATION EVALUATION

By evaluating the status response and condition code bits obtained by executing AIO and TDV instructions after an I/O interrupt, a decision may be made as to whether the I/O operation was completed successfully or terminated due to a fault condition.

For 16-Bit Computer Systems Only. A disk I/O operation may be considered successful if Overflow, Carry, and all the status response bits are as follows:

1. After an AIO instruction, the Overflow and Carry indicators are both 0.
2. After a TDV instruction, the Overflow and Carry indicators are both 0.
3. The Error flag in the odd I/O channel register (assigned to disk) is 0.
4. Bits 0, 1, and 4 (Operational Status Byte) in the even I/O channel register (assigned to disk) are all 0.

For 32-Bit Computer Systems Only. A disk I/O operation may be considered successful if all the status response and condition code bits are as follows:

1. After an AIO instruction, condition code bits 1 and 2 are both 0.
2. After an AIO instruction, Operational Status Byte bits 8, 9, and 12 are all 0. Note that bit 8 may be a 1 if the SIL (Suppress Incorrect Length) flag is set.
3. After a TDV instruction, condition code bits 1 and 2 are both 0.
4. After a TDV instruction, Operational Status Byte bits 8-15 are all 0.

In either system, if one or more of the specified bits are not 0, a TIO instruction must be issued to obtain the state of the device. Also, a Sense order must be executed to obtain 10 additional bytes of status and fault information.

It is recommended that an error log be maintained in which appropriate status and fault information (i.e., bytes 8 and 9 of Sense order, etc.) be recorded that will aid in hardware maintenance and repair.

I/O FAULT EVALUATION AND RECOVERY PROCEDURES

Status information that is obtained by executing an AIO, TDV, and TIO instruction after an I/O interrupt is evaluated in a prescribed manner as listed in Table 9. The first column provides a summary description of the fault when the specified bits are not 0 (indicating generally that a fault condition has occurred). Column 2 shows a specific bit configuration only if two or more bits are used to differentiate the fault indication and detailed description of the fault condition. Thus, the first two columns of Table 9 permit a fault condition to be defined as a function of the status response and condition code bits.

The Recommended Corrective Action (column 9) for a specific fault depends on the failing order (columns 3-8). Generally, for a failing Seek order, a reference is made to Recovery Procedure 3, Table 10; for failing Restore, Release, and Sense orders, a reference is made to Recovery Procedure 2; and for failing Read, Write, Check-Write, or Header Read/Write orders, a reference is made to Recovery Procedure 4.

If the device is not operational, program recovery is not possible but operator intervention may be required (see Chapter 4, "Operator Panel Controls and Indicators"). If the FAULT light on the operator control panel is on, press the FAULT switch to attempt recovery. If the light goes off, repeat the previous operation. If the light remains on, call the customer service engineer.

Table 9. Removable Disk Storage System Testing Sequence

Sequence and Summary Description	Condition Code, Status Response Bits and Detailed Description	Seek	Restore or Release	Read 1, 2, Write, or Checkwrite	Header Read	Header Write	Sense	Recommended Corrective Action
Step 1. Performed only if TIO 5,6 ≠ 00, indicating that controller is not ready.	TIO 5,6 01 or 10.	X						Use Recovery Procedure 3.
	Not Defined. The controller is not ready immediately after an I/O interrupt.		X				X	Use Recovery Procedure 2.
				X	X	X		Use Recovery Procedure 4.
	TIO 5,6 11.	X						Use Recovery Procedure 3.
	Controller Busy. The controller has failed to return to the ready state immediately after an I/O interrupt due to a hardware malfunction or the command list is coded with multiple interrupts.		X				X	Use Recovery Procedure 2.
				X	X	X		Use Recovery Procedure 4.
Step 2. Performed only if TIO 1,2 ≠ 00, indicating that device is not ready.	TIO 1,2 01. Not Operational. The device has detected a hardware power failure or has lost the head position integrity.	X	X	X	X	X	X	Use Recovery Procedure 1.
	TIO 1,2 · 10. Not Available. The device is not available for usage through the addressed controller for the dual access hardware configuration.	X	X	X	X	X	X	Use Recovery Procedure 1.
	TIO 1,2 · 11.	X						Use Recovery Procedure 3.
	Device Busy. The device did not return to the ready state. Normally, the device is not busy when the controller is not busy. Even if the heads are moving, the device is ready when the controller is ready.		X				X	Use Recovery Procedure 2.
				X	X	X		Use Recovery Procedure 4.
Step 3. Performed only if TDV CC1,CC2 = 01.	TDV CC1,CC2 = 01. Test Mode. The controller has been placed in the test mode either due to a hardware failure or a programming error.	X	X	X	X	X	X	Use Recovery Procedure 1.
Step 4A. Applicable for 16-bit computer systems only. Performed only if the Error flag ≠ 0, indicating an IOP parity error.	I/O Parity Error. The Error flag in the odd channel register indicates that a parity error has been detected on bytes received during an input operation, or a memory parity error was detected on an output operation. The following errors generate an IOP Halt to the device controller: a. A memory parity error detected while fetching an order code from memory on order-out. b. A memory parity error detected while fetching a new IOCD from memory during a data chaining operation.	X						Use Recovery Procedure 3.
			X				X	Use Recovery Procedure 2.
				X	X	X		Use Recovery Procedure 4.
Step 4B. Applicable for 32-bit computer systems only. Performed only if TDV 10, 11, 12, and 13 ≠ 0000, indicating an IOP operational error.	TDV 11 = 1.	X						Use Recovery Procedure 3.
	Memory Address Error. This error may be due to a hardware malfunction or to a programming error.		X				X	Use Recovery Procedure 2.
				X	X	X		Use Recovery Procedure 4.
	TDV 12 = 1.	X						Use Recovery Procedure 3.
	IOP Memory Error. This error is due to a hardware malfunction.		X				X	Use Recovery Procedure 2.
				X	X	X		Use Recovery Procedure 4.
	TDV 13 = 1.	X						Use Recovery Procedure 3.
	IOP Control Error. This error is due to a hardware malfunction or to a programming error.		X				X	Use Recovery Procedure 2.
				X	X	X		Use Recovery Procedure 4.
	TDV 10 = 1.	X						Use Recovery Procedure 3.
	Transmission Memory Error. This error is due to a hardware malfunction.		X				X	Use Recovery Procedure 2.
				X	X	X		Use Recovery Procedure 4.
Step 5. Performed only if TDV 2 ≠ 0.	Sector Unavailable. The surface address of the Seek operation is outside the range of available sectors, or the current surface operation has been aborted because the previous surface operation has incremented the surface address across a cylinder boundary. Both conditions are programming errors.	X						Use Recovery Procedure 3.
				X	X	X		Use Recovery Procedure 4.

Table 9. Removable Disk Storage System Testing Sequence (cont.)

Sequence and Summary Description	Condition Code, Status Response Bits and Detailed Description	Seek	Restore or Release	Read 1, 2, Write, or Checkwrite	Header Read	Header Write	Sense	Recommended Corrective Action
Step 6. Performed only if TDV 6 ≠ 0, indicating a Seek Timeout.	Seek Timeout. A hardware seek timeout has been detected because the on-cylinder device state has failed to occur after head motion has started. This is a hardware malfunction.	X	X	X	X	X		Use Recovery Procedure 3.
							X	Use Recovery Procedure 2.
Step 7. Performed only if TDV 7 ≠ 0, indicating a header parity error.	Header Parity Error. A parity error in the header has been detected, indicating that the header portion of the sector cannot be read correctly.						X	Use Recovery Procedure 2.
				X	X			Use Recovery Procedure 3.
Step 8. Performed only if TDV 4 ≠ 0. Sense Byte 8, bits 2, 3, and 4 indicate, respectively, the error as sector head, or cylinder (see Table 3).	Header Verification Error. This error may occur during a surface, Header Read, or Sense operation. In all cases, the current surface address is not incremented. Surface Operations. For the first surface operation after a Seek operation, the header verification error indicates a sector header corresponding to the addressed surface could not be found in any of the six sectors of the addressed cylinder and head. After the first surface operation, the header verification error indicates the header of the currently addressed sector's header is incorrect. In either case, the surface operation has not been performed. Header Read Operations. The header verification error during the Header Read operation indicates that the currently addressed sector's header is incorrect. Sense Operations. The header verification error during the Sense operation indicates that a header has been read whose cylinder and head field does not compare with the current cylinder and head address. This header may be any of the six headers of the cylinder and head.						X	Use Recovery Procedure 2.
				X	X			Use Recovery Procedure 3.
Step 9. Performed only if TDV 1 ≠ 0, indicating a flaw mark.	Flaw Mark. A flaw mark has been detected during a surface, Header Read or Sense operation. In all cases, the current surface address is not incremented. Surface Operations. For the first surface operation after a Seek operation, the flaw mark indicates a flaw has been detected in a sector's header of the addressed cylinder and head while the sector headers were being scanned to obtain the addressed sector. On subsequent operations, the flaw mark indicates the currently addressed sector's header contains a flaw. This condition will be present when the surface address has been incremented from an unflawed cylinder and head to a flawed cylinder and head surface. In either case, the surface operation is not performed. Header Read Operations. Flaw mark has been detected in the currently addressed sector. This indicator should not be considered an error. Sense Operations. Flaw mark during a Sense operation indicates that a header has been read containing a flaw. This header may be any of the six headers of the currently addressed cylinder and head. The flaw mark indication should not be considered an error.				X		X	Use Recovery Procedure 6.
				X				Use Recovery Procedure 5.
Step 10. Performed only if OSB ≠ 0 (16-bit computer) or TDV 9 ≠ 0 (32-bit computer), indicating a Transmission Data error. Note: OSB = Operational Status Byte	Transmission Data Error. This error may be caused by any of the following: a. Data Over-Run. When the system fails to maintain data transfer rate required during Read, Write, Checkwrite, Header Read, or Header Write operation. (TDV0 will also be set to a 1.)			X	X	X		Use Recovery Procedure 4.
	b. Data Parity Error. The sector data parity has failed to compare to the expected parity during a Read, Checkwrite, or Header Read operation. (Bit 0 of Sense Byte 8 is also set to a 1.)			X				Use Recovery Procedure 4.
	c. Checkwrite Error. When a data comparison error occurs during a Checkwrite operation. (Bit 1 of Sense Byte 8 is also set to a 1.)			X				Use Recovery Procedure 4.

Table 9. Removable Disk Storage System Testing Sequence (cont.)

Sequence and Summary Description	Condition Code, Status Response Bits and Detailed Description	Seek	Restore or Release	Read 1, 2, Write, or Checkwrite	Header Read	Header Write	Sense	Recommended Corrective Action
Step 11. Performed only if TIO4 ≠ 0, indicating an "unusual end".	Unusual End. If none of the above error indications (as described in Steps 1-10) are present, then the current operation was terminated with an "unusual end" due to any of the following errors: a. Invalid order code issued. b. The device's read clock was lost during header or data reading. c. Receiving a Seek order while the arm is in motion or an incorrect length was used with the Seek order. d. Attempting to start the Header Write operation at a sector other than zero.	X						Use Recovery Procedure 3.
			X				X	Use Recovery Procedure 2.
				X	X	X		Use Recovery Procedure 4.
Step 12. Performed only if TDV 8 ≠ 0 (32-bit computer) or if OSB 1 ≠ 0 (16-bit computer), indicating an incorrect length error.	Incorrect Length. An incorrect length has been detected. Except for the Header and Seek operations, this indication should not be considered an error (see SIL flag).				X	X		Use Recovery Procedure 4.
				X			X	Use Recovery Procedure 6.
Step 13.	Inconsistent Status Error. Whenever the device-dependent status has failed to indicate the specific failure for which the device-dependent status checking was invoked, it is considered a hardware malfunction.	X	X	X	X	X	X	Use Recovery Procedure 3.

Table 10. Recommended Recovery Procedures

Recovery Procedure Number	Description
1	Abort – Operator Intervention. All operations on the device must be aborted and operator notification given that the device is either not operational or unavailable or the controller is in the test mode. The operator must then perform action to return the device to the operational state.
2	Order Retry – Program Recovery. An HIO instruction should be issued (in the case of controller and/or device not ready) and the operation retried. This sequence should be retried ten times before considering the operation irrecoverable.
3	Order Retry After Restore – Program Recovery. An HIO instruction must be issued (in the case of controller and/or device busy) followed by a Restore operation to reestablish head positional reference. A Seek operation should then be issued followed by the operation previously in error (if not a Seek operation). This sequence should be retried ten times before considering the failure irrecoverable.
4	Order Retry After Seek – Program Recovery. An HIO instruction should be issued (in the case of controller and/or device not ready) followed by a Seek operation to reestablish the surface address and head position. The operation in error is then retried. This sequence should be retried ten times before considering the failure irrecoverable.
5	Alternate Surface Selection – Program Recovery. A Header Read operation is performed to determine the assigned alternate surface address. This recovery is appropriate only when the header area is used for alternate assignment. Whenever a flaw mark is detected, a Header Read operation must be performed to differentiate between flawed sectors (only flaw mark set) and hardware problems (other fault indicators set).
6	Program Notification – No Recovery Required. The program is notified of the following conditions for which recovery is not necessarily required: 1. A header flaw marker has been detected during a Header Read or Sense operation. 2. An incorrect length indication appears following an operation for which partial information transfer is acceptable.

IRRECOVERABLE ERRORS

If a particular fault condition persists after performing the recommended recovery procedure, the fault is irrecoverable. Specific action to be taken after an irrecoverable fault will vary with the order and with the user's application. For example, in the case of sense failures, the system may tolerate such errors, whereas read failures may require a range of action such as job abortion to shutting down the system. On the other hand, irrecoverable write operations may require the choosing of another surface area and the flawing of the failing surface.

4. OPERATIONS

CONTROLS AND INDICATORS

The location of disk storage unit operator and maintenance panel controls and indicators is shown in Figure 15.

The Operator Panel controls and indicators are described in Table 11; Logic Chassis Maintenance Panel controls and indicators are described in Table 12.

POWER TURN-ON PROCEDURE

The following procedure prepares the system to go on-line.

1. Set the Filter Box Panel circuit breaker to ON in each storage unit cabinet.

   Caution: Do not turn circuit breaker on or off when system is in operation.

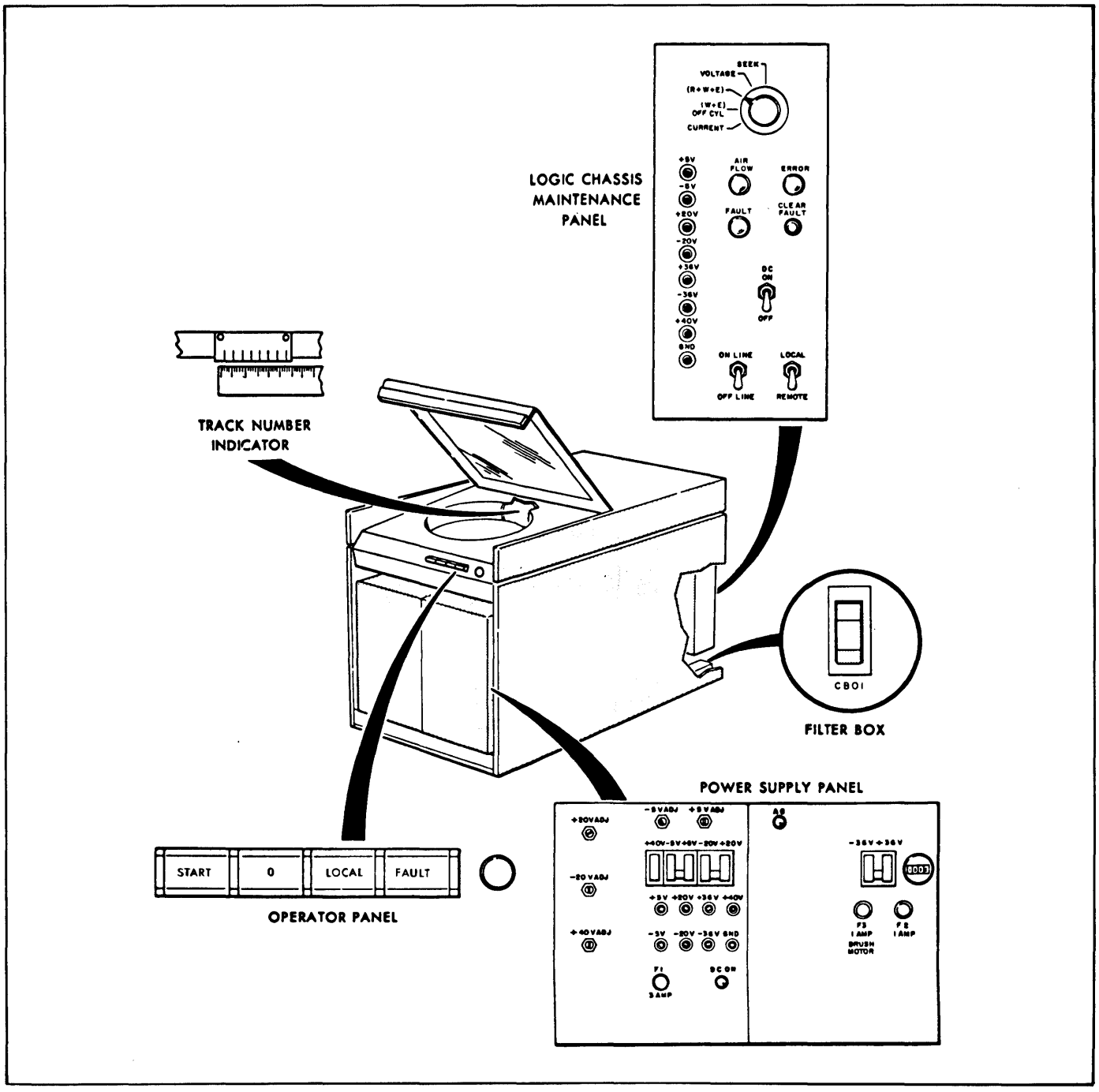


Figure 15. Location of Panels, Controls, and Indicators

Table 11. Operator Panel Controls and Indicators

Control or Indicator	Function
START switch/indicator	When pressed with indicator not lighted, starts spindle drive motor and lights indicator. The first Seek sequence begins provided that the following conditions are met: 1. Disk pack in place. 2. Front and top covers closed. 3. Circuit breakers on. 4. DC switch on. 5. Index Sensor switch closed. 6. Sequence power available from controller (if Logic Chassis Maintenance Panel LOCAL/REMOTE switch is set to REMOTE), or from power supply (if switch is set to LOCAL). Lights when switch is on, even if one or more of the above conditions is not met. This allows the operator to know which units will sequence on when control unit sequence power becomes available. When pressed with indicator lighted, initiates power-off sequence.
Unit Number	Unit Number lights when Logic Chassis Maintenance Panel ON LINE/OFF LINE switch is set to ON LINE position and read/write heads are "loaded" (positioned). Significance of Unit Number (0 through 7) is limited to indicating physical location of a spindle within the system. The number is unchangeable.
LOCAL indicator	Lights when related module is not on-line as a result of one of the following conditions: 1. LOCAL/REMOTE switch on Logic Chassis Maintenance Panel set to LOCAL. 2. DC switch on Logic Chassis Maintenance Panel set to OFF. 3. ON LINE/OFF LINE switch on Logic Chassis Maintenance Panel set to OFF LINE.
FAULT switch/indicator	Lights when one or more of the following unwanted conditions occur: 1. More than one head is selected. 2. Read and Write Selects exist at the same time. 3. Read and Erase Selects exist at the same time. 4. Erase is selected with no write driver. 5. Erase is selected with both write drivers. 6. Either one or both write drivers are on with no erase. 7. Write or erase is selected without an On Cylinder signal. 8. Low voltage (±5v, ±20v, ±36v, +40v) condition sensed. Pressing switch clears fault condition and turns indicator off.
Logic Number plug/indicator	Lights when related device is selected by controller. Removable plug that indicates device address (logic number); device address can be changed by interchanging logic number plugs.

Table 12. Logic Chassis Maintenance Panel

Control or Indicator	Function
ON LINE/OFF LINE switch	Setting switch to ON LINE links unit with controller. Setting switch to OFF LINE causes following: 1. Prevents control unit from initiating Seek or Read/Write operations. 2. Turns off Operator Panel Unit Number and Logic Number indicators. 3. Turns on Operator Panel LOCAL indicator. 4. Inhibits Unit Ready and Unit Selected signals to the controller.
Error Select switch and ERROR indicator	Five-position rotary switch tests each of five Error Register bits. When bit is set, ERROR indicator lights. (Indicator may light briefly between each position of the switch).
FAULT indicator	Indicator lights when any of the flip-flop bits (FF's) in the Error Register (except Seek Error FF) is set, independent of position of Error Select switch.
CLEAR FAULT switch	Clears Fault FF and all bits of the Error register when pressed.
LOCAL/REMOTE switch	Allows the power-on sequence to be controlled by either 1. A signal from the controller when set to REMOTE or 2. +20Y-vdc from the power supply when set to LOCAL. LOCAL indicator on Operator Panel lights when switch is set to LOCAL.
AIR FLOW indicator	Lights to indicate adequate air flow within blower assembly.
DC ON/OFF switch	Removes dc power from all logic cards, except the transmitters and receivers, when placed in the OFF position.

2. Observe each operator panel. Only the units to be operating should have their START indicators lighted. Press any START switches on or off as required.

3. Apply sequence power at the controller by setting the S1 switch to REM.

   When a unit completes its power-up sequence, the related Unit Number indicator will light.

   Note: The controller is in a separate cabinet. The S1 switch is on the Controller Power Distribution Panel, which is at the top of the cabinet. The MARGIN dial is on the Controller Power Supply Panel, which is at the bottom of the cabinet.

4. When the required Unit Number indicators are lighted, the system is on-line and the unit is ready to receive an order.

The following procedure prepares a unit to go on-line, thereby joining other on-line units in the system.

1. Install a disk pack (refer to "Disk Pack Installation" paragraph).

2. Open the rear panel of the cabinet and position the Logic Chassis Maintenance Panel switches as follows:

   ON LINE/OFF LINE switch to ON LINE

   DC switch to ON

   LOCAL/REMOTE switch to REMOTE

3. Set the Filter Box Panel circuit breaker to ON. Close the rear panel.

4. Open the front panel of the cabinet and position the power supply switches as follows:

   +40v circuit breaker to ON

   ±5v circuit breaker to ON

   ±20v circuit breaker to ON

   ±36v circuit breaker to ON

   The blower motor will begin to operate. The following indicators will light:

   AC ON (Power Supply Panel)

   DC ON (Power Supply Panel)

   AIR FLOW (Logic Chassis Maintenance Panel)

5. Close cabinet front panel.

6. Press the Operator Panel START switch. The indicator will light.

7. When the controller sequence power becomes available, the Operator Panel Unit Number indicator lights (heads have been positioned or "loaded") and the first Seek operation begins.

8. The first Seek operation is completed when the heads are returned to track 0. The unit is now ready to receive a Read, Write, or Seek order from the controller.

POWER TURN-OFF PROCEDURE

The following procedure removes power from the entire system.

1. Turn off sequence power at the controller by setting S1 switch to OFF.

   Note: The controller is in a separate cabinet. The S1 switch is on the Controller Power Distribution Panel, which is at the top of the cabinet.

2. Set Filter Box Panel circuit to OFF in each storage unit cabinet.

DISK PACK INSTALLATION

Note: The disk pack to be installed must be clean (see "Disk Pack Cleaning"). Before loading it, be sure the disk drive is completely stopped.

1. Raise the unit's front cover.

   Note: A spindle lock mechanism is actuated when the front cover is opened. The mechanism holds the spindle stationary while loading a disk pack.

2. Lift the disk pack by the plastic canister handle.

3. Disengage the bottom dust cover from the disk pack using the knob in the center of the cover. Set the cover aside.

4. Carefully place the disk pack onto the spindle, avoiding rough contact.

   Note: During maintenance procedures the read/write heads are sometimes manually positioned. Make certain that the heads are fully retracted.

5. Slowly twist the canister handle clockwise until resistance is met. The disk pack is now locked in place.

   Caution: Too rapid rotation of the pack will cause an impact force at lock-in that may damage the lockshaft.

6. Carefully lift the canister clear of the disk pack and reassemble with the bottom cover, so that the interior will remain dust free.

DISK PACK REMOVAL

1. If Operator Panel START indicator is lighted, press switch to turn off.

2. Check that disk pack rotation has stopped completely.

   Note: A spindle lock mechanism is actuated when the front cover is opened. A loud ratcheting noise occurs when the front cover of a spinning disk pack is opened. While this action is not recommended, it will not damage the disk.

3. Raise the front cover on the unit.

   Caution: During maintenance procedures the read/write heads are sometimes manually positioned. Make certain that the heads are fully retracted.

4. Place the canister carefully over the mounted disk pack so that the post protruding from the center of the disk pack is received into the canister handle.

5. Twist the canister handle counterclockwise until it clicks two or three times, securing the cover to the disk pack, and freeing it from the spindle.

6. Carefully lift the canister and the disk pack clear of the spindle.

7. Close the front cover of the unit.

8. Place the bottom dust cover in position on the disk pack and tighten it.

DISK DRIVE/PACK INSPECTION FOR DAMAGE

When a disk pack is received, it should be carefully examined for possible damage. A distorted disk pack can cause head to disk interference that can result in a "crash". Since a number of packs are used on more than one drive unit, a single defective pack or drive unit can spread a malfunction to all the units. If packs are subsequently installed on a damaged disk drive unit, they will also be damaged. When these defective packs are used with other drive units, these units will also be damaged. A damaged head or dirty drive unit will initiate this same damage cycle.

The following procedure is recommended to check disk packs prior to installation on a disk drive unit:

1. Visually inspect pack for damage such as covers not locking onto pack, bent or warped disks, or gross misalignment within the covers.

2. Remove bottom cover.

3. Turn pack upside down and level it so that the pack can be spun inside the top cover freely. Apply spinning force to the center, not the disk.

4. Visually inspect pack for excessive up and down motion (runout) of any of the recording disks, especially the gold-colored sector disk. If runout exceeds 1/8 inch when tested in this manner, reject the pack. Also, a 1/16 inch or more separation between the sector disk and the adjacent recording disk is cause for rejection.

5. Load the seemingly good pack on a disk drive unit and remove the top cover.

6. Turn drive unit power on and allow disk pack cleaning brushes to start cleaning cycle. As the brushes move in and out of the pack, listen for any noise that may be caused by the brush arms making contact with a disk surface, or the sector disk making contact with the drive sector block. If such noise is heard, turn off unit power. Reject defective pack. Bring to immediate attention of Customer Service Engineer.

   Caution: Do not allow read/write heads to load, since extensive damage to the heads could result from a defective pack.

7. If no noise was detected, replace top cover and remove pack from drive unit spindle. Inspect sector disk on pack and sector block on drive unit for possible damage from the two making contact. (Sector disk to sector block contact is most often caused by a bent sector disk or improper alignment of the machine sector sensing block.)

8. As a final test, while a program is being run, listen closely (near the drive unit top cover) for any head to disk contact that causes a rasping sound. If such noise is heard, turn drive unit power off and remove pack. The pack has sustained mechanical damage. The drive unit has also been damaged and should not be run with any other packs, since it may damage them with the heads damaged by the first failure.

DISK PACK CLEANING

Disk pack surfaces should be cleaned semiannually or after 3,000 hours or when contaminated pack is suspected. The top and bottom covers of the disk pack should be kept clean and free of fingerprints, label adhesives, etc. To clean covers, use a lint-free cloth moistened with pure 91 percent isopropyl alcohol (from a chemical supply house – not a drug store; drug store alcohols often contain harmful oils, medicinals, and perfumes).

Disk packs should be cleaned only by Customer Service Engineers.

DISK PACK FILTER REPLACEMENT

Disk packs are fitted with an air filter made of mesh nylon in a plastic frame. The filter is located in the base of the hub and is secured in place by an "O" ring. In normal use, a filter should be replaced monthly to maintain a clean disk pack. More frequent replacement may be necessary, since it depends upon the number of operating hours and environment.

FILTER REMOVAL

To remove a filter from a disk pack for inspection or replacement, the procedure is:

1. Remove disk pack bottom cover.

2. With thumb and index finger, squeeze the "O" ring, which secures the filter in place, with a sliding motion so that the ring stretches in one direction. A gap will be formed between the ring and the hub, permitting the ring to be removed from the hub.

   Caution: Do not use a sharp instrument to remove the "O" ring, since sharp objects may puncture the filter or generate contamination by abrading the hub.

3. Remove filter from hub.

FILTER INSTALLATION

To replace or install a new filter, the procedure is:

1. Slide filter over bottom of hub and seat it below the "O" ring groove.

2. Hook a portion of the ring into the groove. By slightly stretching the ring around the hub, seat the ring into the ring groove.

   Caution: Do not roll the "O" ring into its groove as this may result in a twisted ring that can come loose during machine operation.

3. Replace bottom cover on disk pack.

DISK PACK USAGE LOG

A log must be attached to each disk drive unit for identifying all packs that have been used on that unit. Since one defective pack or drive unit can spread damage to other units in an installation, it is important to check which packs have been used when a malfunctioning unit or defective pack has been discovered.

The log entry should consist of pack identification and date and time of insertion.

RECOMMENDED DO'S AND DON'TS FOR DISK PACK

- Handle a disk pack only with the top and bottom covers on, except when loading or unloading it.
- After installing a disk pack on a drive, reassemble the top with the bottom cover, so that it remains dust free.
- Replace cracked, distorted, or damaged covers.
- Clean disk pack covers with a lint-free soft cloth to prevent dust build up.
- Use only a lint-free cloth moistened with pure isopropyl alcohol (from a chemical supply house) to remove label adhesive, fingerprints, etc.

  Caution: Do not use medicinal isopropyl alcohol from a drug store, since it often contains harmful additives.

- Do not touch the surfaces of the disks with finger, pencil, clothing, or other objects.
- Use only the center trim shield of the disk pack for labeling.
- Do not put notes, markers, identification cards, etc., inside the pack.
- Do not stop a disk drive by pressing on the top disk. Wait until it stops by itself.
- Only store disk packs flat, resting on the bottom cover, unless they are in their shipping containers.
- Do not store disk packs in direct sunlight and do not expose them to magnetic fields from high current electric cables, transformers, or similar equipment. An ideal environment is the computer room.
- Keep storage area free of dust and contaminants.
- Do not drop the disk pack. This could change the disk contour and cause misalignment of the disk surfaces with the heads. If it is dropped, inspect for damage (see "Disk Drive/Pack Inspection for Damage").
- Before using a disk pack, condition it to machine room temperature for a minimum of two hours.

Xerox Corporation
701 South Aviation Boulevard
El Segundo, California 90245

XEROX

Reader Comment Form

We would appreciate your comments and suggestions for improving this publication.

Publication No.	Rev. Letter	Title	Current Date

How did you use this publication?

☐ Learning ☐ Installing ☐ Sales
☐ Reference ☐ Maintaining ☐ Operating

Is the material presented effectively?

☐ Fully Covered ☐ Well Illustrated ☐ Well Organized ☐ Clear

What is your overall rating of this publication?

☐ Very Good ☐ Fair ☐ Very Poor
☐ Good ☐ Poor

What is your occupation?

Your other comments may be entered here. Please be specific and give page, column, and line number references where applicable. To report errors, Please use the Xerox Software Improvement or Difficulty Report (1188) instead of this form.

Your Name & Return Address

2190(12/72)

Thank You For Your Interest. (fold & fasten as shown on back, no postage needed if mailed in U.S.A.)

Staple

Staple

Fold

First Class
Permit No. 229
El Segundo,
California

BUSINESS REPLY MAIL

No postage stamp necessary if mailed in the United States

Postage will be paid by

Xerox Corporation
701 South Aviation Boulevard
El Segundo, California 90245

Attn: Programming Publications

Fold

XEROX

701 South Aviation Boulevard
El Segundo, California 90245
213 679-4511